

Single P-channel MOSFET

ELM14409AA-N

<http://www.elm-tech.com>

■ General description

ELM14409AA-N uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate resistance.

■ Features

- $V_{ds} = -30V$
- $I_d = -15A$ ($V_{gs} = -10V$)
- $R_{ds(on)} < 7.5m\Omega$ ($V_{gs} = -10V$)
- $R_{ds(on)} < 12m\Omega$ ($V_{gs} = -4.5V$)

■ Maximum absolute ratings

$T_a = 25^\circ C$. Unless otherwise noted.

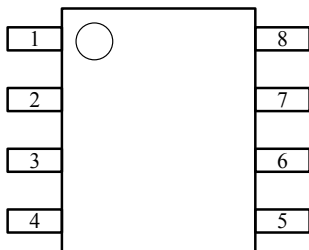
Parameter	Symbol	Limit	Unit	Note	
Drain-source voltage	V_{ds}	-30	V		
Gate-source voltage	V_{gs}	± 20	V		
Continuous drain current	I_d	$T_a = 25^\circ C$	-15.0	A	1
		$T_a = 70^\circ C$	-12.8		
Pulsed drain current	I_{dm}	-80	A	2	
Power dissipation	P_d	$T_c = 25^\circ C$	3.0	W	1
		$T_c = 70^\circ C$	2.1		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	$^\circ C$		

■ Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$R_{\theta ja}$	$t \leq 10s$	26	$^\circ C/W$	1
Maximum junction-to-ambient		Steady-state	50	$^\circ C/W$	
Maximum junction-to-lead	$R_{\theta jl}$	14	24	$^\circ C/W$	3

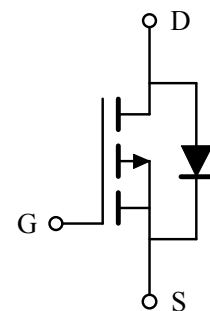
■ Pin configuration

SOP-8(TOP VIEW)



Pin No.	Pin name
1	SOURCE
2	SOURCE
3	SOURCE
4	GATE
5	DRAIN
6	DRAIN
7	DRAIN
8	DRAIN

■ Circuit



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■ Electrical characteristics

Ta=25°C. Unless otherwise noted.

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	BVdss	Id=-250μA, Vgs=0V	-30			V
Zero gate voltage drain current	Idss	Vds=-24V			-5	μA
		Vgs=0V		Ta=55°C	-25	
Gate-body leakage current	Igss	Vds=0V, Vgs=±20V			±100	nA
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=-250μA	-1.4	-1.9	-2.7	V
On state drain current	Id(on)	Vgs=-10V, Vds=-5V	-80			A
Static drain-source on-resistance	Rds(on)	Vgs=-10V		6.2	7.5	mΩ
		Id=-15A	Ta=125°C	8.2	11.5	
		Vgs=-4.5V, Id=-10A		9.5	12.0	mΩ
Forward transconductance	Gfs	Vds=-5V, Id=-15A	35	50		S
Diode forward voltage	Vsd	Is=-1A, Vgs=0V		-0.71	-1.00	V
Max. body-diode continuous current	Is				-5	A
DYNAMIC PARAMETERS						
Input capacitance	Ciss			5270	6400	pF
Output capacitance	Coss	Vgs=0V, Vds=-15V, f=1MHz		945		pF
Reverse transfer capacitance	Crss			745		pF
Gate resistance	Rg	Vgs=0V, Vds=0V, f=1MHz		2	3	Ω
SWITCHING PARAMETERS						
Total gate charge (10V)	Qg			100.0	120.0	nC
Total gate charge (4.5V)	Qg	Vgs=-10V, Vds=-15V		51.5		nC
Gate-source charge	Qgs	Id=-15A		14.5		nC
Gate-drain charge	Qgd			23.0		nC
Turn-on delay time	td(on)			14.0		ns
Turn-on rise time	tr	Vgs=-10V, Vds=-15V		16.5		ns
Turn-off delay time	td(off)	RL=1Ω, Rgen=3Ω		76.5		ns
Turn-off fall time	tf			37.5		ns
Body diode reverse recovery time	trr	If=-15A, dIf/dt=100A/μs		36.7	45.0	ns
Body diode reverse recovery charge	Qrr	If=-15A, dIf/dt=100A/μs		28.0		nC

NOTE :

1. The value of Rθja is measured with the device mounted on 1in² FR-4 board of 2oz. Copper, in still air environment with Ta=25°C. The value in any given applications depends on the user's specific board design, The current rating is based on the t ≤ 10s thermal resistance rating.
2. Repetitive rating, pulse width limited by junction temperature.
3. The Rθja is the sum of the thermal impedance from junction to lead Rθjl and lead to ambient.
4. The static characteristics in Figures 1 to 6 are obtained using 80μs pulses, duty cycle 0.5%max.
5. These tests are performed with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with Ta=25°C. The SOA curve provides a single pulse rating.

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■ Typical electrical and thermal characteristics

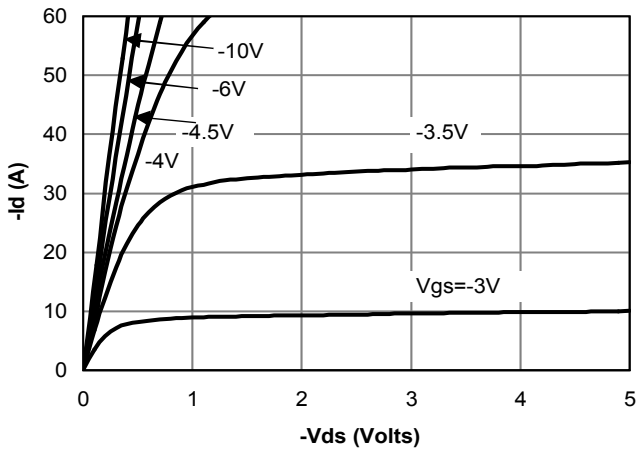


Fig 1: On-Region Characteristics

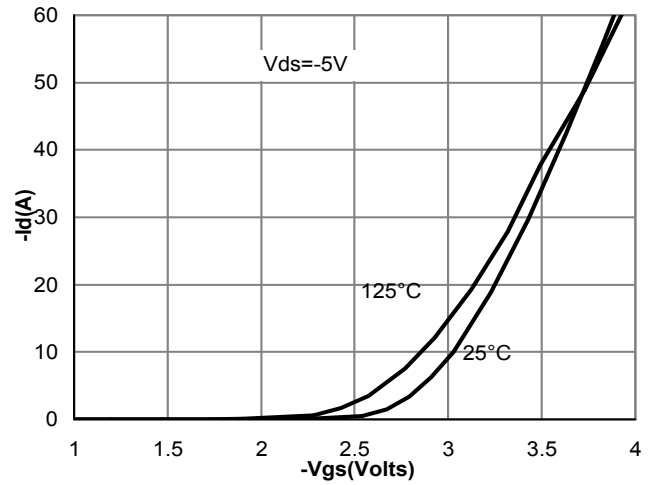


Figure 2: Transfer Characteristics

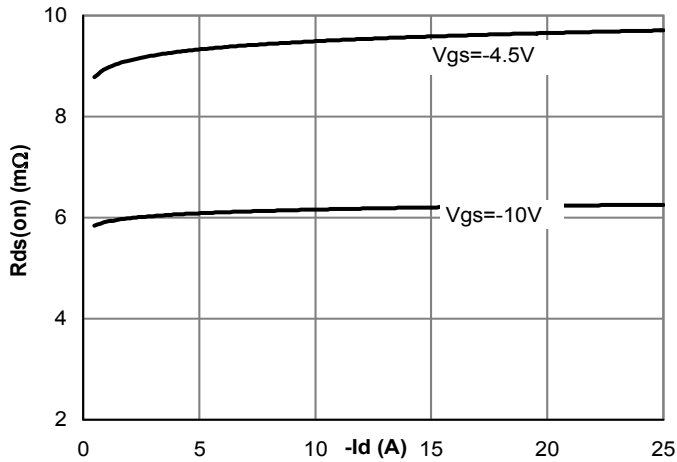


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

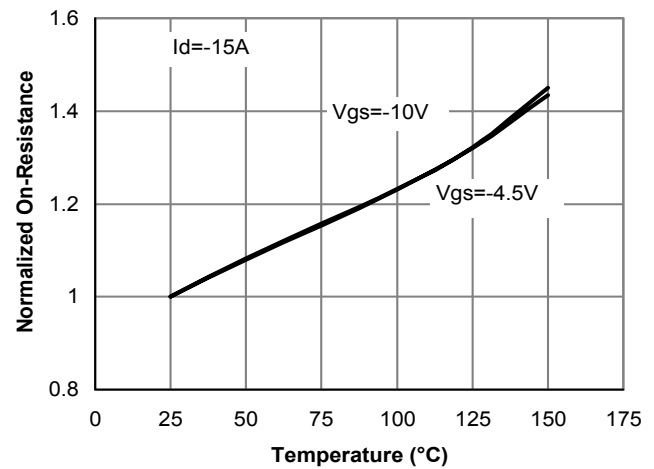


Figure 4: On-Resistance vs. Junction Temperature

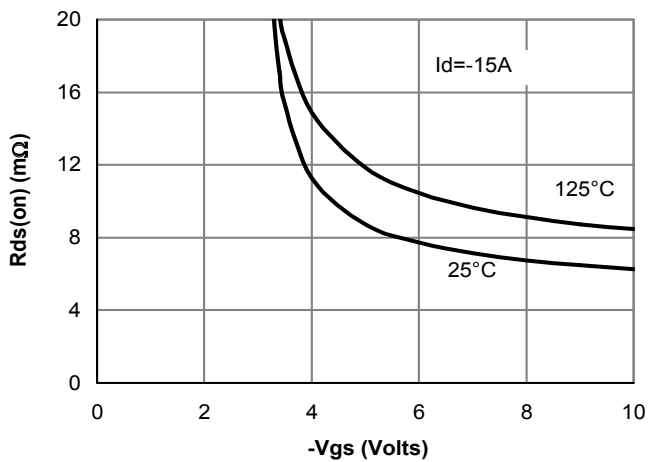


Figure 5: On-Resistance vs. Gate-Source Voltage

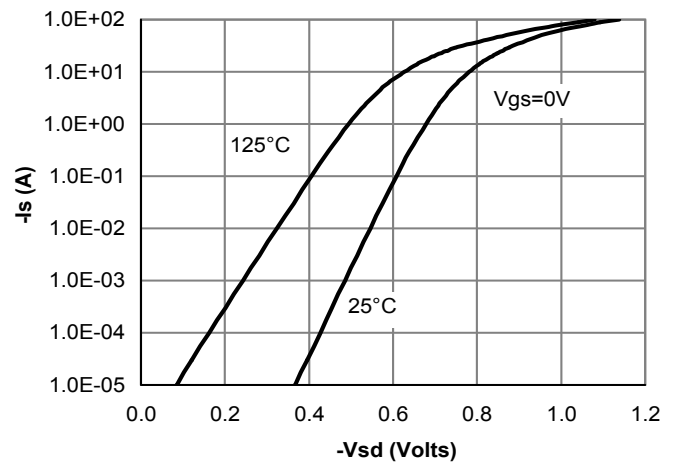


Figure 6: Body-Diode Characteristics

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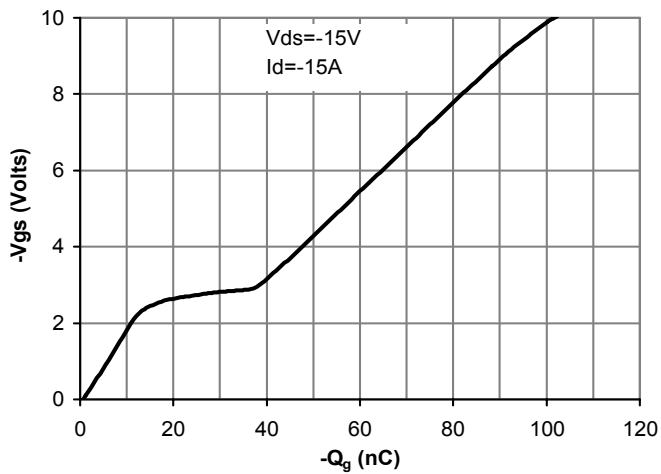


Figure 7: Gate-Charge Characteristics

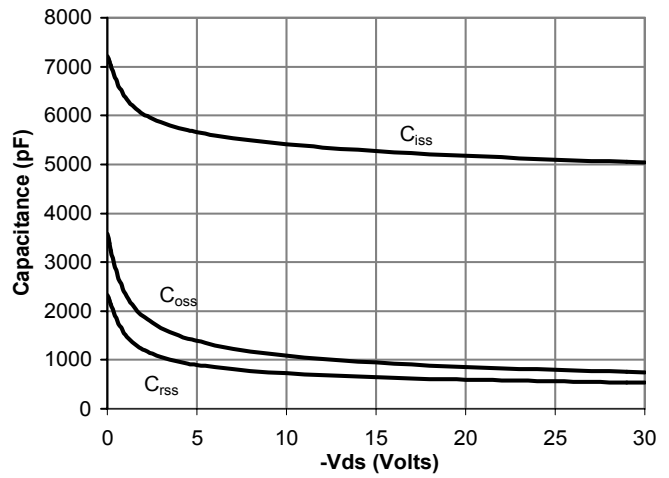


Figure 8: Capacitance Characteristics

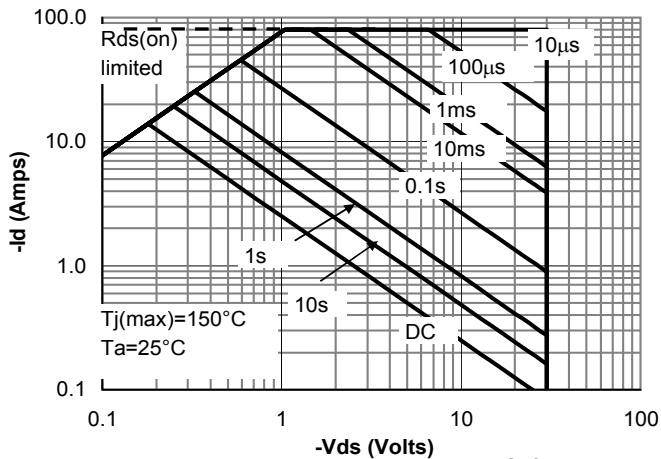


Figure 9: Maximum Forward Biased Safe Operating Area (Note 5)

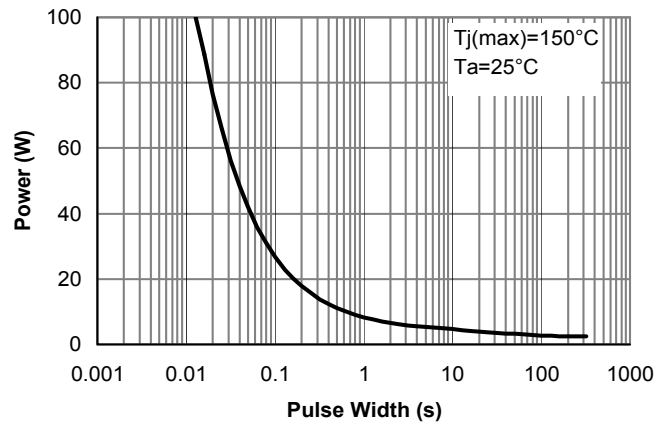


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note 5)

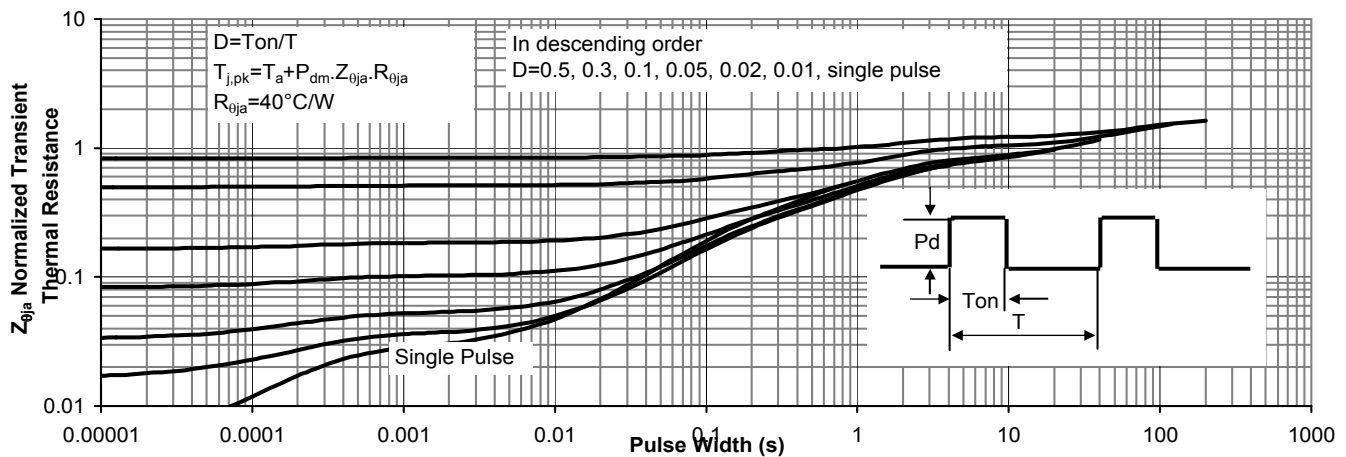


Figure 11: Normalized Maximum Transient Thermal Impedance