

# Single N-channel MOSFET

## ELM4N0008FRA-S

<https://www.elm-tech.com>

### ■ General description

ELM4N0008FRA-S uses advanced trench technology to provide excellent  $R_{ds(on)}$ , low gate charge and low gate threshold voltage.

### ■ Features

- $V_{ds}=100V$
- $I_d=2.2A$
- $R_{ds(on)} = 310m\Omega$  ( $V_{gs}=10V$ )
- $R_{ds(on)} = 320m\Omega$  ( $V_{gs}=4.5V$ )

### ■ Maximum absolute ratings

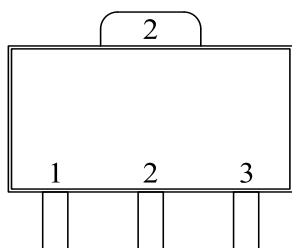
Parameter	Symbol	Limit	Unit	Note	
Drain-source voltage	$V_{ds}$	100	V		
Gate-source voltage	$V_{gs}$	$\pm 20$	V		
Continuous drain current ( $V_{gs}=10V$ )	$I_d$	$T_a=25^\circ C$	2.2	A	1
		$T_a=70^\circ C$	1.7		
Pulsed drain current	$I_{dm}$	5.5	A	2	
Power dissipation	$P_d$	1.5	W	3	
Storage temperature range	$T_{stg}$	-55 to 150	$^\circ C$		
Operating junction temperature range	$T_j$	-55 to 150	$^\circ C$		

### ■ Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit	Note
Thermal resistance junction-to-ambient	$R_{\theta ja}$	-	85	$^\circ C/W$	1
Thermal resistance junction-to-case	$R_{\theta jc}$	-	36	$^\circ C/W$	1

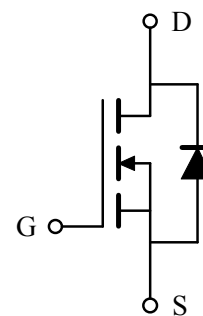
### ■ Pin configuration

SOT-89(TOP VIEW)



Pin No.	Pin name
1	GATE
2	DRAIN
3	SOURCE

### ■ Circuit



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### ■Electrical characteristics

T<sub>j</sub>=25°C. Unless otherwise noted.

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
<b>STATIC PARAMETERS</b>							
Drain-source breakdown voltage	BV <sub>dss</sub>	I <sub>d</sub> =250μA, V <sub>gs</sub> =0V	100	-	-	V	
Drain-source leakage current	I <sub>dss</sub>	V <sub>ds</sub> =80V, V <sub>gs</sub> =0V	-	-	1	μA	
		V <sub>ds</sub> =80V, V <sub>gs</sub> =0V, T <sub>j</sub> =55°C	-	-	5		
Gate-body leakage current	I <sub>gss</sub>	V <sub>ds</sub> =0V, V <sub>gs</sub> =±20V	-	-	±100	nA	
Gate threshold voltage	V <sub>gs(th)</sub>	V <sub>ds</sub> =V <sub>gs</sub> , I <sub>d</sub> =250μA	1.0	1.5	2.5	V	
Static drain-source on-resistance	R <sub>ds(on)</sub>	V <sub>gs</sub> =10V, I <sub>d</sub> =2A	-	260	310	mΩ	2
		V <sub>gs</sub> =4.5V, I <sub>d</sub> =1A	-	270	320		
Forward transconductance	G <sub>fs</sub>	V <sub>ds</sub> =5V, I <sub>d</sub> =2A	-	5.4	-	S	
Diode forward voltage	V <sub>sd</sub>	I <sub>s</sub> =1A, V <sub>gs</sub> =0V	-	-	1.2	V	2
Max. body-diode continuous current	I <sub>s</sub>	V <sub>gs</sub> =V <sub>ds</sub> =0V, Force current	-	-	2.2	A	1, 4
Pulsed body-diode current	I <sub>sm</sub>		-	-	5.5	A	2, 4
<b>DYNAMIC PARAMETERS</b>							
Input capacitance	C <sub>iss</sub>	V <sub>gs</sub> =0V, V <sub>ds</sub> =15V, f=1MHz	-	508.0	711.0	pF	
Output capacitance	C <sub>oss</sub>		-	29.0	41.0	pF	
Reverse transfer capacitance	C <sub>rss</sub>		-	16.4	33.0	pF	
Gate resistance	R <sub>g</sub>	V <sub>ds</sub> =0V, V <sub>gs</sub> =0V, f=1MHz	-	2.8	5.6	Ω	
<b>SWITCHING PARAMETERS</b>							
Total gate charge (10V)	Q <sub>g</sub>	V <sub>gs</sub> =10V, V <sub>ds</sub> =50V, I <sub>d</sub> =2A	-	9.1	12.7	nC	
Gate-source charge	Q <sub>gs</sub>		-	2.0	2.8	nC	
Gate-drain charge	Q <sub>gd</sub>		-	1.4	2.0	nC	
Turn-on delay time	t <sub>d(on)</sub>	V <sub>gs</sub> =10V, V <sub>ds</sub> =50V, I <sub>d</sub> =2A	-	2.0	4.0	ns	
Turn-on rise time	t <sub>r</sub>		-	21.6	39.0	ns	
Turn-off delay time	t <sub>d(off)</sub>	R <sub>gen</sub> =3.3Ω	-	11.2	22.0	ns	
Turn-off fall time	t <sub>f</sub>		-	18.8	37.6	ns	
Reverse recovery time	t <sub>rr</sub>	I <sub>f</sub> =2A, di/dt=100A/μs	-	17.5	-	ns	
Reverse recovery charge	Q <sub>rr</sub>		-	14.0	-	nC	

#### NOTE :

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width ≤ 300μs and duty cycle ≤ 2%.
3. The power dissipation is limited by 150°C junction temperature.
4. The data is theoretically the same as I<sub>d</sub> and I<sub>dm</sub>, in real applications, should be limited by total power dissipation.

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### ■ Typical characteristics

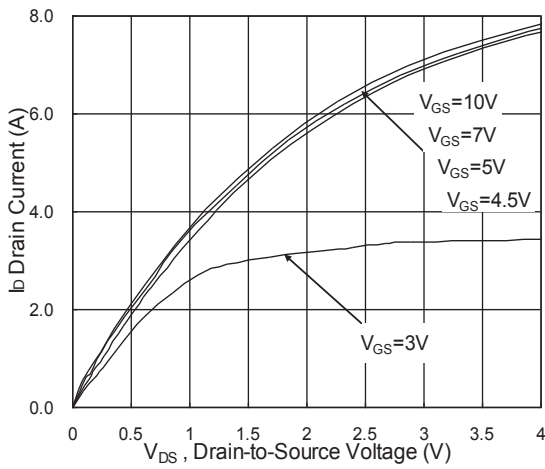


Fig.1 Typical Output Characteristics

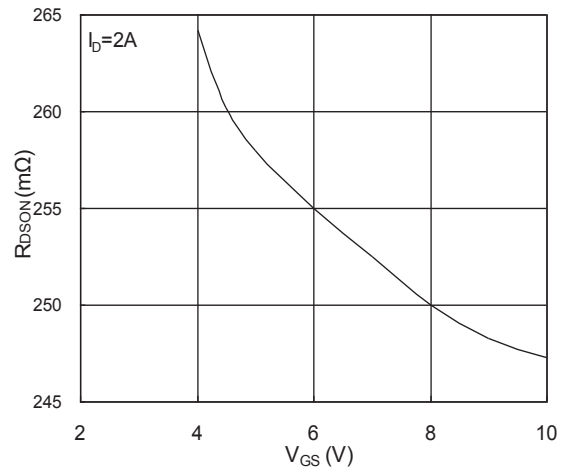


Fig.2 On-Resistance vs G-S Voltage

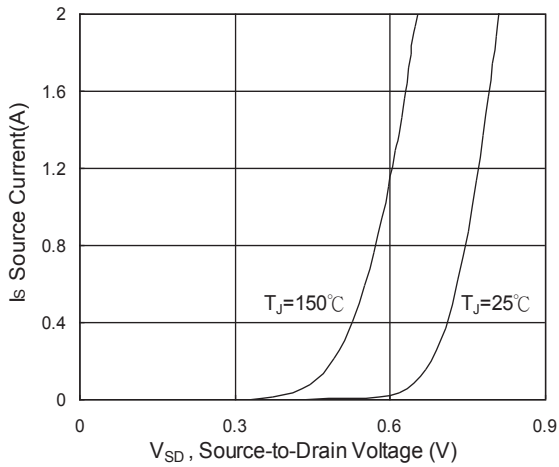


Fig.3 Source Drain Forward Characteristics

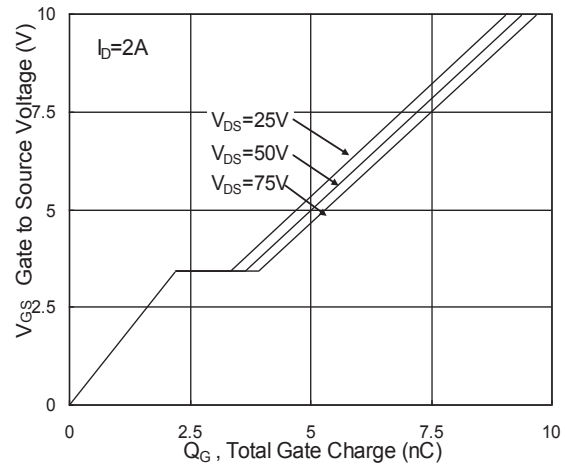


Fig.4 Gate-Charge Characteristics

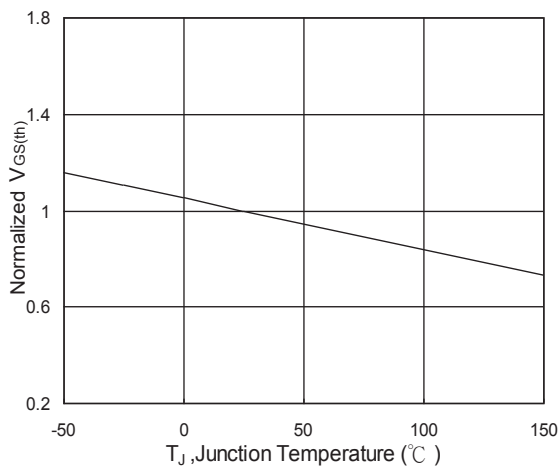


Fig.5 Normalized V<sub>GS(th)</sub> vs T<sub>J</sub>

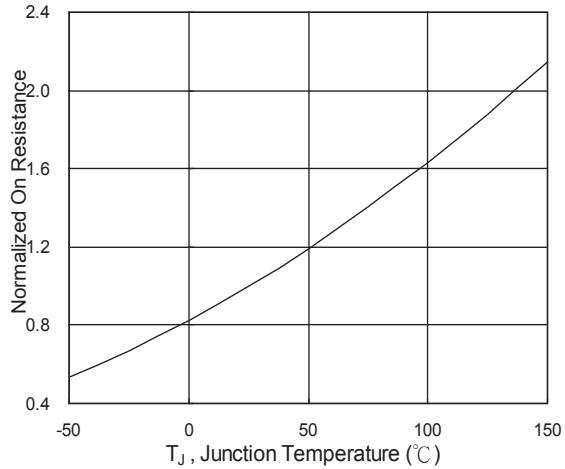


Fig.6 Normalized R<sub>DS(on)</sub> vs T<sub>J</sub>

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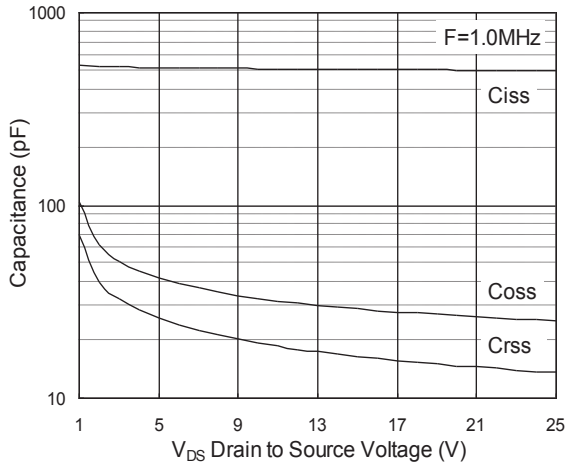


Fig.7 Capacitance

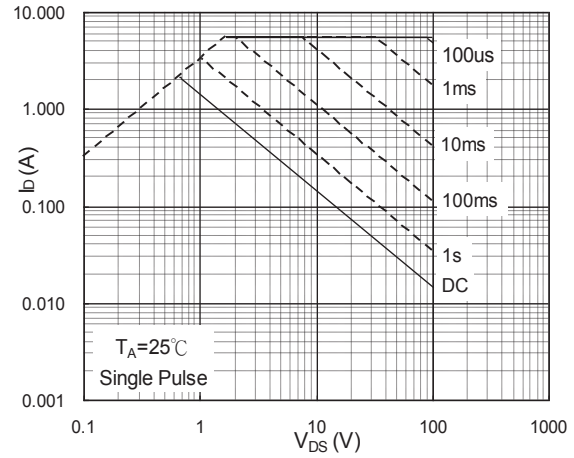


Fig.8 Safe Operating Area

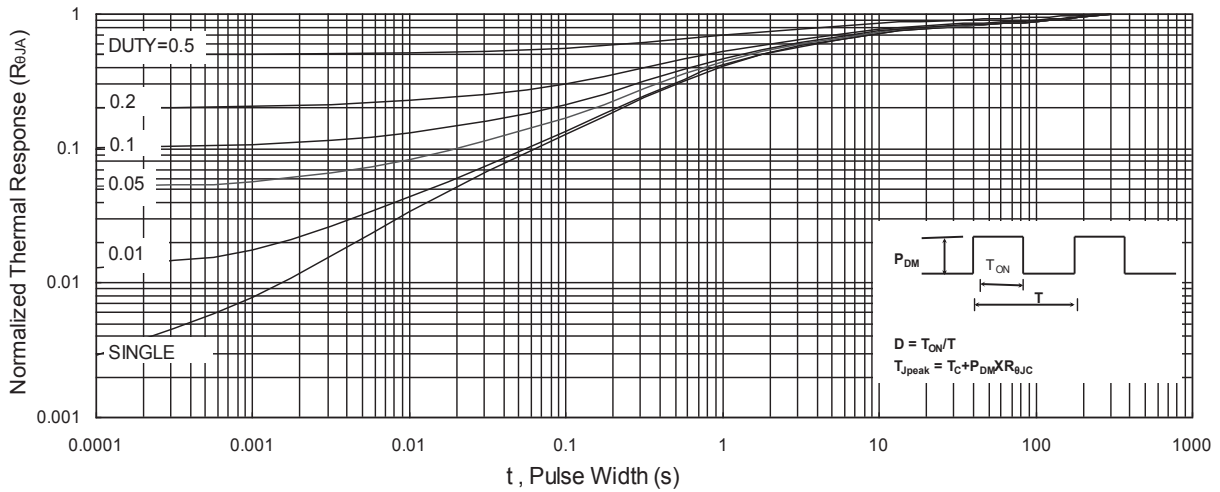


Fig.9 Normalized Maximum Transient Thermal Impedance

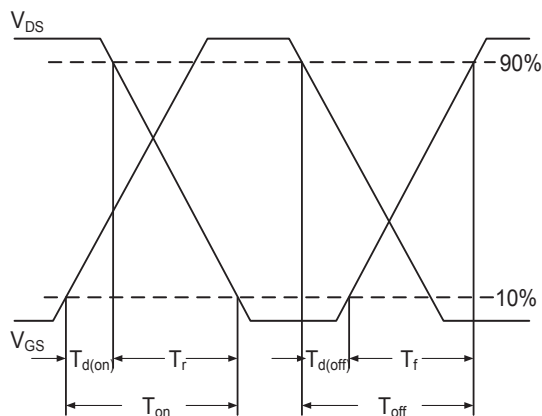


Fig.10 Switching Time Waveform

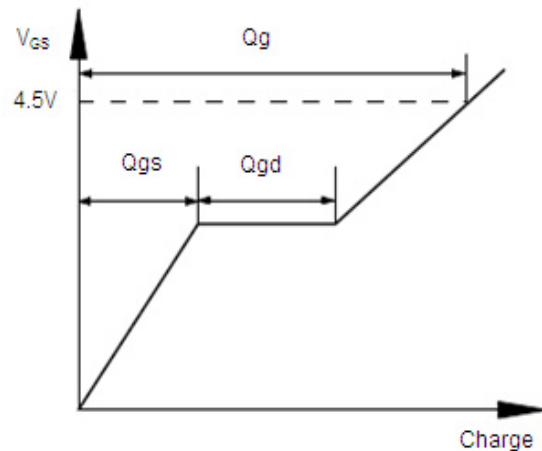


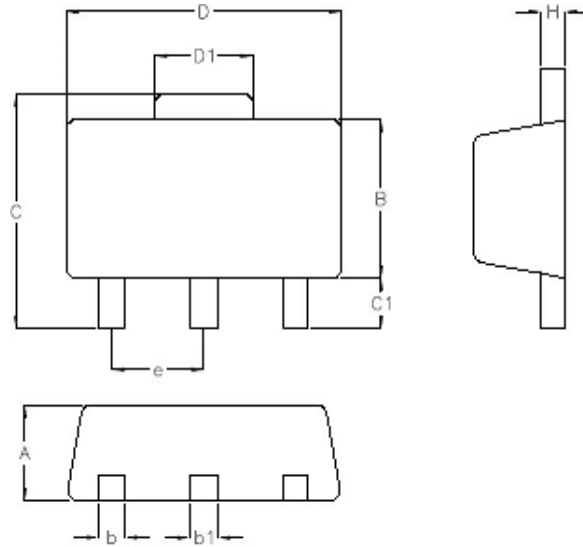
Fig.11 Gate Charge Waveform

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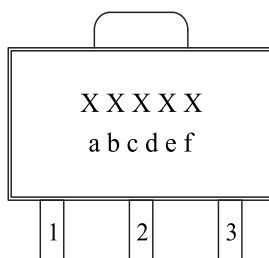
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### ■SOT-89 dimension



Symbols	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	1.397	1.600	0.055	0.063
b	0.420	0.540	0.017	0.021
b1	0.420	0.540	0.017	0.021
B	2.388	2.591	0.094	0.102
C	3.937	4.242	0.155	0.167
C1	0.787	1.194	0.031	0.047
D	4.394	4.597	0.173	0.181
D1	1.397	1.753	0.055	0.069
e	1.448	1.549	0.057	0.061
H	0.350	0.440	0.014	0.017

### ■Marking



Symbols	Content
xxxxx	K0008: Product code
a	Yearly code (2019=K, 2020=L, 2021=M, 2022=N.....)
b, c	Weekly code ( 01 to 53 )
d, e	Sequence ( 01 to 99 or 0A to 0Z )
f	Assembly code ( A to Z (I, O excepted))

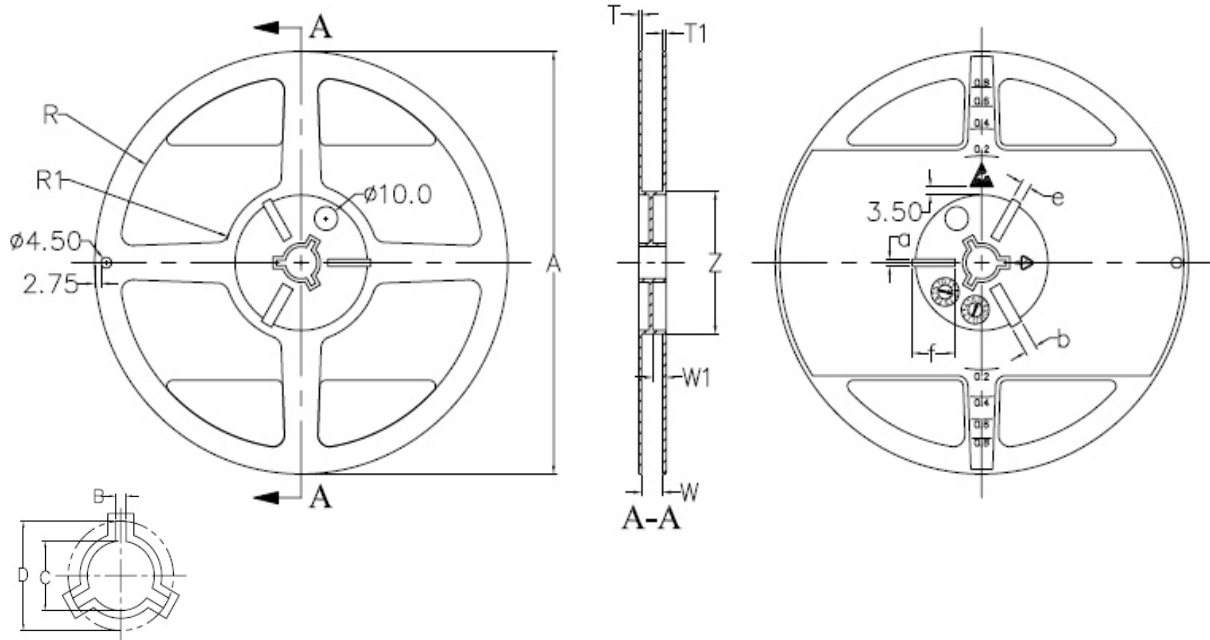
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## ■ SOT-89 Reel & carrier tape dimension

- Reel 7" (1,000 pcs/reel)



Unit: mm

Tape size	A	B	C	D	Z	T	W
12mm	$\phi 178 \pm 1.0$	$2.1 \pm 0.2$	$\phi 13.0 \begin{smallmatrix} +0.50 \\ -0.20 \end{smallmatrix}$	$21.2 \pm 0.3$	$\phi 60.0 \pm 0.5$	$1.30 \pm 0.1$	$12.4 \begin{smallmatrix} +2.0 \\ -0.0 \end{smallmatrix}$
W1	R	R1	a	b	e	f	T1
$5.80 \pm 0.1$	$78.5 \pm 0.3$	$33.5 \pm 0.3$	$3.0 \pm 0.1$	$5.0 \pm 0.1$	$4.0 \pm 0.1$	18.46 Ref	$1.50 \pm 0.1$

- Carrier tape

