

Single N-channel MOSFET

ELM4N0026FAA-N

<https://www.elm-tech.com>

■ General description

ELM4N0026FAA-N uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate threshold voltage.

■ Features

- $V_{ds}=100V$
- $I_d=7.5A$ ($V_{gs}=10V$)
- $R_{ds(on)} = 20m\Omega$ ($V_{gs}=10V$)
- $R_{ds(on)} = 25m\Omega$ ($V_{gs}=4.5V$)

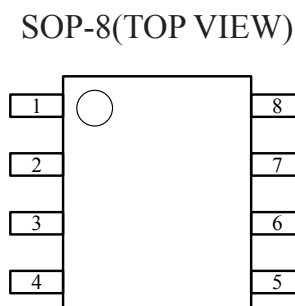
■ Maximum absolute ratings

Parameter		Symbol	Limit	Unit	Note
Drain-source voltage		V_{ds}	100	V	
Gate-source voltage		V_{gs}	± 20	V	
Continuous drain current ($V_{gs}=10V$)	$T_a=25^\circ C$	I_d	7.5	A	1
	$T_a=70^\circ C$		6.0		
Pulsed drain current		I_{dm}	40	A	2
Single pulse avalanche energy		EAS	16	mJ	3
Avalanche current		I_{as}	18	A	
Total power dissipation	$T_a=25^\circ C$	P_d	2.5	W	4
Storage temperature range		T_{stg}	- 55 to 150	$^\circ C$	
Operating junction temperature range		T_j	- 55 to 150	$^\circ C$	

■ Thermal characteristics

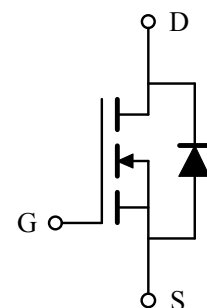
Parameter		Symbol	Typ.	Max.	Unit	Note
Thermal resistance junction-ambient	$t \leq 10s$	$R_{\theta ja}$	--	50	$^\circ C/W$	1
Thermal resistance junction-ambient	Steady-state		--	85	$^\circ C/W$	

■ Pin configuration



Pin No.	Pin name
1	SOURCE
2	SOURCE
3	SOURCE
4	GATE
5	DRAIN
6	DRAIN
7	DRAIN
8	DRAIN

■ Circuit



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■Electrical characteristics

T_j=25°C. Unless otherwise noted.

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BV _{dss}	V _{gs} =0V, I _d =250μA	100	--	--	V	
BV _{dss} temperature coefficient	$\frac{\Delta BV_{dss}}{\Delta T_j}$	Reference to 25°C, I _d =1mA	--	0.08	--	V/°C	
Static drain-source on-resistance	R _{ds(on)}	V _{gs} =10V, I _d =7A	--	16	20	mΩ	2
		V _{gs} =4.5V, I _d =5A	--	19	25		
Gate threshold voltage	V _{gs(th)}	V _{gs} =V _{ds} , I _d =250μA	1.2	--	2.5	V	
V _{gs(th)} temperature coefficient	ΔV _{gs(th)}		--	-5.5	--	mV/°C	
Drain-source leakage current	I _{dss}	V _{ds} =80V, V _{gs} =0V	--	--	10	μA	
		V _{ds} =80V, V _{gs} =0V, T _j =55°C	--	--	100		
Gate-source leakage current	I _{gss}	V _{gs} =±20V, V _{ds} =0V	--	--	±100	nA	
Forward transconductance	G _{fs}	V _{ds} =5V, I _d =7A	--	24	--	S	
Continuous source current	I _s	V _{gs} =V _{ds} =0V, Force current	--	--	7	A	1, 5
Pulsed source current	I _{sm}		--	--	40	A	2, 5
Diode forward voltage	V _{sd}	V _{gs} =0V, I _s =1A	--	--	1.2	V	2
DYNAMIC PARAMETERS							
Input capacitance	C _{iss}	V _{ds} =15V, V _{gs} =0V, f=1MHz	--	1930	--	pF	
Output capacitance	C _{oss}		--	245	--	pF	
Reverse transfer capacitance	C _{rss}		--	125	--	pF	
Gate resistance	R _g	V _{ds} =0V, V _{gs} =0V, f=1MHz	--	1.6	--	Ω	
SWITCHING PARAMETERS							
Total gate charge (10V)	Q _g	V _{ds} =80V, V _{gs} =10V I _d =7A	--	36	--	nC	
Gate-source charge	Q _{gs}		--	5	--	nC	
Gate-drain charge	Q _{gd}		--	10	--	nC	
Turn-on delay time	t _{d(on)}	V _{dd} =50V, V _{gs} =10V R _{gen} =3.3Ω, I _d =7A	--	11.5	--	ns	
Turn-on rise time	t _r		--	29.0	--	ns	
Turn-off delay time	t _{d(off)}		--	42.0	--	ns	
Turn-off fall time	t _f		--	18.0	--	ns	
Reverse recovery time	t _{rr}	I _f =7A, di/dt=100A/μs	--	48	--	nS	
Reverse recovery charge	Q _{rr}		--	29	--	nC	

Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. The EAS data shows Max. rating . The test condition is V_{dd}=25V, V_{gs}=10V, L=0.1mH.
4. The power dissipation is limited by 150°C junction temperature.
5. The data is theoretically the same as I_d and I_{dm}, in real applications, should be limited by total power dissipation.

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■ Typical electrical and thermal characteristics

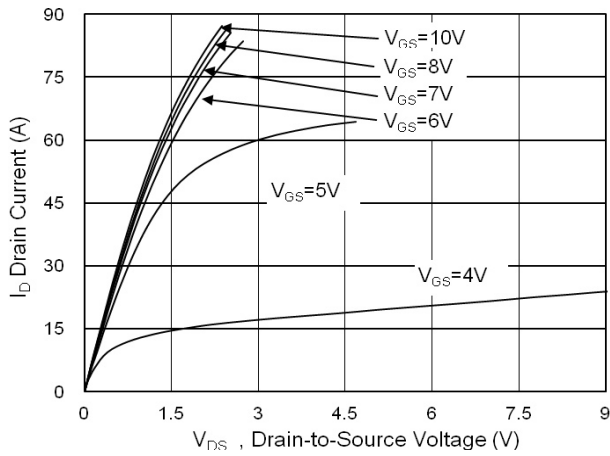


Fig.1 Typical Output Characteristics

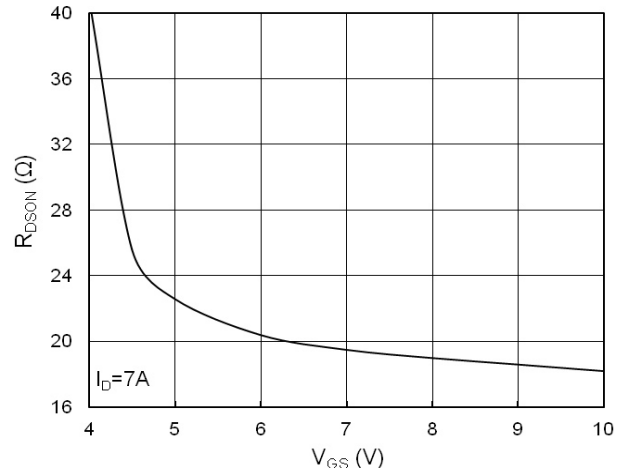


Fig.2 On-Resistance vs. Gate-Source

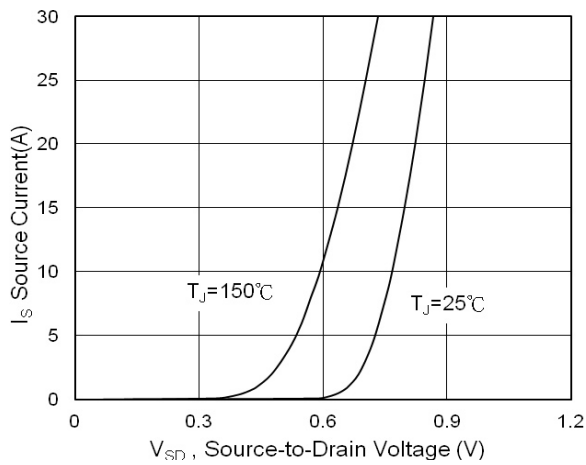


Fig.3 Forward Characteristics Of Reverse

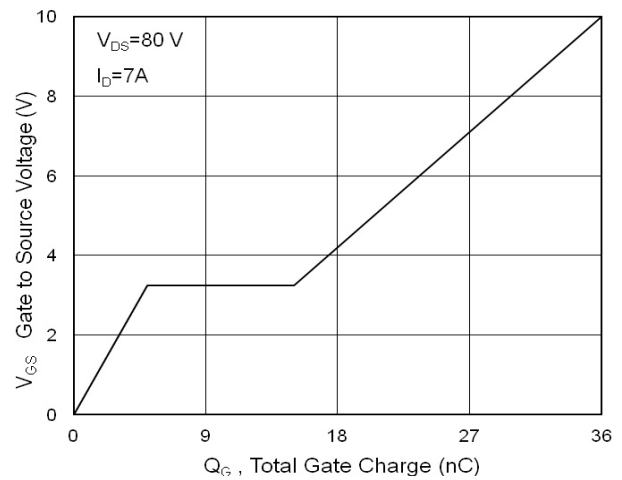


Fig.4 Gate-Charge Characteristics

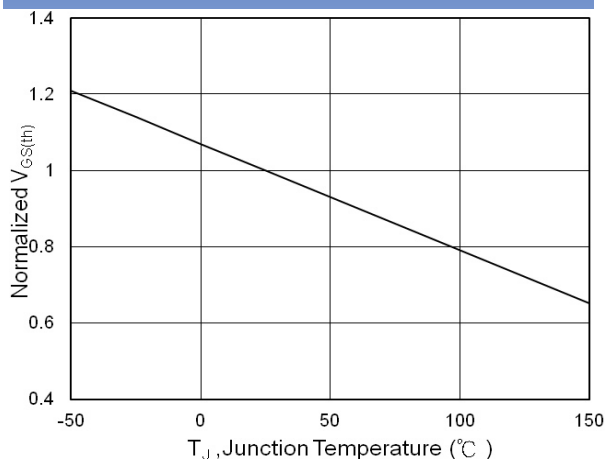


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

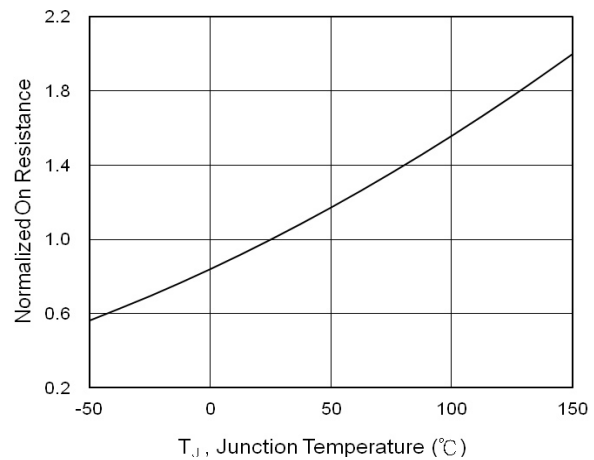


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

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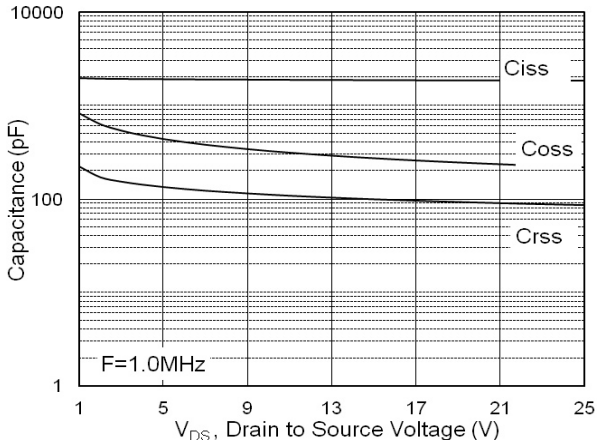


Fig.7 Capacitance

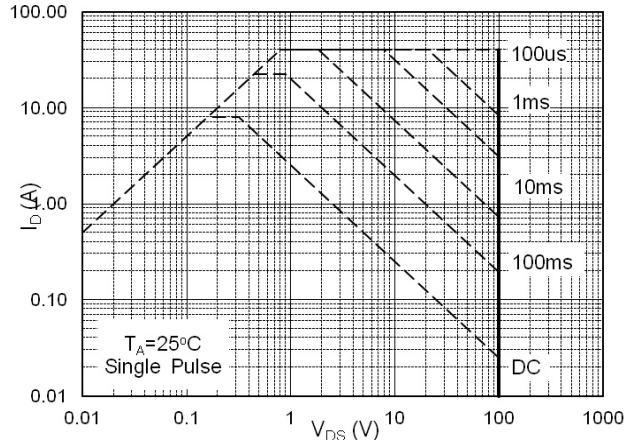


Fig.8 Safe Operating Area

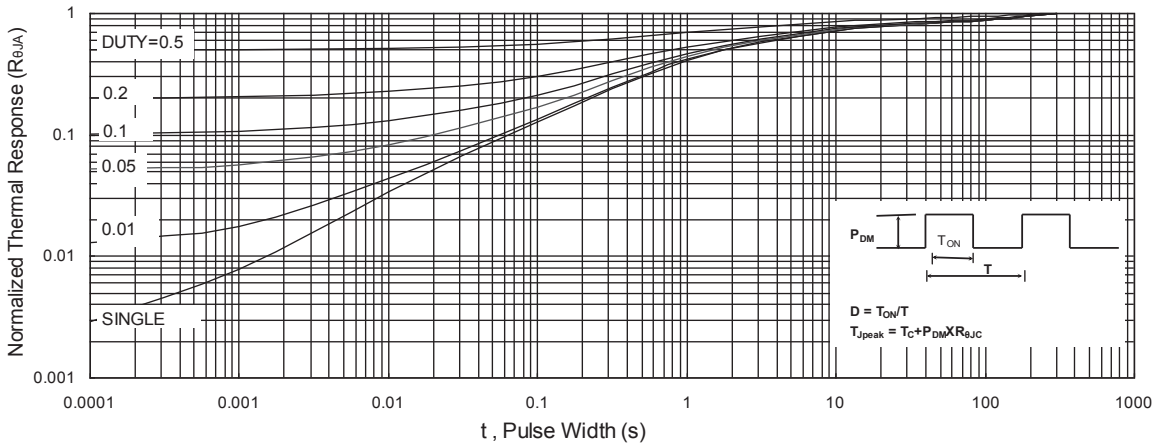


Fig.9 Normalized Maximum Transient Thermal Impedance

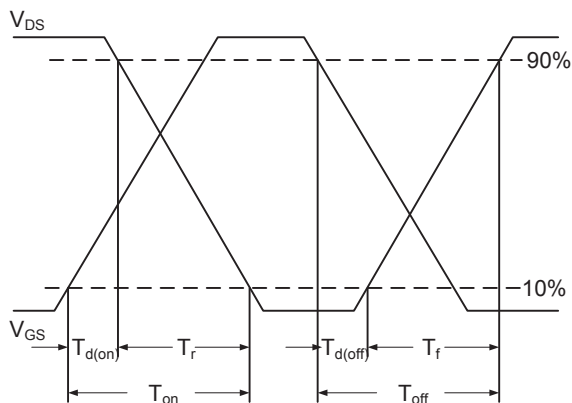


Fig.10 Switching Time Waveform

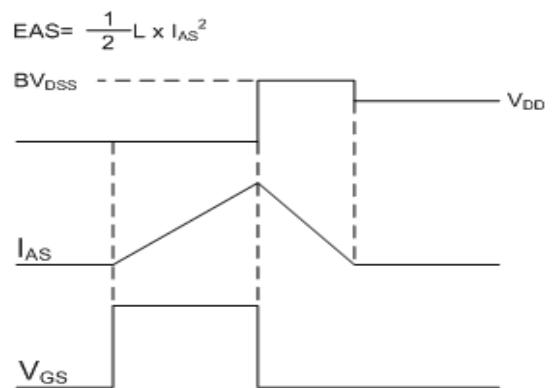


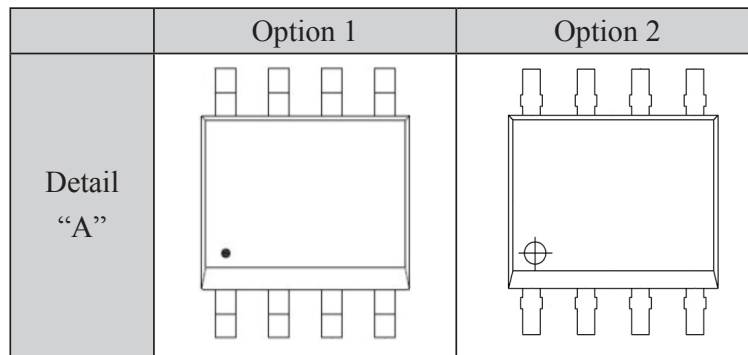
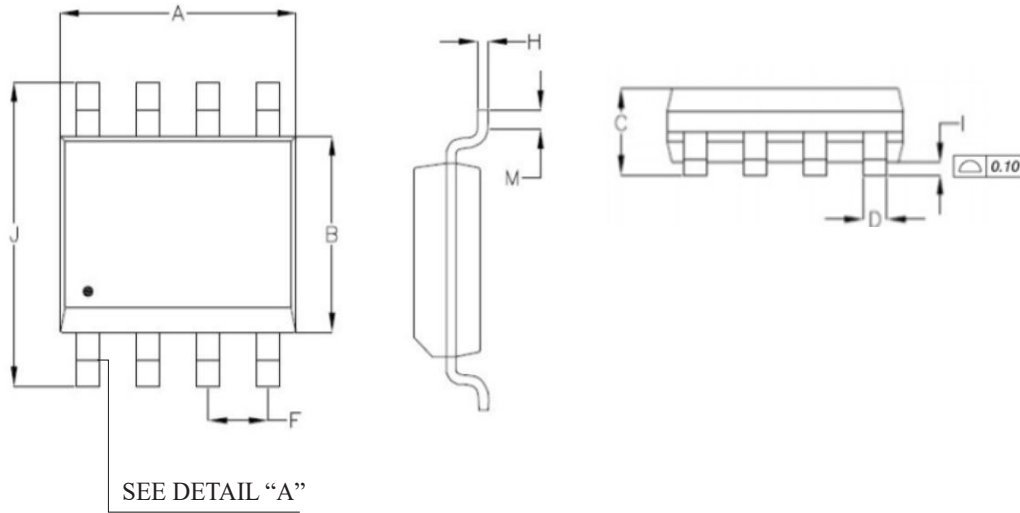
Fig.11 Unclamped Inductive Switching Waveform

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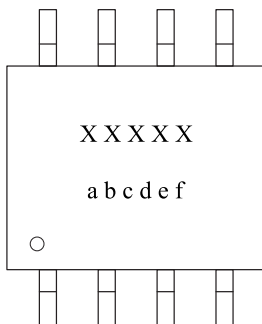
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■SOP-8 dimension (2,500pcs/reel)



Symbols	Millimeters		Inches		Symbols	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	4.80	5.00	0.189	0.197	H	0.17	0.25	0.007	0.010
B	3.80	4.00	0.150	0.157	I	0.10	0.25	0.004	0.010
C	1.35	1.75	0.053	0.069	J	5.80	6.20	0.228	0.244
D	0.31	0.51	0.012	0.020	M	0.40	1.27	0.016	0.050
F	1.27 BSC		0.050 BSC						

■Marking



Symbols	Content
xxxxx	Product code
a	Yearly code: 2019=K, 2020=L, 2021=M, 2022=N...
b, c	Weekly code: 01 to 53
d, e	Sequence: 01 to 99 or 0A to 0Z
f	Assembly code: A to Z (I, O excepted)