

Single P-channel MOSFET

ELM4P6107FCA-S

<https://www.elm-tech.com>

■General description

ELM4P6107FCA-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate threshold voltage.

■Features

- $V_{ds} = -60V$
- $I_d = -1.7A$ ($V_{gs} = -10V$)
- $R_{ds(on)} = 180m\Omega$ ($V_{gs} = -10V$)
- $R_{ds(on)} = 266m\Omega$ ($V_{gs} = -4.5V$)

■Maximum absolute ratings

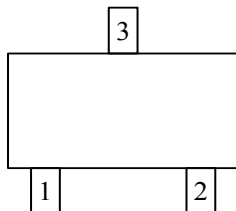
Parameter		Symbol	Limit	Unit	Note
Drain-source voltage		V_{ds}	-60	V	
Gate-source voltage		V_{gs}	± 20	V	
Continuous drain current ($V_{gs} = -10V$)	$T_a = 25^\circ C$	I_d	-1.7	A	1
	$T_a = 70^\circ C$		-1.4		
Pulsed drain current		I_{dm}	-7	A	2
Power dissipation	$T_a = 25^\circ C$	P_d	1	W	3
Junction and storage temperature range		T_j, T_{stg}	-55 to +150	$^\circ C$	

■Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit	Note
Thermal resistance junction-to-ambient	$R_{\theta ja}$	-	125	$^\circ C/W$	1
Thermal resistance junction-to-case	$R_{\theta jc}$	-	80		

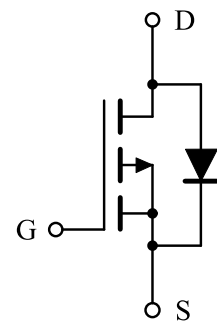
■Pin configuration

SOT-23(TOP VIEW)



Pin No.	Pin name
1	GATE
2	SOURCE
3	DRAIN

■Circuit



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■Electrical characteristics

T_j=25°C. Unless otherwise noted.

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BV _{dss}	V _{gs} =0V, I _d =-250μA	-60	-	-	V	
Zero gate voltage drain current	I _{dss}	V _{ds} =-48V, V _{gs} =0V	-	-	-1	μA	
		V _{ds} =-48V, V _{gs} =0V, T _j =55°C	-	-	-5		
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±20V	-	-	±100	nA	
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , I _d =-250μA	-1.0	-	-2.5	V	
Static drain-source on-resistance	R _{ds(on)}	V _{gs} =-10V, I _d =-1.5A	-	-	180	mΩ	2
		V _{gs} =-4.5V, I _d =-1.0A	-	-	266		
Forward transconductance	G _{fs}	V _{ds} =-5V, I _d =-1.5A	-	5.9	-	S	
Diode forward voltage	V _{sd}	I _s =-1A, V _{gs} =0V	-	-	-1.2	V	2
Max. body-diode continuous current	I _s	V _{gs} =V _{ds} =0V, Force Current	-	-	-1.7	A	1, 4
Pulsed source current	I _{sm}		-	-	-7.0	A	2, 4
DYNAMIC PARAMETERS							
Input capacitance	C _{iss}	V _{gs} =0V, V _{ds} =-15V, f=1MHz	-	531	-	pF	
Output capacitance	C _{oss}		-	59	-	pF	
Reverse transfer capacitance	C _{rss}		-	38	-	pF	
SWITCHING PARAMETERS							
Total gate charge (-4.5V)	Q _g	V _{gs} =-4.5V, V _{ds} =-20V I _d =-1.5A	-	4.60	-	nC	
Gate-source charge	Q _{gs}		-	1.40	-	nC	
Gate-drain charge	Q _{gd}		-	1.62	-	nC	
Turn-on delay time	t _{d(on)}	V _{gs} =-10V, V _{ds} =-15V I _d =-1A, R _{gen} =3.3Ω	-	17.4	-	ns	
Turn-on rise time	t _r		-	5.4	-	ns	
Turn-off delay time	t _{d(off)}		-	37.2	-	ns	
Turn-off fall time	t _f		-	2.4	-	ns	

NOTE :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed, pulse width ≤ 300μs and duty cycle ≤ 2%.
3. The power dissipation is limited by 150°C junction temperature.
4. The data is theoretically the same as I_d and I_{dm}, in real applications, should be limited by total power dissipation.

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■ Typical electrical and thermal characteristics

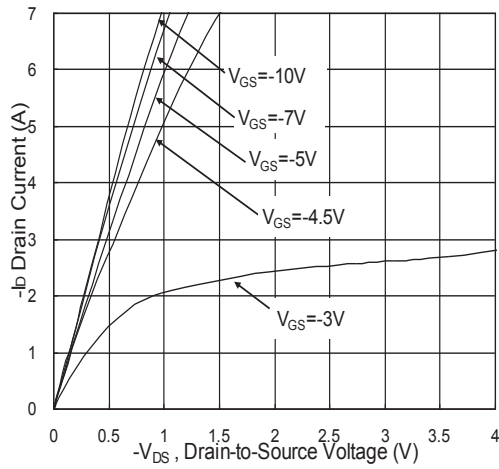


Fig.1 Typical Output Characteristics

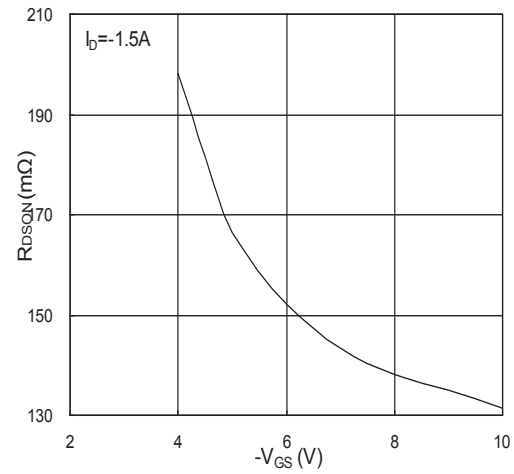


Fig.2 On-Resistance v.s Gate-Source

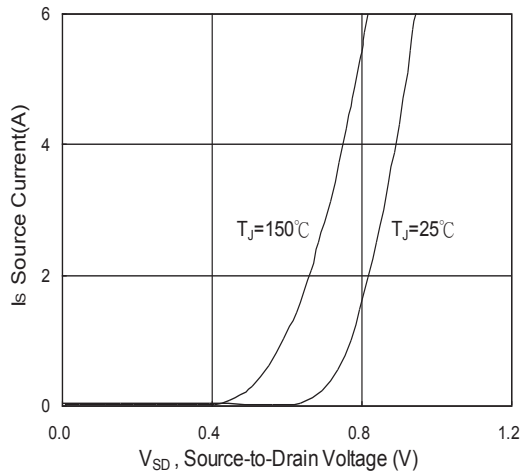


Fig.3 Forward Characteristics Of Reverse

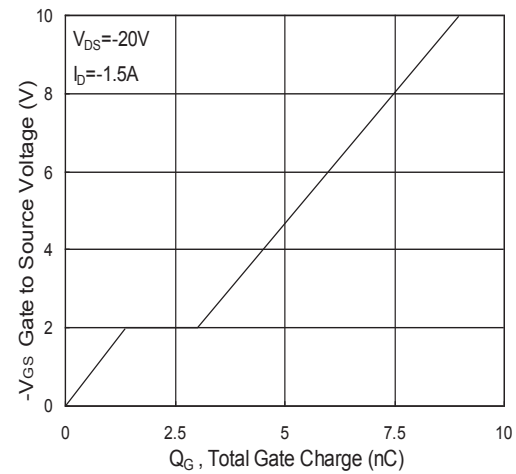


Fig.4 Gate-Charge Characteristics

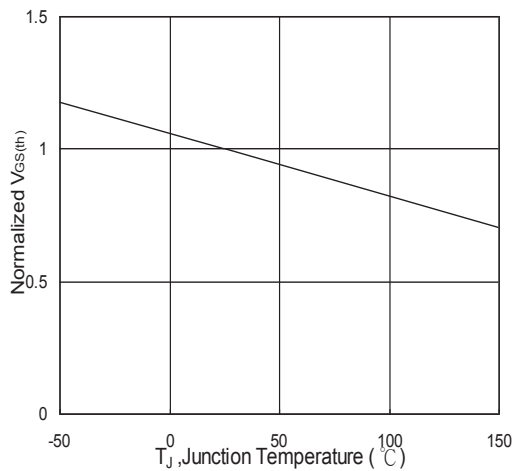


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

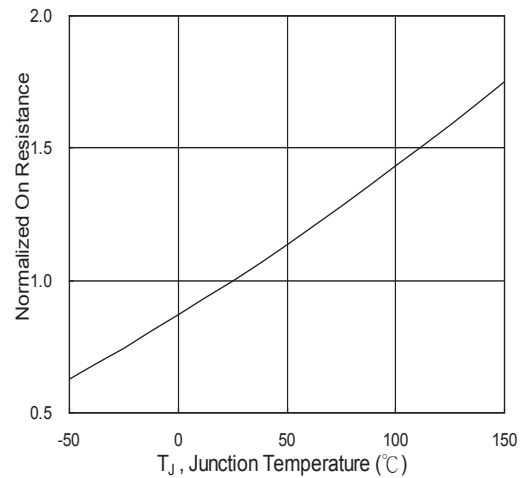


Fig.6 Normalized $R_{DS(on)}$ v.s T_J

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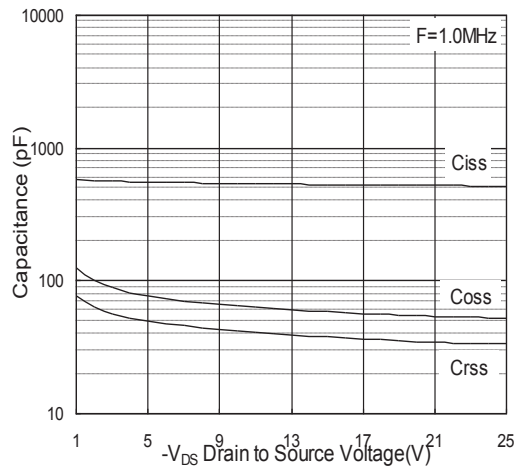


Fig.7 Capacitance

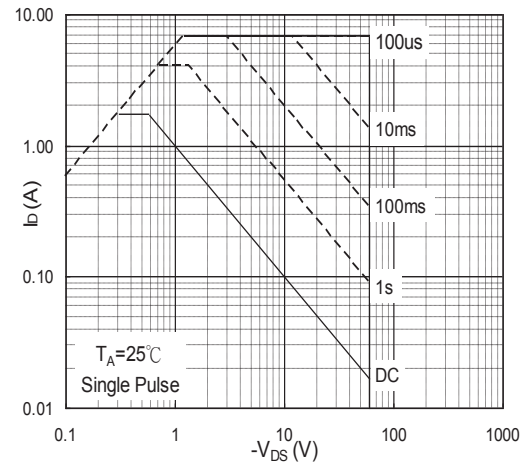


Fig.8 Safe Operating Area

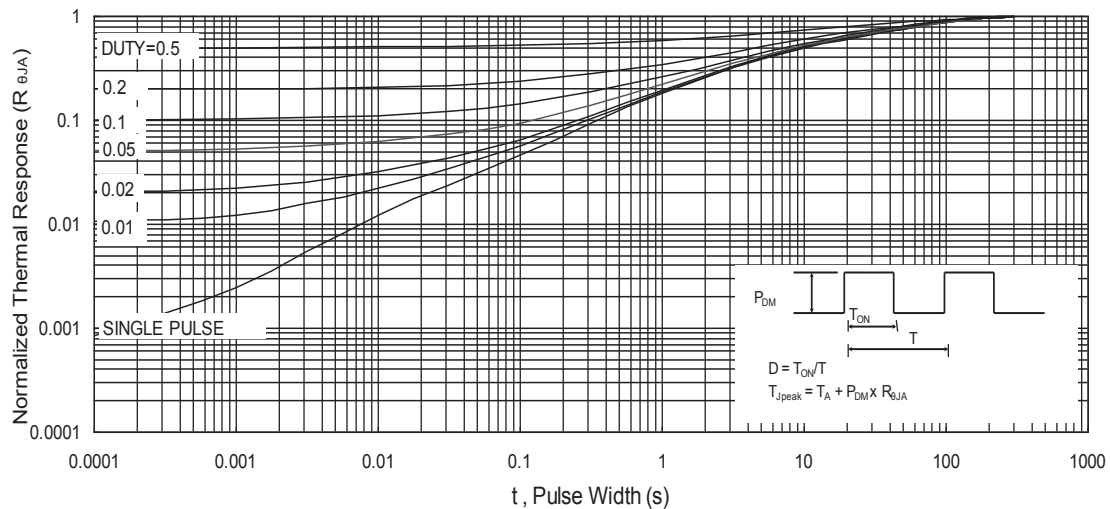


Fig.9 Normalized Maximum Transient Thermal Impedance

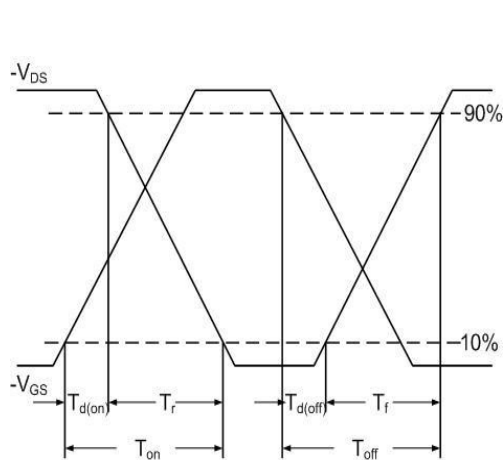


Fig.10 Switching time waveform

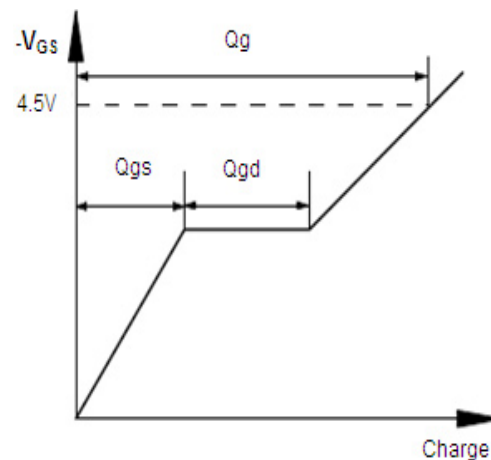


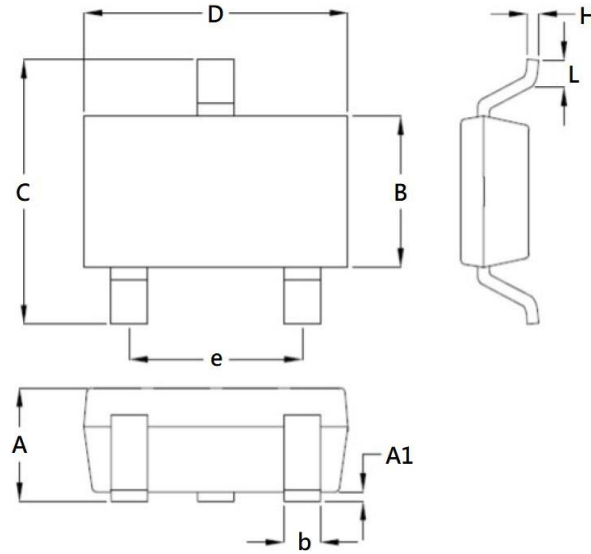
Fig.11 Gate Charge waveform

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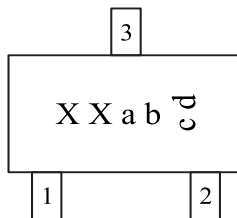
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■SOT-23 dimension (3,000pcs/reel)



Symbols	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.850	1.295	0.033	0.051
A1	0.000	0.152	0.000	0.006
B	1.397	1.803	0.055	0.071
b	0.300	0.508	0.012	0.020
C	2.591	3.010	0.102	0.119
D	2.692	3.100	0.106	0.122
e	1.900		0.075	
H	0.080	0.254	0.003	0.010
L	0.300	0.610	0.012	0.024

■Marking



Symbols	Content
xx	Product code
a	Yearly code : 2019=9, 2020=A, 2021=B, 2022=C.....
b	Weekly code : A to Z, a to z (53 weeks in total)
c	Sequence : 1 to 9 or A to Z
d	Assembly code : A to Z (I, O excepted)