

Single N-channel MOSFET

ELM51012EA-S

<http://www.elm-tech.com>

■General description

ELM51012EA-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate threshold voltage.

■Features

- $V_{ds}=20V$
- $I_d=0.7A$
- $R_{ds(on)} = 360m\Omega$ ($V_{gs}=4.5V$)
- $R_{ds(on)} = 420m\Omega$ ($V_{gs}=2.5V$)
- $R_{ds(on)} = 560m\Omega$ ($V_{gs}=1.8V$)

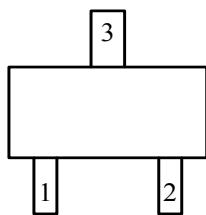
■Maximum absolute ratings

Ta=25°C. Unless otherwise noted.

Parameter	Symbol	Limit	Unit
Drain-source voltage	V_{ds}	20	V
Gate-source voltage	V_{gs}	± 12	V
Continuous drain current $T_j=150^{\circ}\text{C}$	I_d	0.7	A
		0.4	
Pulsed drain current	I_{dm}	1.0	A
Power dissipation	P_d	0.27	W
		0.16	
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	°C

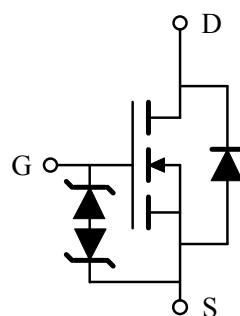
■Pin configuration

SOT-523(TOP VIEW)



Pin No.	Pin name
1	GATE
2	SOURCE
3	DRAIN

■Circuit



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■Electrical characteristics

T_a=25°C. Unless otherwise noted.

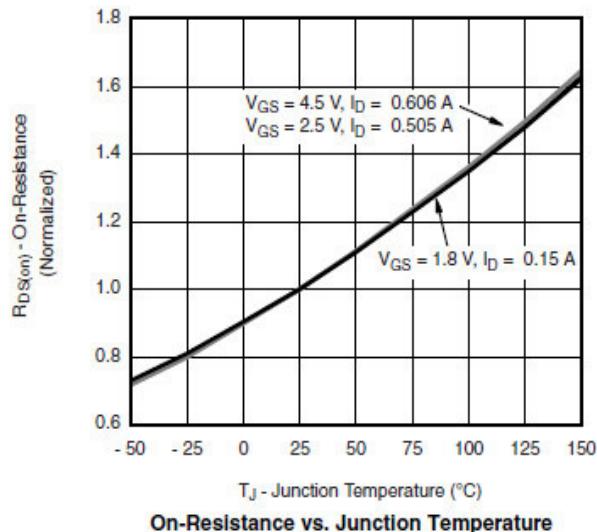
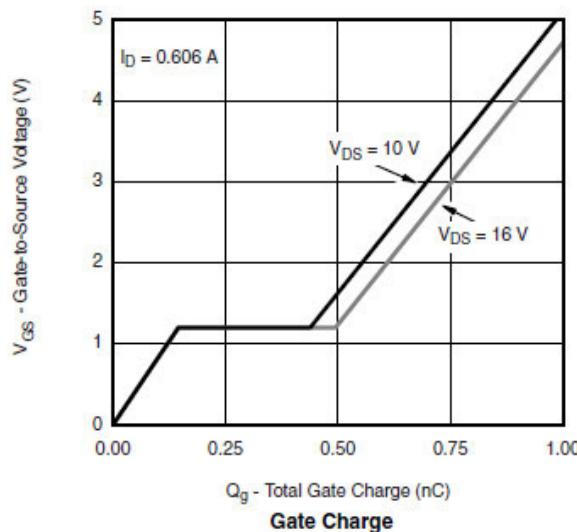
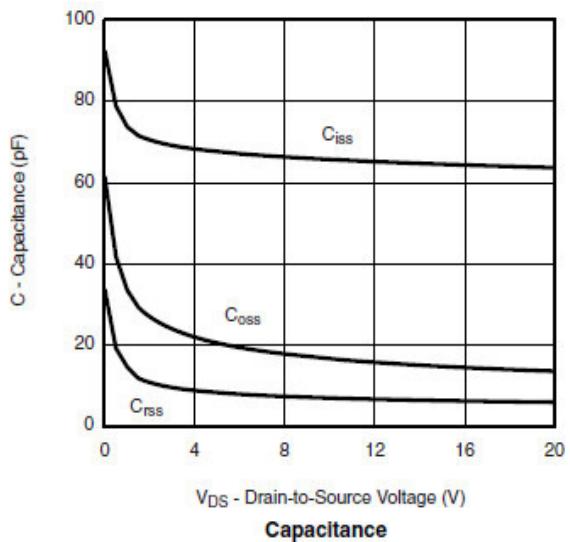
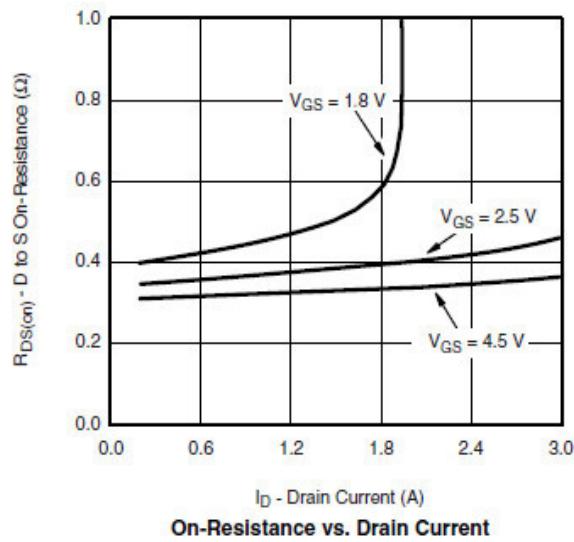
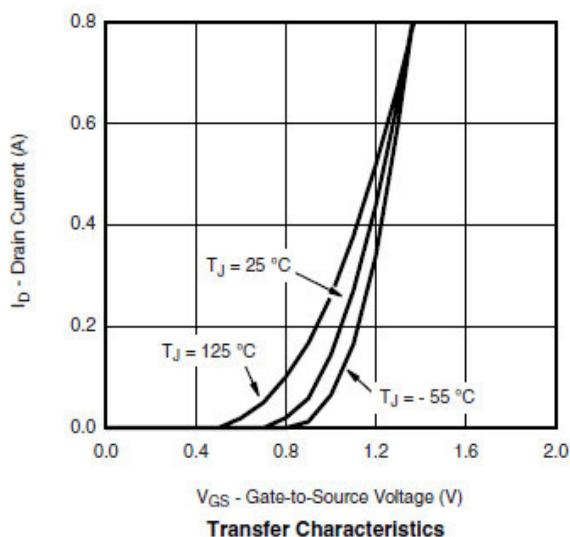
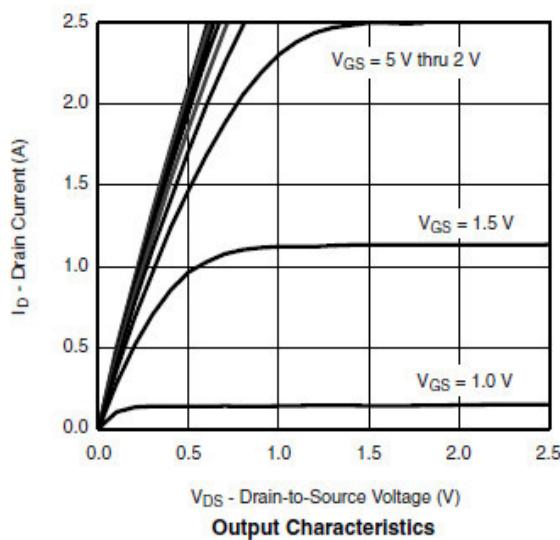
Parameter	Symbol	Condition		Min.	Typ.	Max.	Unit	
STATIC PARAMETERS								
Drain-source breakdown voltage	BV _{dss}	Id=250μA, V _{gs} =0V		20			V	
Zero gate voltage drain current	Id _{ss}	V _{ds} =16V, V _{gs} =0V	Ta=85°C		1		μA	
					5			
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±12V				±1	mA	
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , Id=250μA		0.3		0.8	V	
On state drain current	I _{d(on)}	V _{gs} =4.5V, V _{ds} =5V		0.7			A	
Static drain-source on-resistance	R _{d(on)}	V _{gs} =4.5V, Id=0.6A			240	360	mΩ	
		V _{gs} =2.5V, Id=0.5A			300	420		
		V _{gs} =1.8V, Id=0.4A			420	560		
Forward transconductance	G _{fs}	V _{ds} =10V, Id=0.4A			1		S	
Diode forward voltage	V _{sd}	Is=0.15A, V _{gs} =0V			0.65	1.20	V	
Max. body-diode continuous current	I _s					0.3	A	
DYNAMIC PARAMETERS								
Input capacitance	C _{iss}	V _{gs} =0V, V _{ds} =10V, f=1MHz			70		pF	
Output capacitance	C _{oss}				20		pF	
Reverse transfer capacitance	C _{rss}				8		pF	
SWITCHING PARAMETERS								
Total gate charge	Q _g	V _{gs} =4.5V, V _{ds} =10V, Id=0.6A			1.06	1.38	nC	
Gate-source charge	Q _{gs}				0.18		nC	
Gate-drain charge	Q _{gd}				0.32		nC	
Turn-on delay time	t _{d(on)}	V _{gs} =4.5V, V _{ds} =10V RL=20Ω, Id=0.5A, R _{gen} =1Ω			18	26	ns	
Turn-on rise time	t _r				20	28	ns	
Turn-off delay time	t _{d(off)}				70	110	ns	
Turn-off fall time	t _f				25	40	ns	

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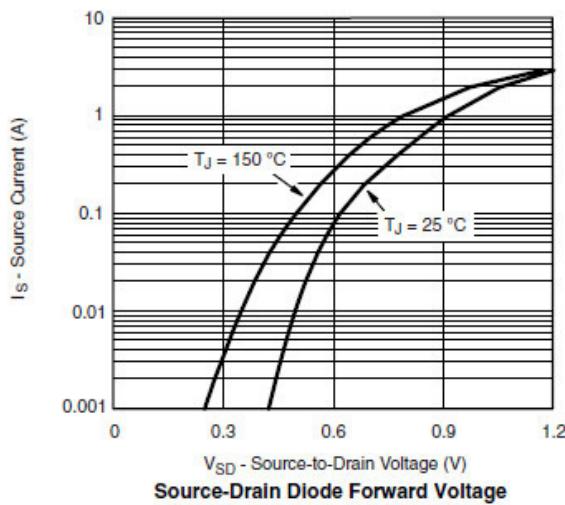
■ Typical electrical and thermal characteristics



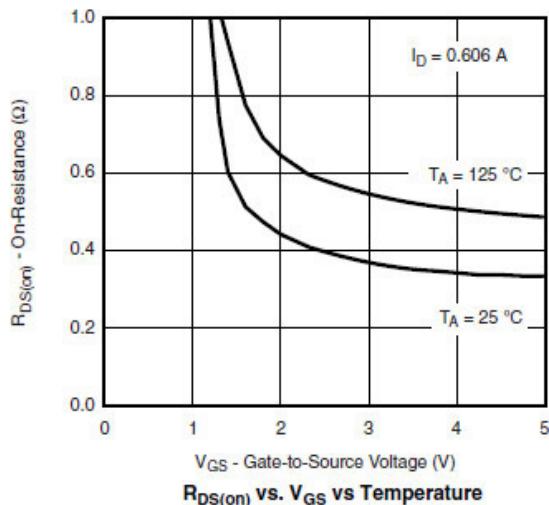
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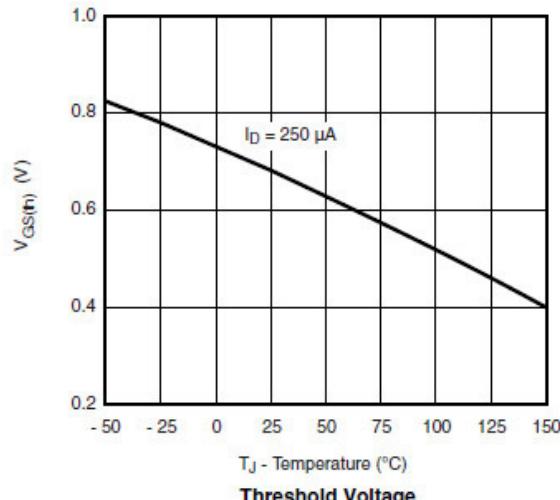
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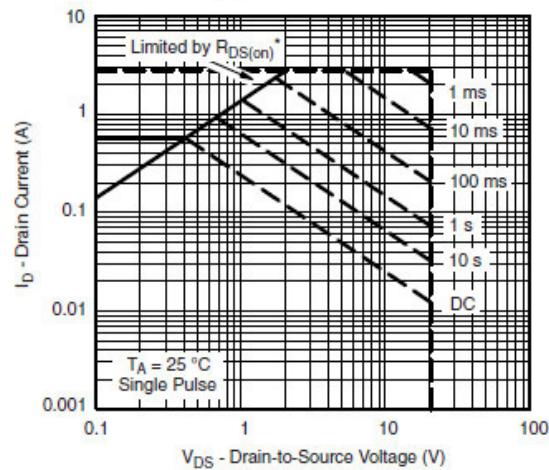
Source-Drain Diode Forward Voltage



$R_{DS(on)}$ vs. V_{GS} vs Temperature

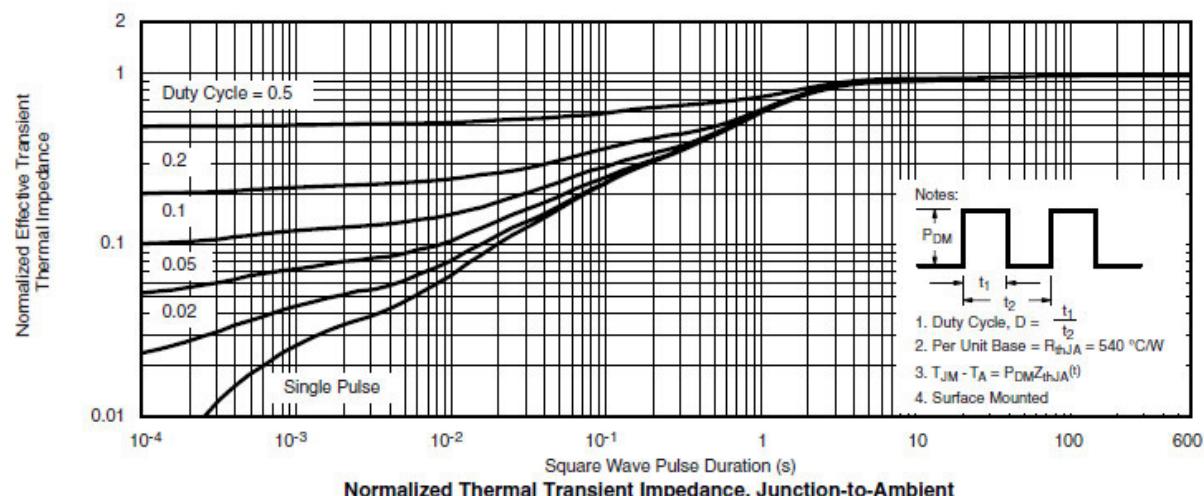


Threshold Voltage



* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient



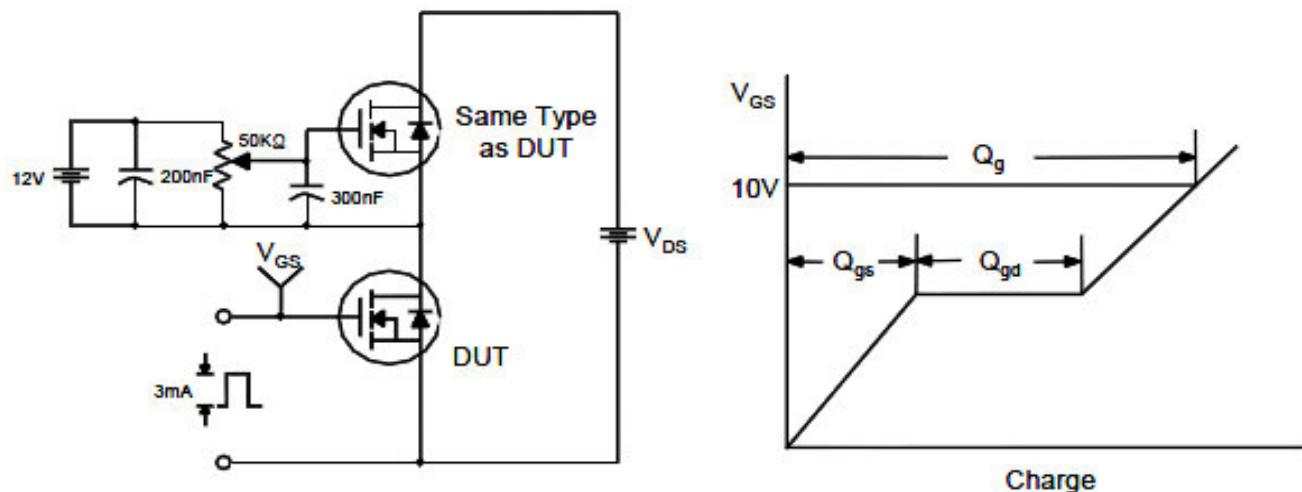
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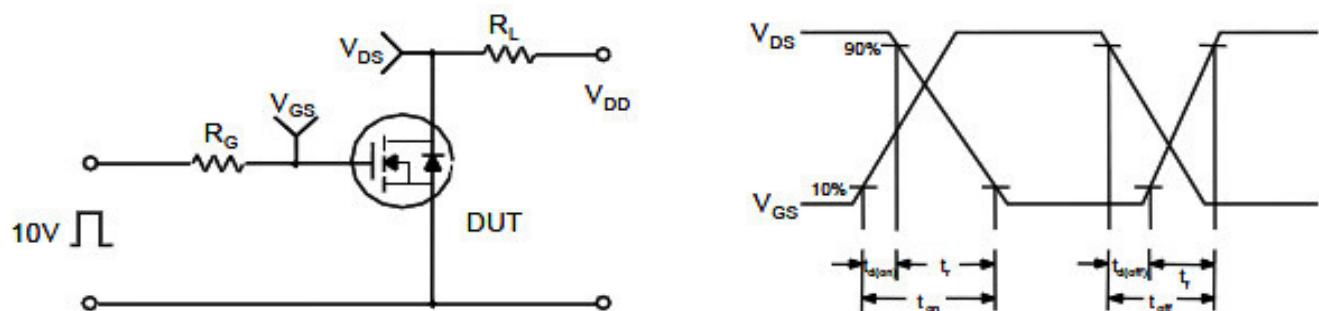
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■ Test circuit and waveform

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

