

# Dual P-channel MOSFET

## ELM51023EA-S

<http://www.elm-tech.com>

### ■ General description

ELM51023EA-S uses advanced trench technology to provide excellent  $R_{ds(on)}$ , low gate charge and operation with gate voltages as low as 1.8V and internal ESD protection.

### ■ Features

- $V_{ds} = -20V$
- $I_d = -0.45A$
- $R_{ds(on)} = 800m\Omega$  ( $V_{gs} = -4.5V$ )
- $R_{ds(on)} = 950m\Omega$  ( $V_{gs} = -2.5V$ )
- $R_{ds(on)} = 1250m\Omega$  ( $V_{gs} = -1.8V$ )
- ESD protected

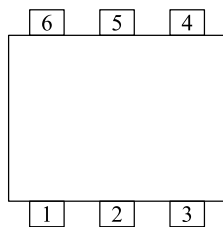
### ■ Maximum absolute ratings

$T_a = 25^\circ C$ . Unless otherwise noted.

Parameter	Symbol	Limit	Unit
Drain-source voltage	$V_{ds}$	-20	V
Gate-source voltage	$V_{gs}$	$\pm 12$	V
Continuous drain current ( $T_j = 150^\circ C$ )	$I_d$	$T_a = 25^\circ C$	-0.45
		$T_a = 70^\circ C$	-0.25
Pulsed drain current	$I_{dm}$	-1.0	A
Power dissipation	$P_d$	$T_c = 25^\circ C$	0.27
		$T_c = 70^\circ C$	0.16
Junction and storage temperature range	$T_j, T_{stg}$	-55 to 150	$^\circ C$

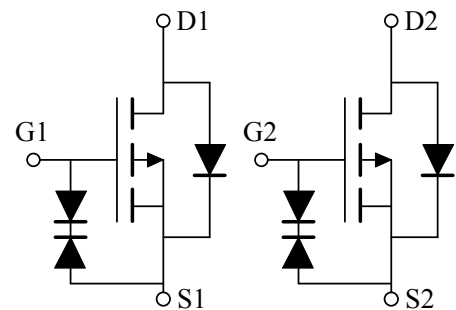
### ■ Pin configuration

SOT-563(TOP VIEW)



Pin No.	Pin name
1	SOURCE1
2	GATE1
3	DRAIN2
4	SOURCE2
5	GATE2
6	DRAIN1

### ■ Circuit



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### ■Electrical characteristics

Ta=25°C. Unless otherwise noted.

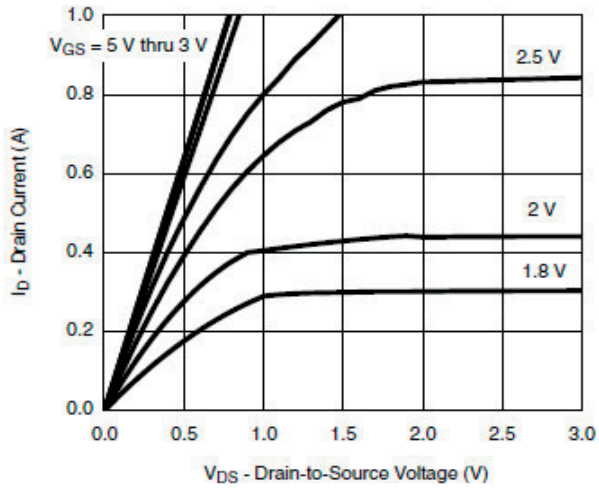
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>						
Drain-source breakdown voltage	BVdss	Id=-250μA, Vgs=0V	-20			V
Zero gate voltage drain current	Idss	Vds=-20V, Vgs=0V			-5	μA
		Ta=85°C			-10	
Gate-body leakage current	Igss	Vds=0V, Vgs=±12V			±1	mA
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=-250μA	-0.4		-1.0	V
On state drain current	Id(on)	Vgs=-4.5V, Vds≥-5V	-0.7			A
Static drain-source on-resistance	Rds(on)	Vgs=-4.5V, Id=-0.6A		550	800	mΩ
		Vgs=-2.5V, Id=-0.5A		700	950	
		Vgs=-1.8V, Id=-0.4A		1000	1250	
Forward transconductance	Gfs	Vds=-10V, Id=-0.4A		1		S
Diode forward voltage	Vsd	Is=-0.15A, Vgs=0V		-0.65	-1.20	V
Max. body-diode continuous current	Is				-0.3	A
<b>DYNAMIC PARAMETERS</b>						
Input capacitance	Ciss			70	100	pF
Output capacitance	Coss	Vgs=0V, Vds=-10V, f=1MHz		20		pF
Reverse transfer capacitance	Crss			10		pF
<b>SWITCHING PARAMETERS</b>						
Total gate charge	Qg	Vgs=-4.5V, Vds=-10V Id≐-0.25A		1.0	1.3	nC
Gate-source charge	Qgs			0.1		nC
Gate-drain charge	Qgd			0.3		nC
Turn-on delay time	td(on)	Vgs=-4.5V, Vds=-10V RL=30Ω, Id≐-0.2A Rgen=10Ω		10	15	ns
Turn-on rise time	tr			10	15	ns
Turn-off delay time	td(off)			40	60	ns
Turn-off fall time	tf			30	50	ns

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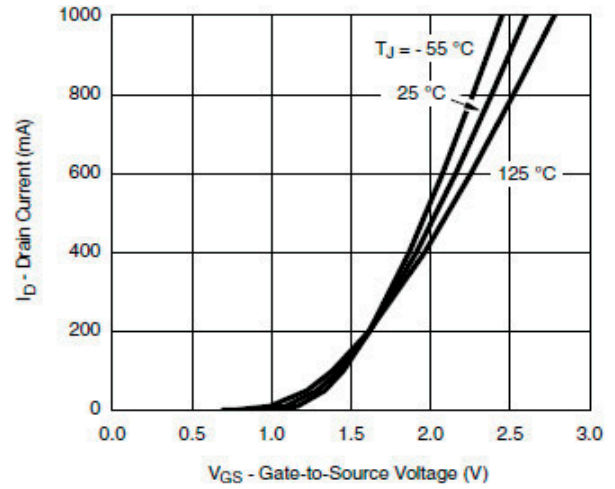
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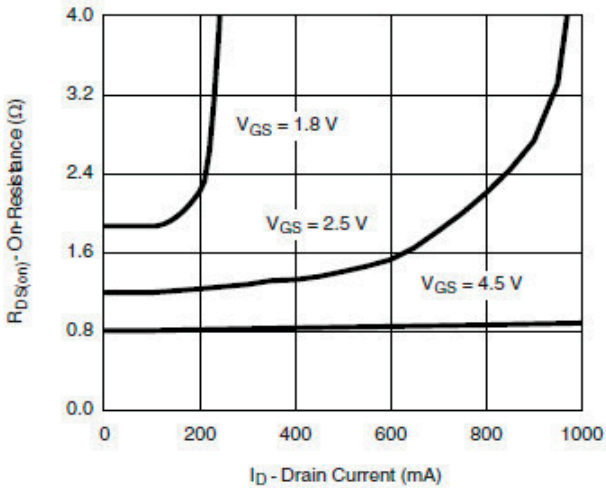
### ■ Typical electrical and thermal characteristics



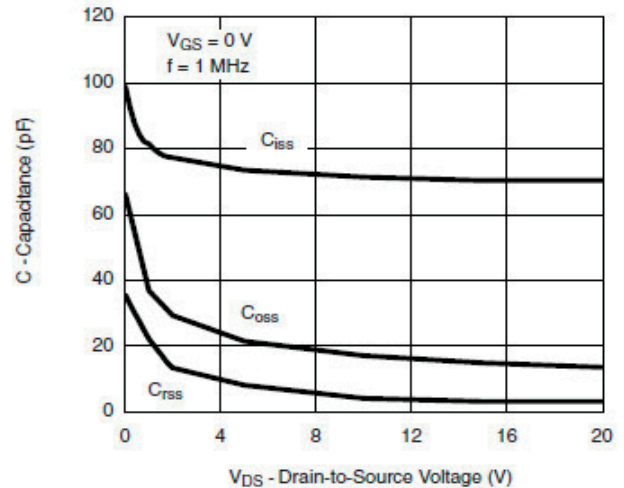
Output Characteristics



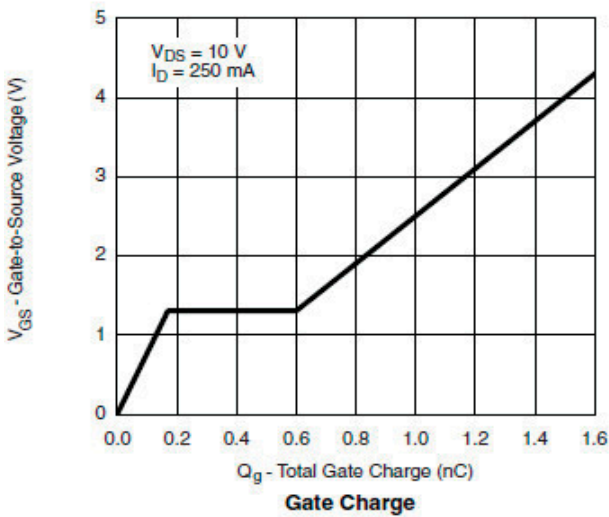
Transfer Characteristics



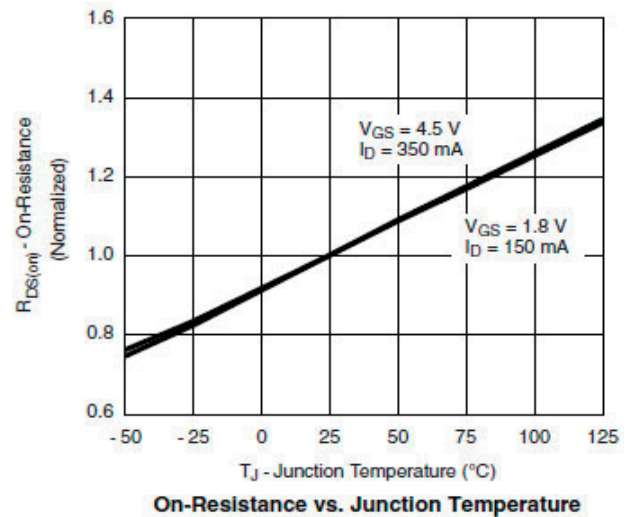
On-Resistance vs. Drain Current



Capacitance



Gate Charge

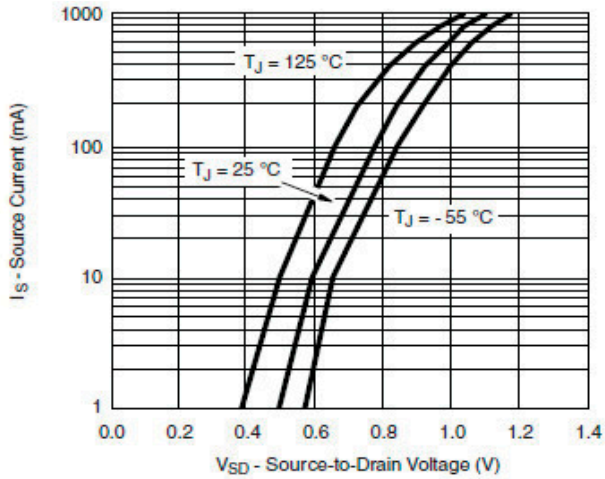


On-Resistance vs. Junction Temperature

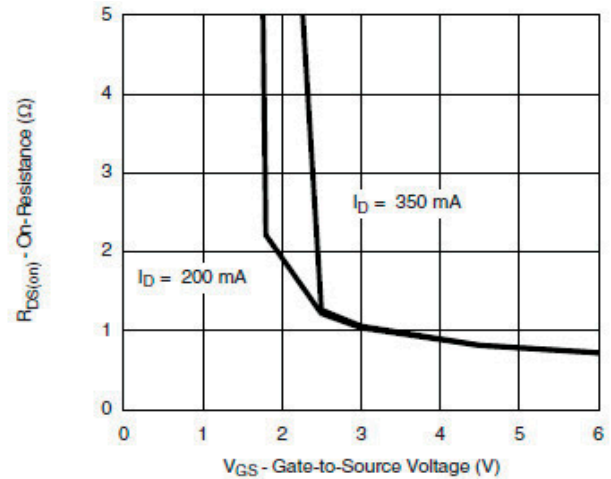
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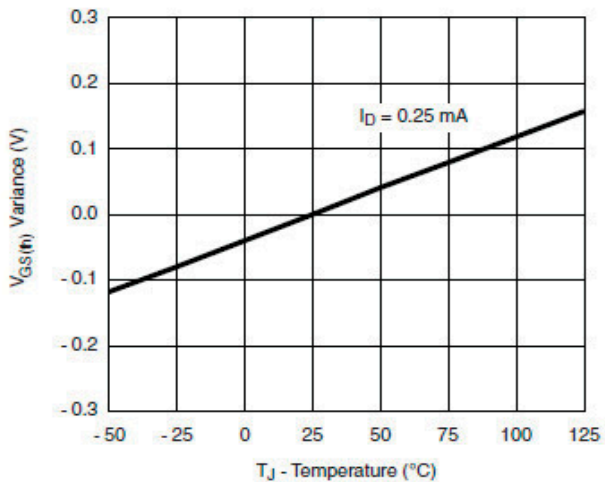
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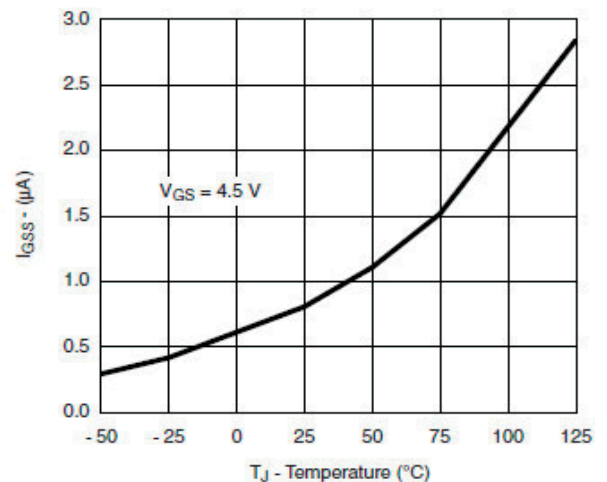
Source-Drain Diode Forward Voltage



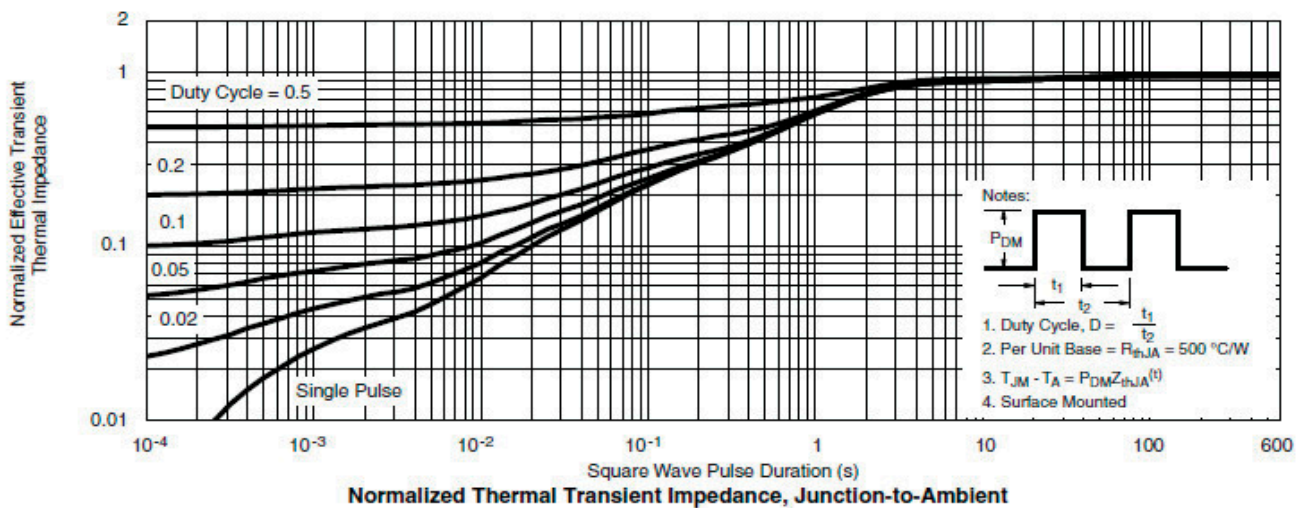
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage Variance vs. Temperature



$I_{GSS}$  vs. Temperature



Normalized Thermal Transient Impedance, Junction-to-Ambient

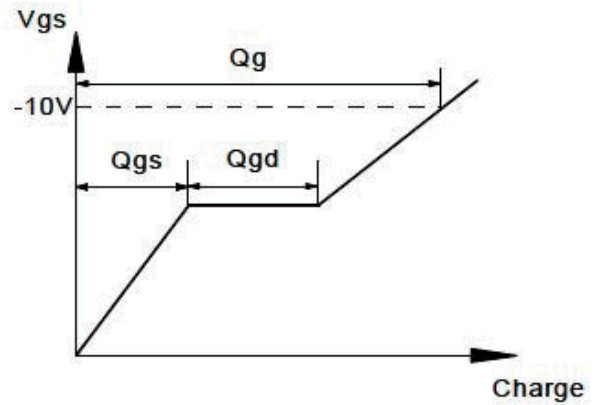
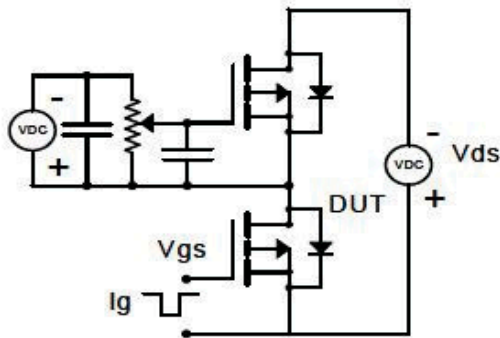
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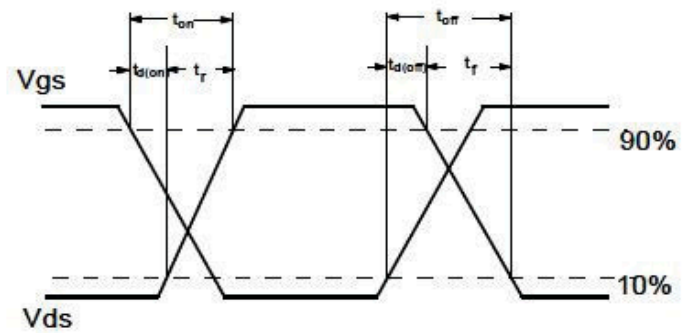
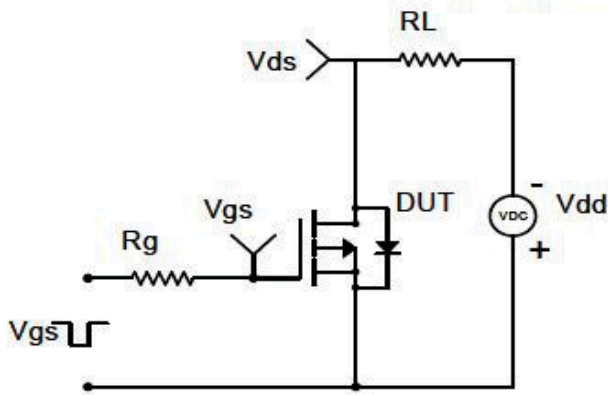
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## ■ Test circuit & waveform

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

