

# Single P-channel MOSFET

## ELM51033EA-S

<http://www.elm-tech.com>

### ■ General description

ELM51033EA-S uses advanced trench technology to provide excellent  $R_{ds(on)}$ , low gate charge and low gate threshold voltage. Internal ESD protection is included.

### ■ Features

- $V_{ds} = -30V$
- $I_d = -0.6A$
- $R_{ds(on)} = 900m\Omega$  ( $V_{gs} = -10V$ )
- $R_{ds(on)} = 1150m\Omega$  ( $V_{gs} = -4.5V$ )
- $R_{ds(on)} = 1450m\Omega$  ( $V_{gs} = -2.5V$ )
- ESD Protected

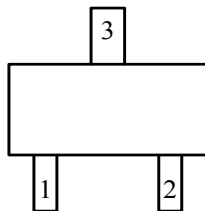
### ■ Maximum absolute ratings

$T_a = 25^\circ C$ . Unless otherwise noted.

Parameter	Symbol	Limit	Unit	
Drain-source voltage	$V_{ds}$	-30	V	
Gate-source voltage	$V_{gs}$	$\pm 12$	V	
Continuous drain current	$I_d$	$T_a = 25^\circ C$	-0.6	A
		$T_a = 70^\circ C$	-0.2	
Pulsed drain current	$I_{dm}$	-1.0	A	
Power dissipation	$P_d$	$T_c = 25^\circ C$	0.27	W
		$T_c = 70^\circ C$	0.16	
Junction and storage temperature range	$T_j, T_{stg}$	- 55 to 150	$^\circ C$	

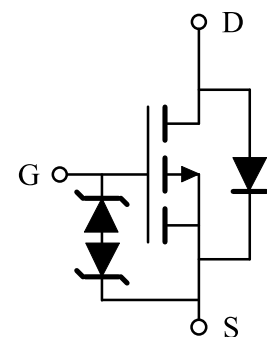
### ■ Pin configuration

SOT-523(TOP VIEW)



Pin No.	Pin name
1	GATE
2	SOURCE
3	DRAIN

### ■ Circuit



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### ■ Electrical characteristics

Ta=25°C. Unless otherwise noted.

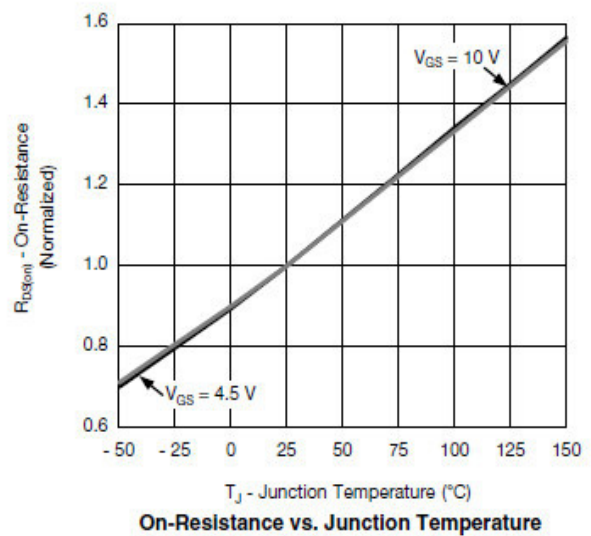
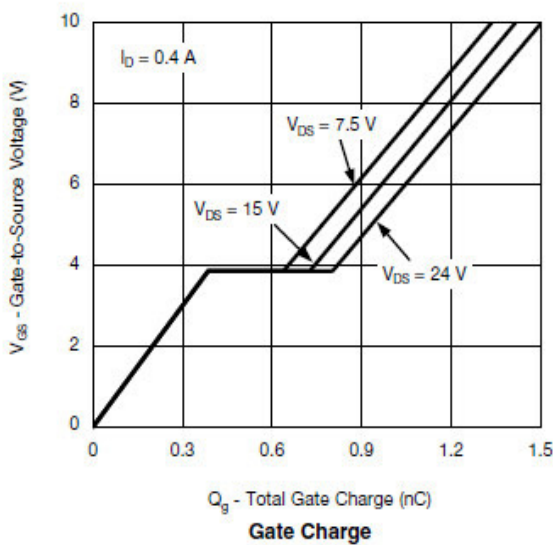
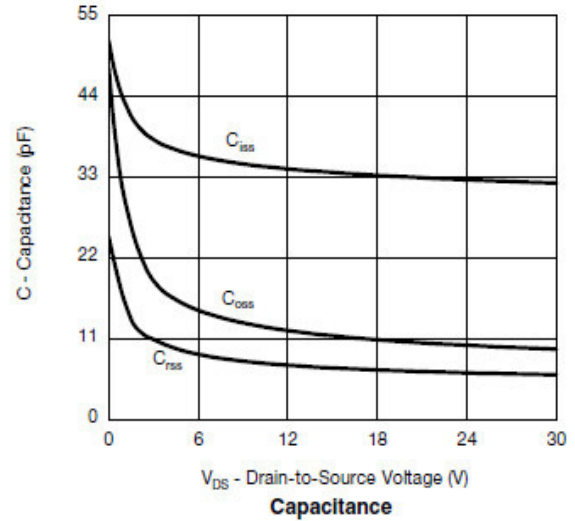
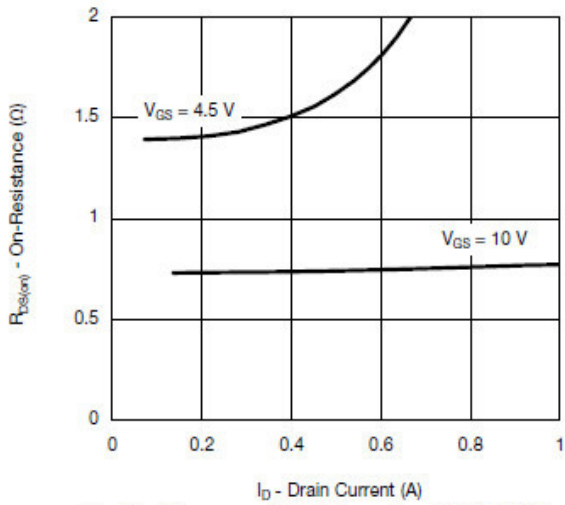
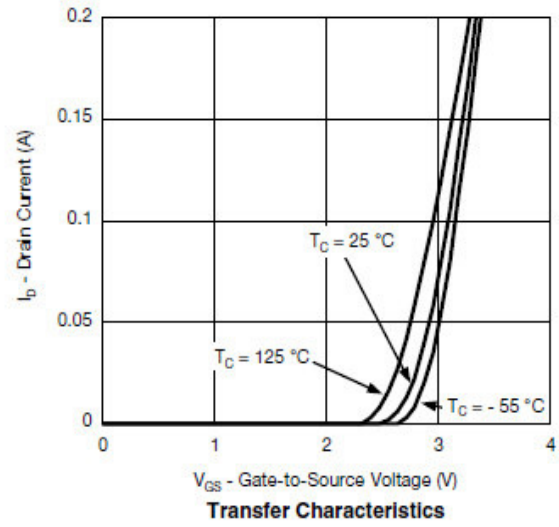
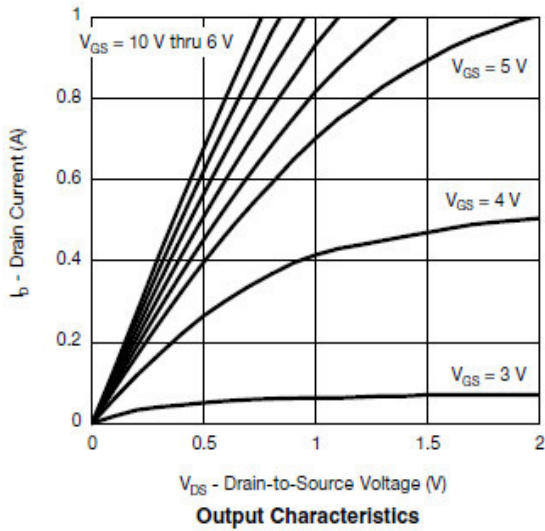
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>						
Drain-source breakdown voltage	BVdss	Vgs=0V, Id=-250μA	-30			V
Zero gate voltage drain current	Idss	Vds=-24V, Vgs=0V			-1	μA
		Vds=-24V, Vgs=0V, Ta=85°C			-5	
Gate-body leakage current	Igss	Vds=0V, Vgs=±12V			±5	mA
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=-250μA	-0.7		-1.5	V
On state drain current	Id(on)	Vgs=-4.5V, Vds≥-5V	-0.5			A
Static drain-source on-resistance	Rds(on)	Vgs=-10V, Id=-0.6A		560	900	mΩ
		Vgs=-4.5V, Id=-0.3A		730	1150	
		Vgs=-2.5V, Id=-0.2A		1000	1450	
Forward transconductance	Gfs	Vds=-15V, Id=-0.5A		1		S
Diode forward voltage	Vsd	Is=-0.3A, Vgs=0V		-0.65	-1.20	V
Max. body-diode continuous current	Is				-0.3	A
<b>DYNAMIC PARAMETERS</b>						
Input capacitance	Ciss	Vgs=0V, Vds=-15V, f=1MHz		34		pF
Output capacitance	Coss			12		pF
Reverse transfer capacitance	Crss			8		pF
<b>SWITCHING PARAMETERS</b>						
Total gate charge	Qg	Vgs=-4.5V, Vds=-15V Id=-0.4A		0.8	1.3	nC
Gate-source charge	Qgs			0.4		nC
Gate-drain charge	Qgd			0.4		nC
Turn-on delay time	td(on)	Vgs=-4.5V, Vds=-15V RL=38Ω, Id=-0.2A Rgen=1Ω		35	50	ns
Turn-on rise time	tr			20	30	ns
Turn-off delay time	td(off)			10	20	ns
Turn-off fall time	tf			10	20	ns

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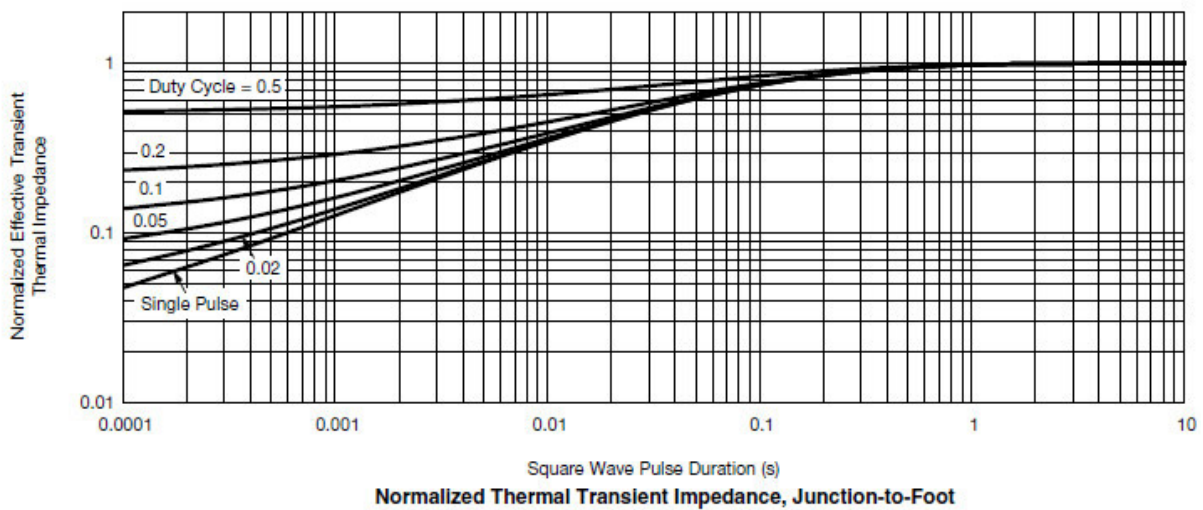
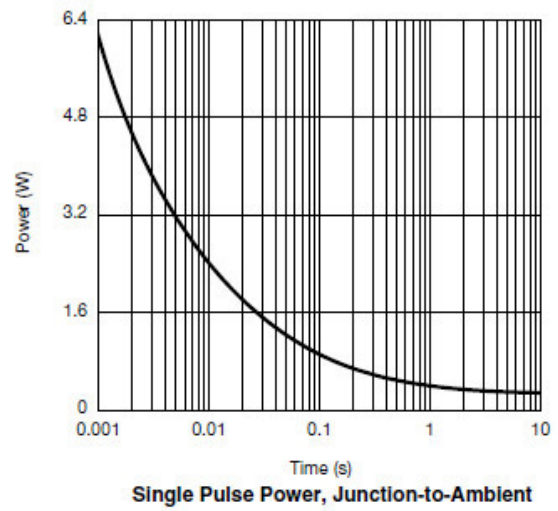
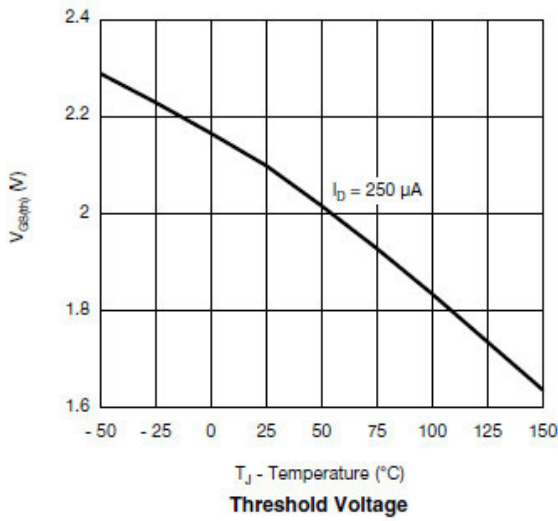
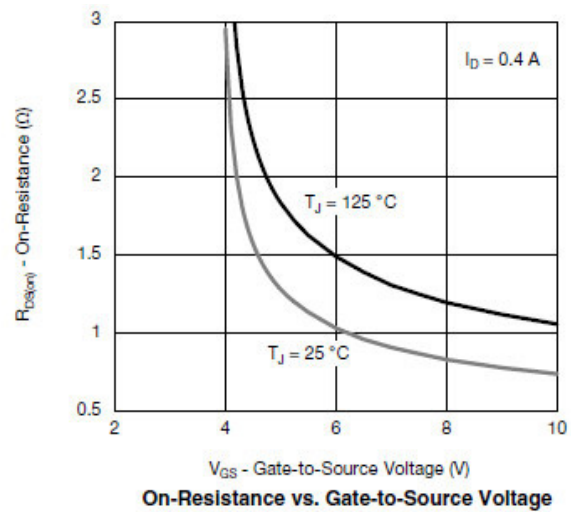
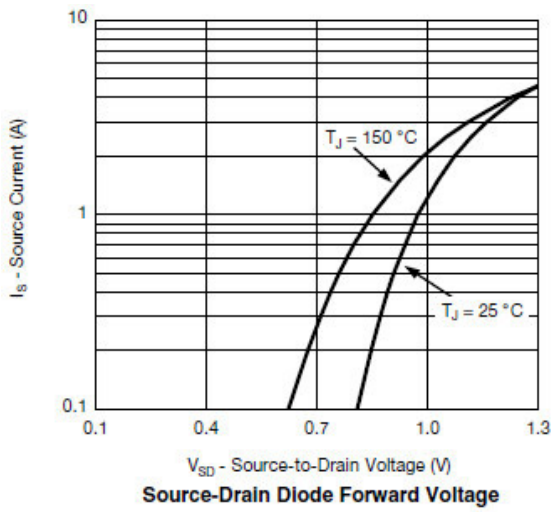
## Typical electrical and thermal characteristics



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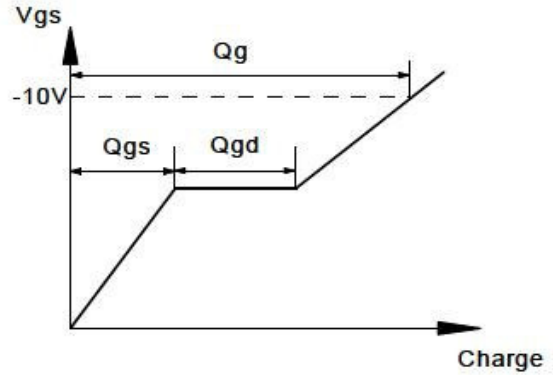
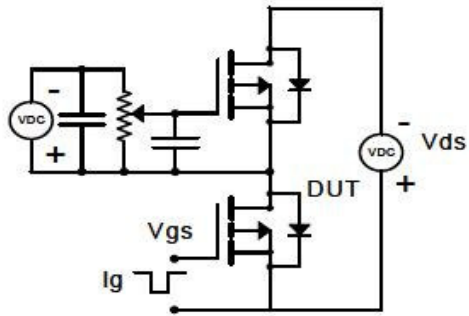
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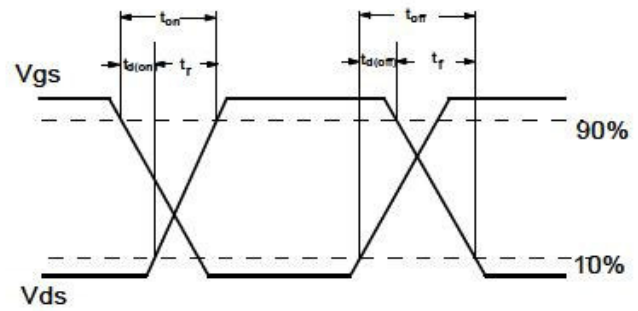
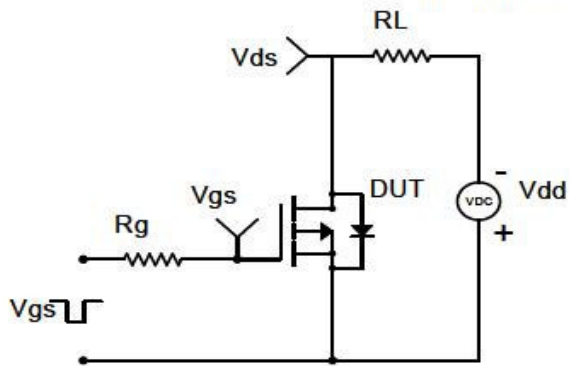
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## ■ Test circuit and waveform

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

