

Single P-channel MOSFET

ELM52431WSA-N

<http://www.elm-tech.com>

■ General description

ELM52431WSA-N uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate threshold voltage.

■ Features

- $V_{ds} = -20V$
- $I_d = -9.6A$
- $R_{ds(on)} = 25m\Omega$ ($V_{gs} = -4.5V$)
- $R_{ds(on)} = 33m\Omega$ ($V_{gs} = -2.5V$)
- $R_{ds(on)} = 42m\Omega$ ($V_{gs} = -1.8V$)

■ Maximum absolute ratings

$T_a = 25^\circ C$. Unless otherwise noted.

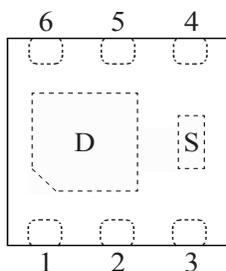
| Parameter | Symbol | Limit | Unit |
|--------------------------------|-----------|--------------------|------------|
| Drain-source voltage | V_{ds} | -20 | V |
| Gate-source voltage | V_{gs} | ± 12 | V |
| Continuous drain current | I_d | $T_a = 25^\circ C$ | -9.6 |
| | | $T_a = 70^\circ C$ | -7.7 |
| Pulsed drain current | I_{dm} | -30 | A |
| Power dissipation | P_d | $T_c = 25^\circ C$ | 2.5 |
| | | $T_c = 70^\circ C$ | 2.2 |
| Operating junction temperature | T_j | 150 | $^\circ C$ |
| Storage temperature range | T_{stg} | - 55 to 150 | $^\circ C$ |

■ Thermal characteristics

| Parameter | Symbol | Typ. | Max. | Unit |
|----------------------------------------|-----------------|------|------|--------------|
| Thermal resistance junction-to-ambient | $R_{\theta ja}$ | | 36 | $^\circ C/W$ |

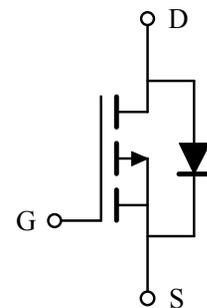
■ Pin configuration

DFN6-2x2(TOP VIEW)



| Pin No. | Pin name |
|---------|----------|
| 1 | DRAIN |
| 2 | DRAIN |
| 3 | GATE |
| 4 | SOURCE |
| 5 | DRAIN |
| 6 | DRAIN |

■ Circuit



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■ Electrical characteristics

Ta=25°C. Unless otherwise noted.

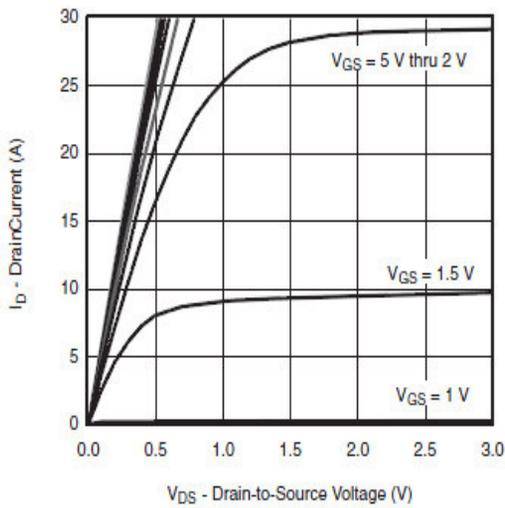
| Parameter | Symbol | Condition | Min. | Typ. | Max. | Unit |
|------------------------------------|---------------------|------------------------------------------------------------------------------------------------------------------------|------|-------|-------|------|
| STATIC PARAMETERS | | | | | | |
| Drain-source breakdown voltage | BV _{dss} | V _{gs} =0V, I _d =-250μA | -20 | | | V |
| Zero gate voltage drain current | I _{dss} | V _{ds} =-16V, V _{gs} =0V | | | -1 | μA |
| | | V _{ds} =-16V, V _{gs} =0V, T _a =85°C | | | -30 | |
| Gate-body leakage current | I _{gss} | V _{ds} =0V, V _{gs} =±12V | | | ±100 | nA |
| Gate threshold voltage | V _{gs(th)} | V _{ds} =V _{gs} , I _d =-250μA | -0.4 | | -0.7 | V |
| On state drain current | I _{d(on)} | V _{gs} =-4.5V, V _{ds} ≥-5V | -20 | | | A |
| Static drain-source on-resistance | R _{ds(on)} | V _{gs} =-4.5V, I _d =-3.0A | | 19 | 25 | mΩ |
| | | V _{gs} =-2.5V, I _d =-3.0A | | 25 | 33 | |
| | | V _{gs} =-1.8V, I _d =-2.5A | | 34 | 42 | |
| Forward transconductance | G _{fs} | V _{ds} =-10V, I _d =-6.5A | | 31 | | S |
| Diode forward voltage | V _{sd} | I _s =-1.0A, V _{gs} =0V | | -0.75 | -1.30 | V |
| Max. body-diode continuous current | I _s | | | | -2.9 | A |
| DYNAMIC PARAMETERS | | | | | | |
| Input capacitance | C _{iss} | V _{gs} =0V, V _{ds} =-10V, f=1MHz | | 1700 | | pF |
| Output capacitance | C _{oss} | | | 350 | | pF |
| Reverse transfer capacitance | C _{rss} | | | 250 | | pF |
| SWITCHING PARAMETERS | | | | | | |
| Total gate charge | Q _g | V _{gs} =-4.5V, V _{ds} =-10V I _d ≐-9.6A | | 24 | 50 | nC |
| Gate-source charge | Q _{gs} | | | 3 | | nC |
| Gate-drain charge | Q _{gd} | | | 6 | | nC |
| Turn-on delay time | t _{d(on)} | V _{gs} =-4.5V, V _{ds} =-10V R _L =1.3Ω, I _d ≐-7.7A R _{gen} =1.0Ω | | 25 | 45 | ns |
| Turn-on rise time | t _r | | | 30 | 55 | ns |
| Turn-off delay time | t _{d(off)} | | | 60 | 100 | ns |
| Turn-off fall time | t _f | | | 25 | 40 | ns |

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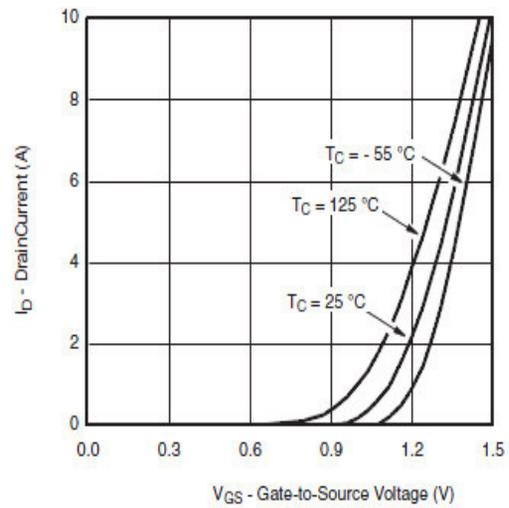
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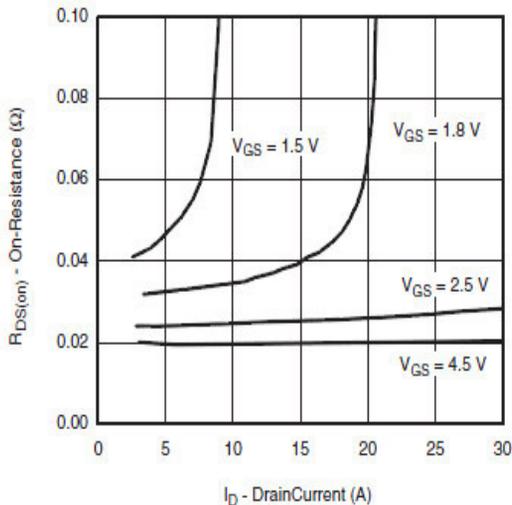
■ Typical electrical and thermal characteristics



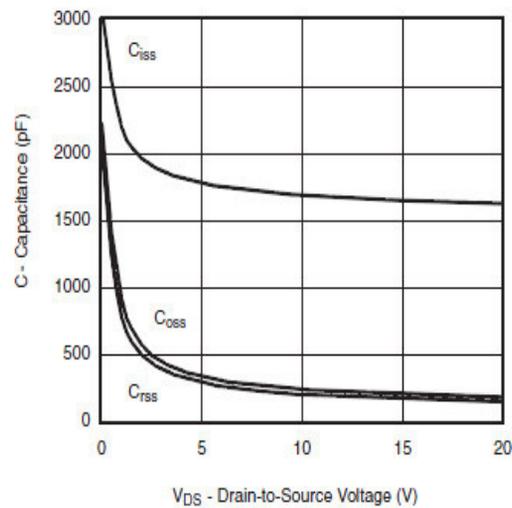
Output Characteristics



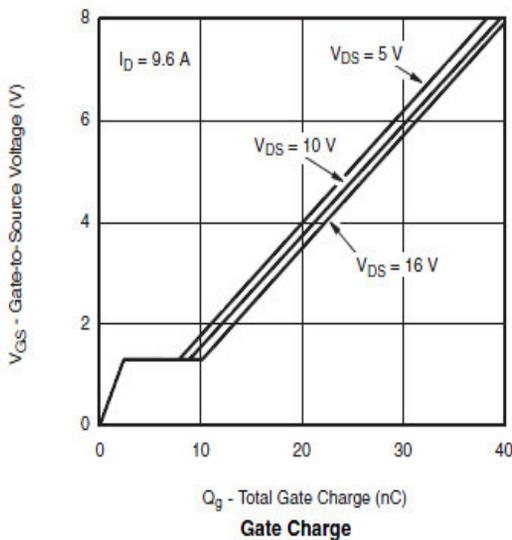
Transfer Characteristics



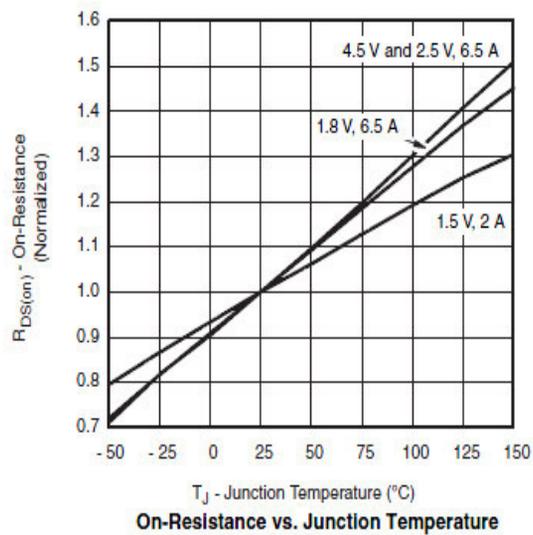
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



Gate Charge

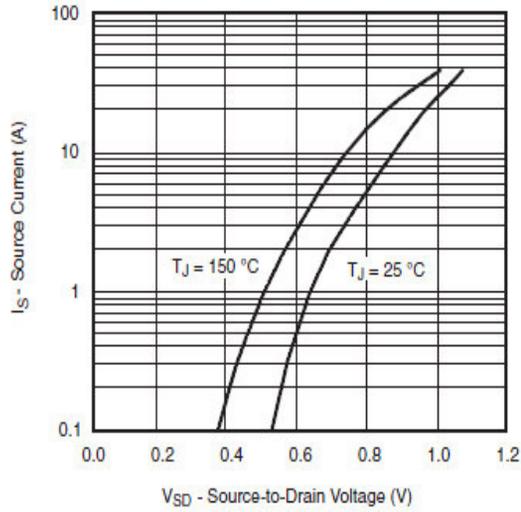


On-Resistance vs. Junction Temperature

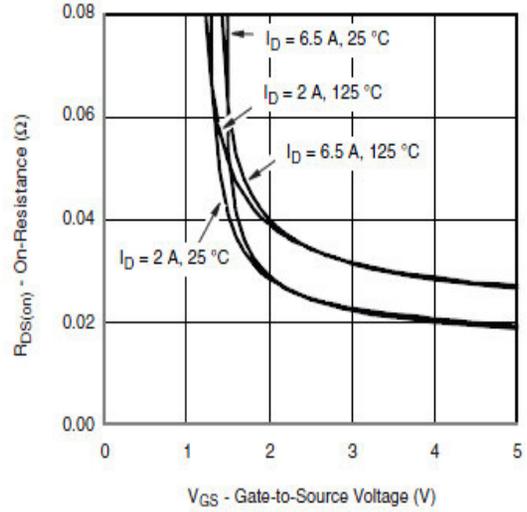
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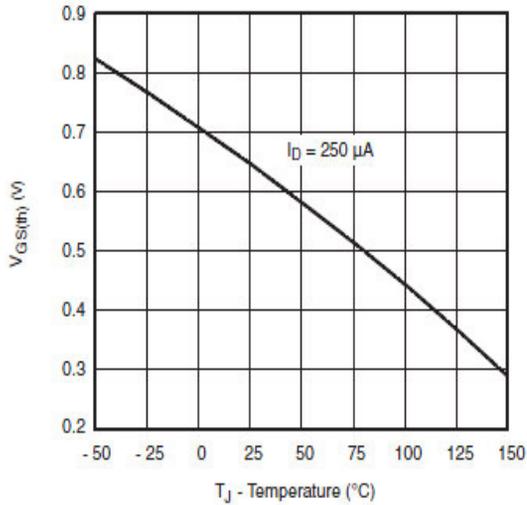
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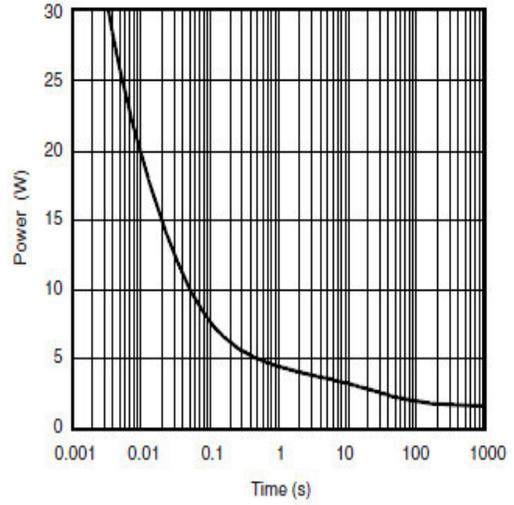
Source-Drain Diode Forward Voltage



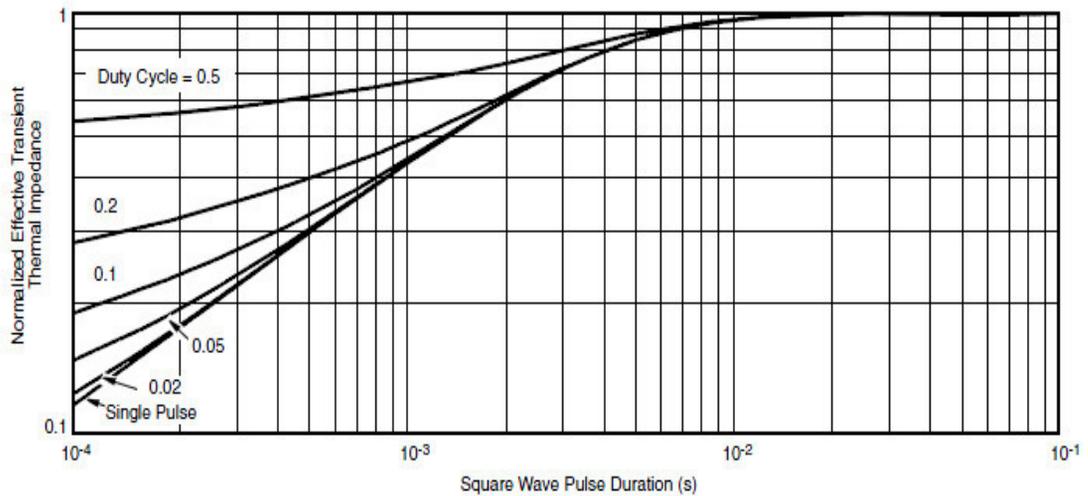
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

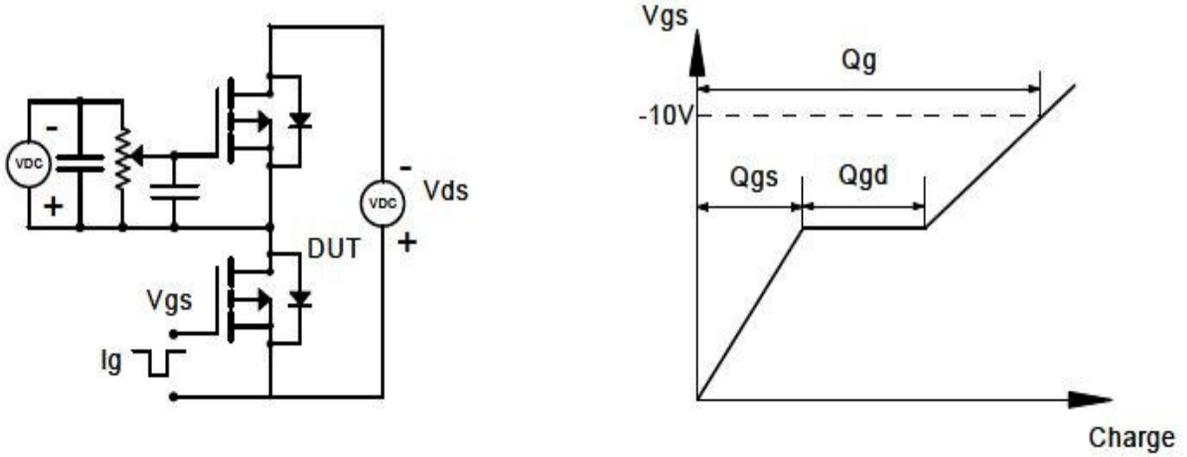
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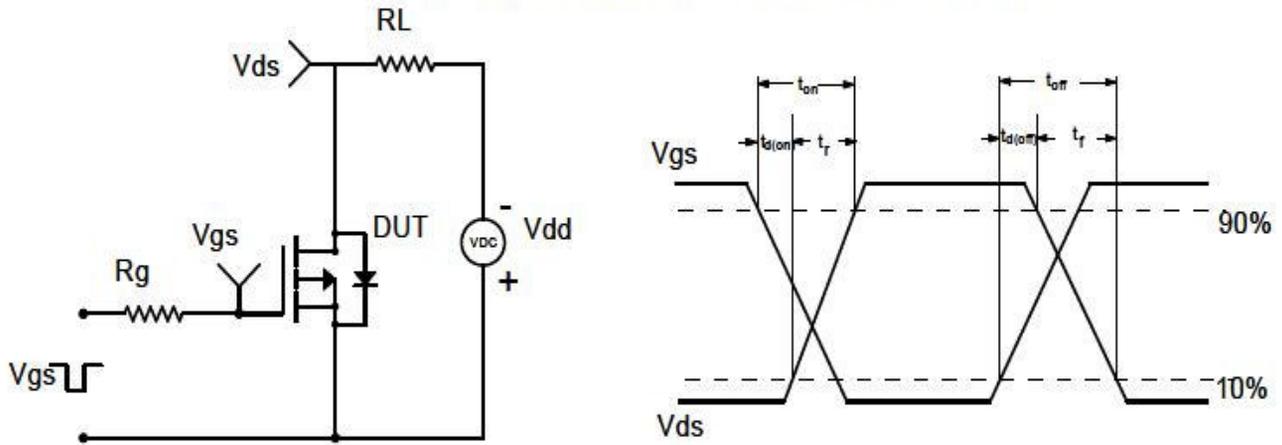
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■ Test circuit and waveform

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

