

# Dual P-channel MOSFET

## ELM53911WA-N

<http://www.elm-tech.com>

### ■ General description

ELM53911WA-N uses advanced trench technology to provide excellent  $R_{ds(on)}$  and low gate charge.

### ■ Features

- $V_{ds} = -30V$
- $I_d = -4.3A$
- $R_{ds(on)} = 68m\Omega$  ( $V_{gs} = -10V$ )
- $R_{ds(on)} = 88m\Omega$  ( $V_{gs} = -4.5V$ )

### ■ Maximum absolute ratings

$T_a = 25^\circ C$ . Unless otherwise noted.

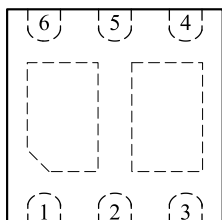
Parameter	Symbol	Limit	Unit
Drain-source voltage	$V_{ds}$	-30	V
Gate-source voltage	$V_{gs}$	$\pm 20$	V
Continuous drain current ( $T_j = 150^\circ C$ )	Id	$T_a = 25^\circ C$	-4.3
		$T_a = 70^\circ C$	-3.4
Pulsed drain current	$I_{dm}$	-28	A
Power dissipation	Pd	$T_c = 25^\circ C$	7.8
		$T_c = 70^\circ C$	5.0
Operating junction temperature	$T_j$	150	$^\circ C$
Storage temperature range	$T_{stg}$	-55 to 150	$^\circ C$

### ■ Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit
Thermal resistance junction-to-ambient	$R_{\theta ja}$		120	$^\circ C/W$

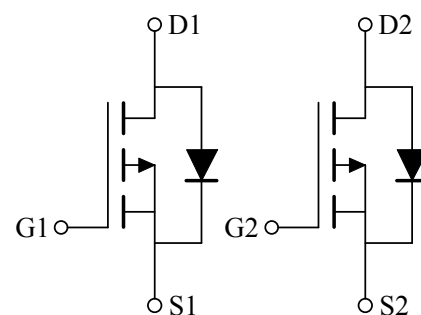
### ■ Pin configuration

DFN6-2x2(TOP VIEW)



Pin No.	Pin name
1	SOURCE1
2	GATE1
3	DRAIN2
4	SOURCE2
5	GATE2
6	DRAIN1

### ■ Circuit



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### ■ Electrical characteristics

Ta=25°C. Unless otherwise noted.

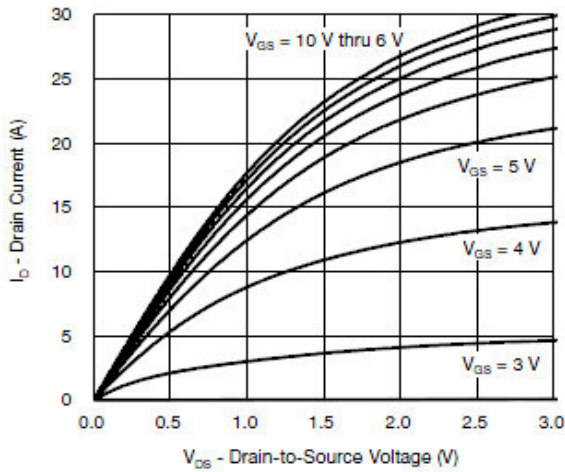
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>						
Drain-source breakdown voltage	BVdss	Id=-250μA, Vgs=0V	-30			V
Zero gate voltage drain current	Idss	Vds=-24V, Vgs=0V Ta=85°C			-1	μA
					-30	
Gate-body leakage current	Igss	Vds=0V, Vgs=±20V			±100	nA
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=-250μA	-1.0		-2.0	V
On state drain current	Id(on)	Vgs=-10V, Vds≥-5V	-10			A
Static drain-source on-resistance	Rds(on)	Vgs=-10V, Id=-3A		55	68	mΩ
		Vgs=-4.5V, Id=-2A		75	88	
Forward transconductance	Gfs	Vds=-15V, Id=-3A		8		S
Diode forward voltage	Vsd	Is=-3A, Vgs=0V		-0.75	-1.30	V
Max. body-diode continuous current	Is				-1.6	A
<b>DYNAMIC PARAMETERS</b>						
Input capacitance	Ciss	Vgs=0V, Vds=-15V, f=1MHz		450		pF
Output capacitance	Coss			56		pF
Reverse transfer capacitance	Crss			46		pF
<b>SWITCHING PARAMETERS</b>						
Total gate charge	Qg	Vgs=-4.5V, Vds=-10V Id≐-4.0A		4.2	7.2	nC
Gate-source charge	Qgs			1.3		nC
Gate-drain charge	Qgd			1.6		nC
Turn-on delay time	td(on)	Vgs=-10V, Vds=-15V Id≐-3A, RL=5Ω Rgen=1Ω		10	20	ns
Turn-on rise time	tr			5	10	ns
Turn-off delay time	td(off)			20	40	ns
Turn-off fall time	tf			5	10	ns

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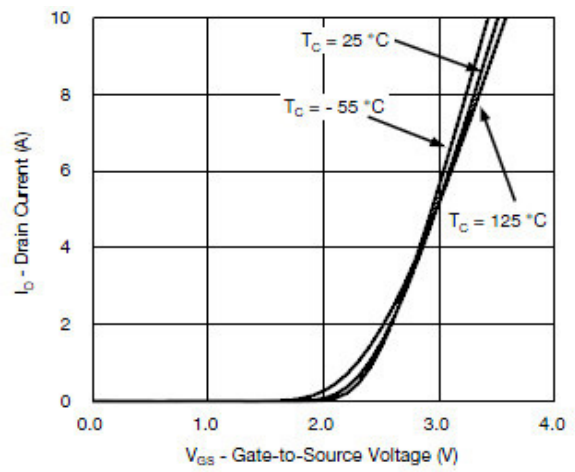
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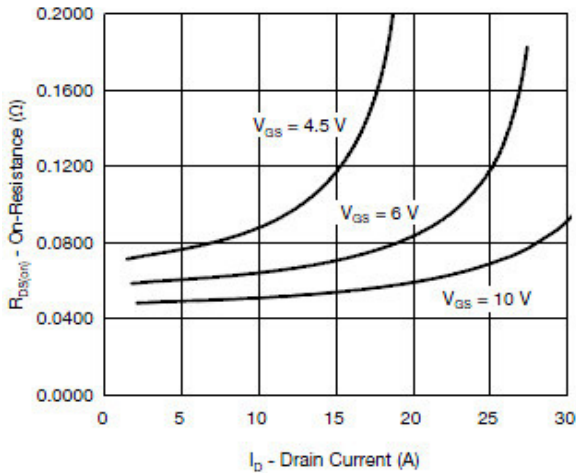
## ■ Typical electrical and thermal characteristics



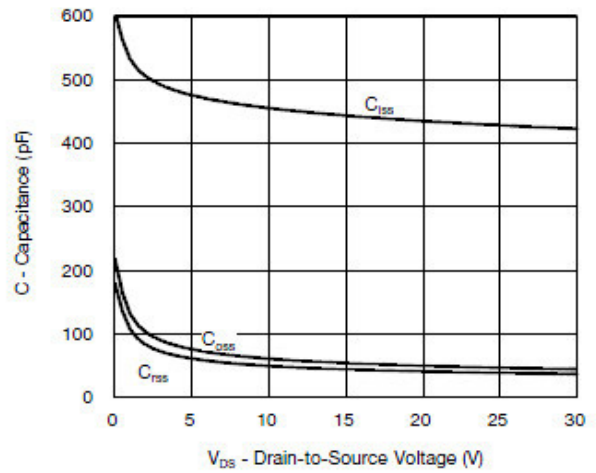
Output Characteristics



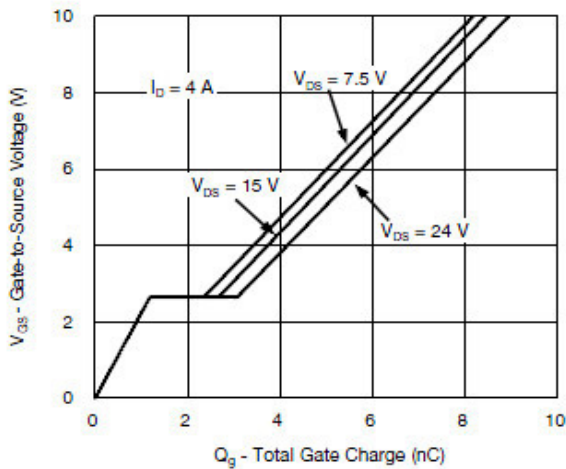
Transfer Characteristics



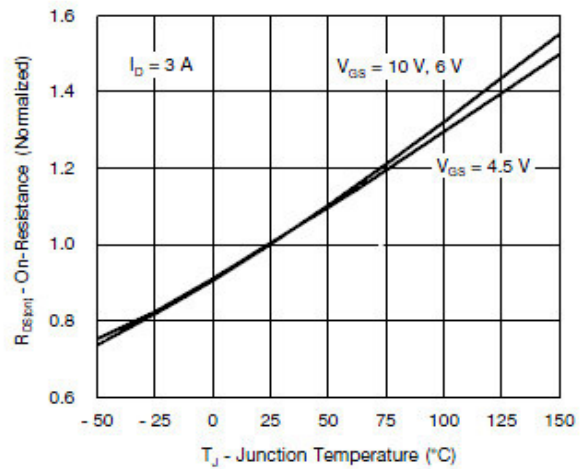
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



Gate Charge

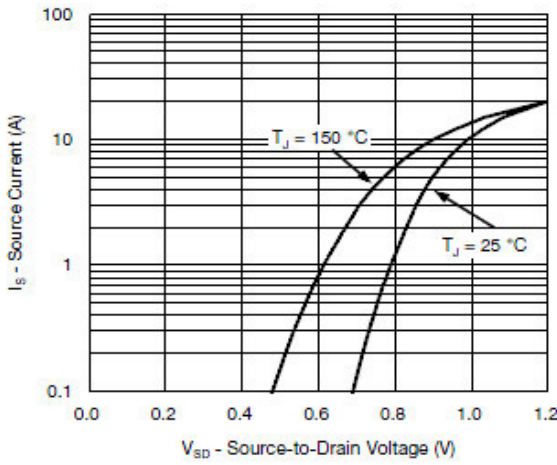


On-Resistance vs. Junction Temperature

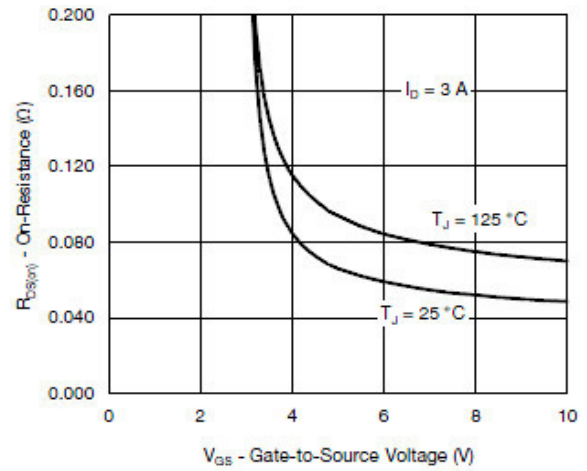
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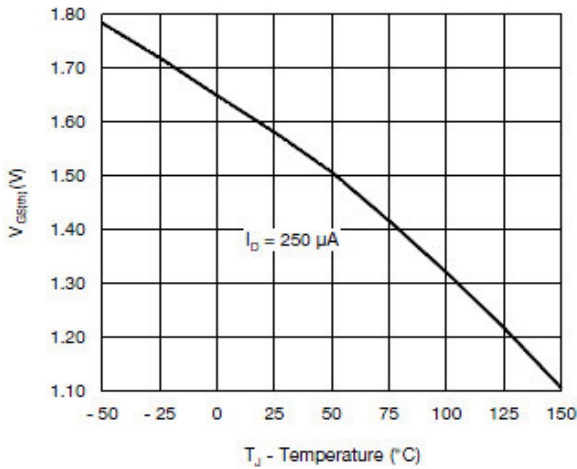
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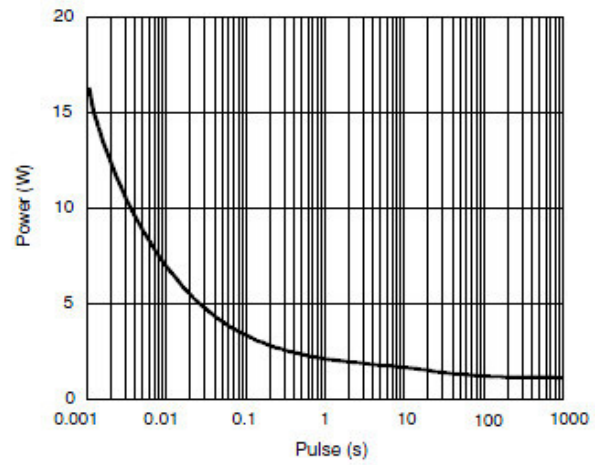
Source-Drain Diode Forward Voltage



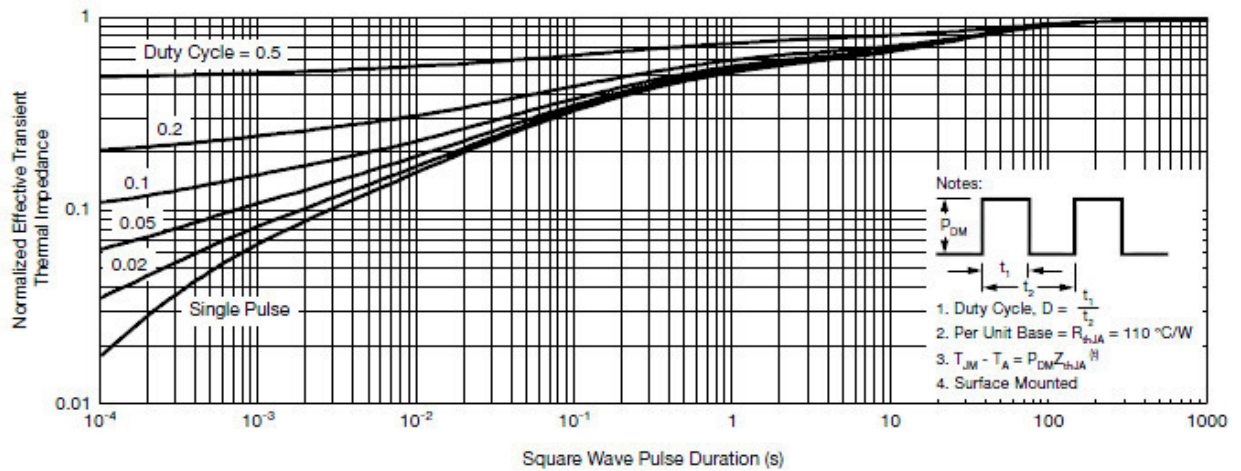
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient

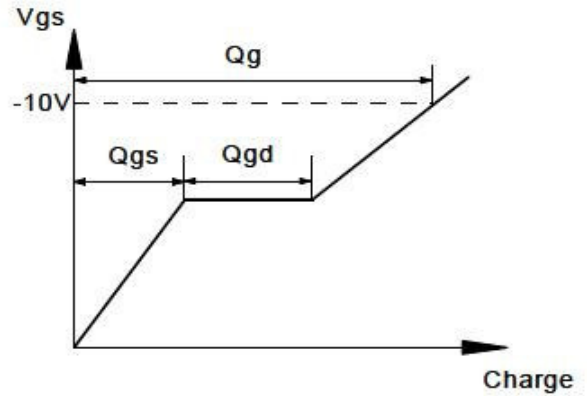
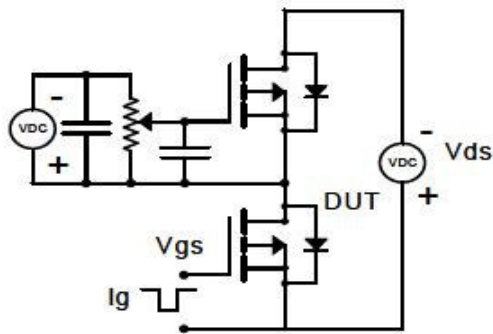
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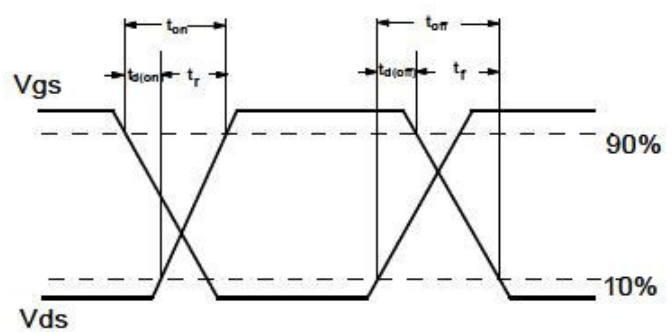
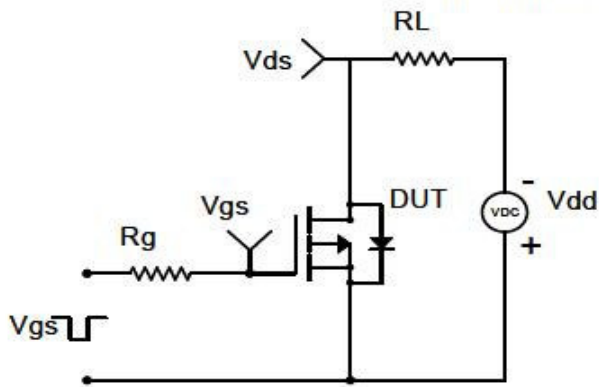
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## ■ Test circuit & waveform

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

