

# Dual N-channel MOSFET

## ELM54922WA-N

<http://www.elm-tech.com>

### ■ General description

ELM54922WA-N uses advanced trench technology to provide excellent  $R_{ds(on)}$  and low gate charge.

### ■ Features

- $V_{ds}=100V$
- $I_d=2.0A$
- $R_{ds(on)} = 290m\Omega$  ( $V_{gs}=10V$ )
- $R_{ds(on)} = 300m\Omega$  ( $V_{gs}=4.5V$ )

### ■ Maximum absolute ratings

$T_a=25^\circ C$ . Unless otherwise noted.

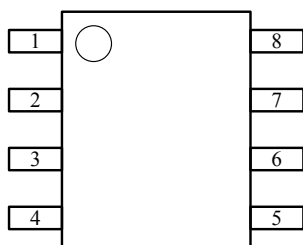
Parameter	Symbol	Limit	Unit
Drain-source voltage	$V_{ds}$	100	V
Gate-source voltage	$V_{gs}$	$\pm 20$	V
Continuous drain current( $T_j=150^\circ C$ )	$I_d$	$T_a=25^\circ C$	2.0
		$T_a=70^\circ C$	1.5
Pulsed drain current	$I_{dm}$	8	A
Power dissipation	$P_d$	$T_c=25^\circ C$	2.8
		$T_c=70^\circ C$	1.8
Operating junction temperature	$T_j$	150	$^\circ C$
Storage temperature range	$T_{stg}$	-55 to 150	$^\circ C$

### ■ Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit
Thermal resistance junction-to-ambient	$R_{\theta ja}$		62.5	$^\circ C/W$

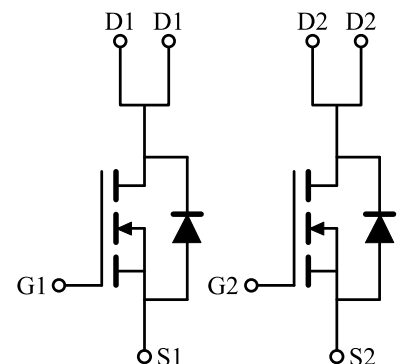
### ■ Pin configuration

SOP-8(TOP VIEW)



Pin No.	Pin name
1	SOURCE1
2	GATE1
3	SOURCE2
4	GATE2
5	DRAIN2
6	DRAIN2
7	DRAIN1
8	DRAIN1

### ■ Circuit



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### ■ Electrical characteristics

Ta=25°C. Unless otherwise noted.

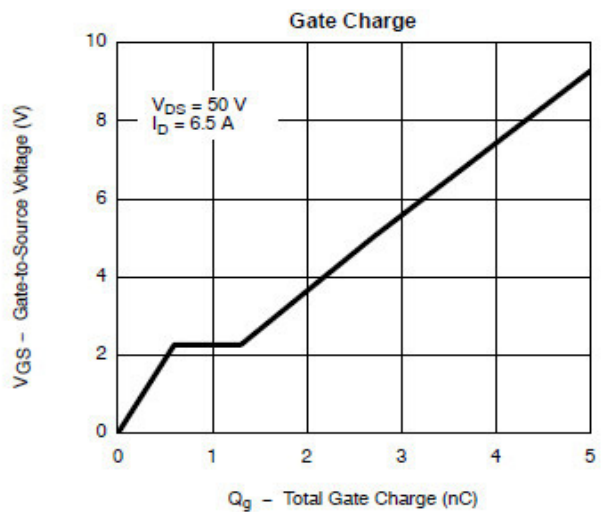
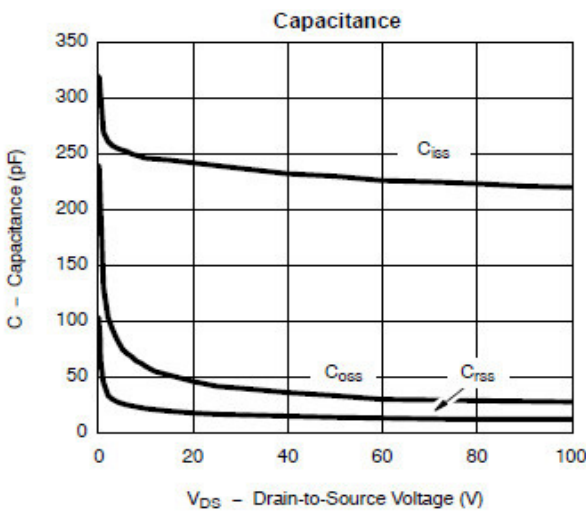
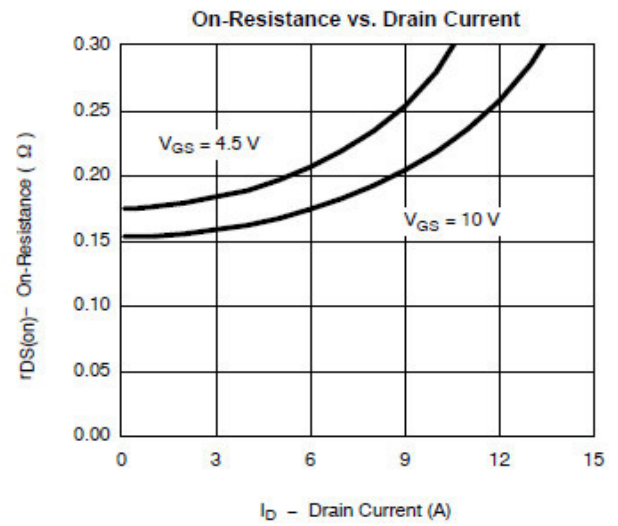
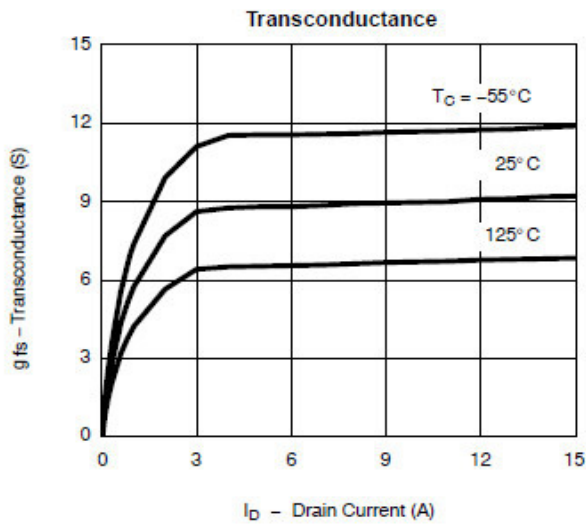
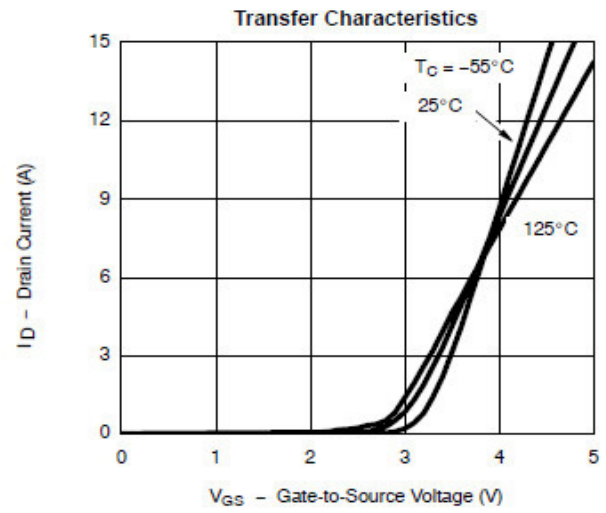
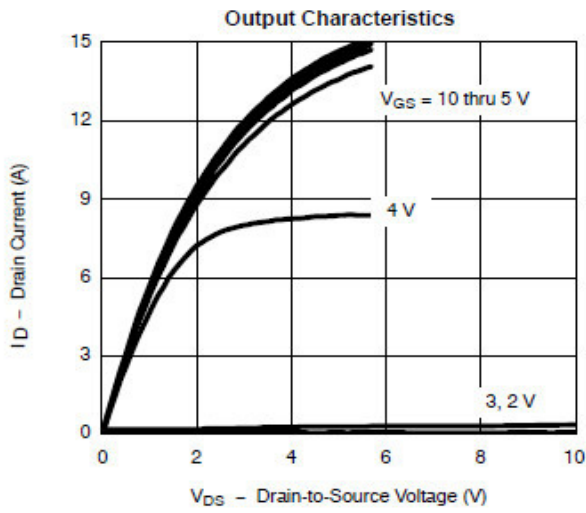
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
<b>STATIC PARAMETERS</b>						
Drain-source breakdown voltage	BVdss	Id=250μA, Vgs=0V	100			V
Zero gate voltage drain current	Idss	Vds=80V Vgs=0V			1	μA
		Ta=85°C			5	
Gate-body leakage current	Igss	Vds=0V, Vgs=±20V			±100	nA
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=250μA	1.0		2.0	V
On state drain current	Id(on)	Vgs=4.5V, Vds≥5V	8			A
Static drain-source on-resistance	Rds(on)	Vgs=10V, Id=2.0A		265	290	mΩ
		Vgs=4.5V, Id=1.5A		275	300	
Forward transconductance	Gfs	Vds=15V, Id=1.5A		8.5		S
Diode forward voltage	Vsd	Is=1.5A, Vgs=0V		0.8	1.3	V
Max. body-diode continuous current	Is				1.5	A
<b>DYNAMIC PARAMETERS</b>						
Input capacitance	Ciss	Vgs=0V, Vds=25V, f=1MHz		250		pF
Output capacitance	Coss			40		pF
Reverse transfer capacitance	Crss			20		pF
<b>SWITCHING PARAMETERS</b>						
Total gate charge	Qg	Vgs=5.0V, Vds=50V Id≐3.0A		2.7	5.0	nC
Gate-source charge	Qgs			0.7		nC
Gate-drain charge	Qgd			0.7		nC
Turn-on delay time	td(on)	Vgs=10V, Vds=50V, Id≐3.0A RL=7.5Ω, Rgen=2.5Ω		7	12	ns
Turn-on rise time	tr			8	15	ns
Turn-off delay time	td(off)			8	15	ns
Turn-off fall time	tf			10	18	ns

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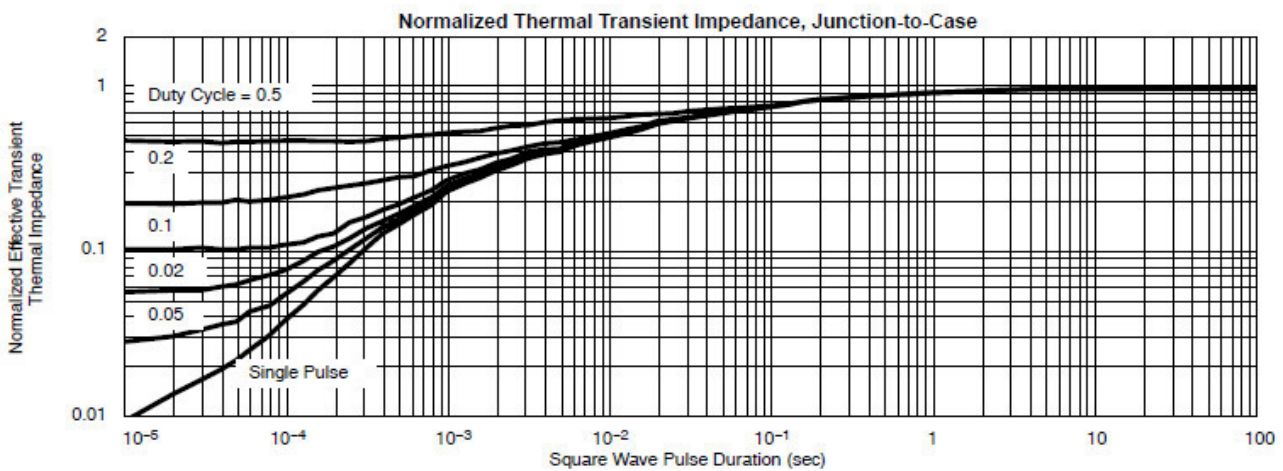
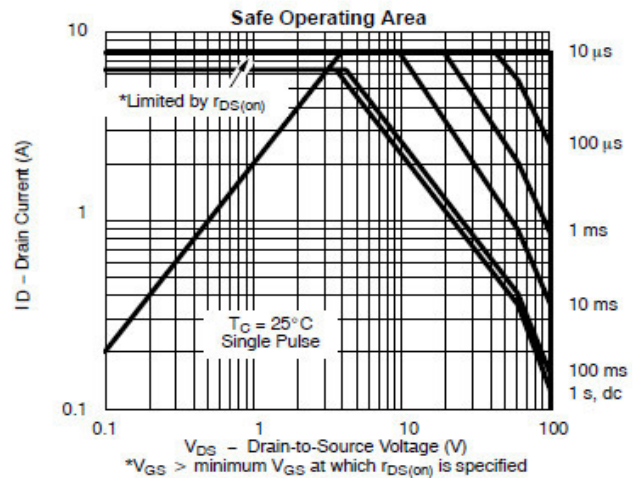
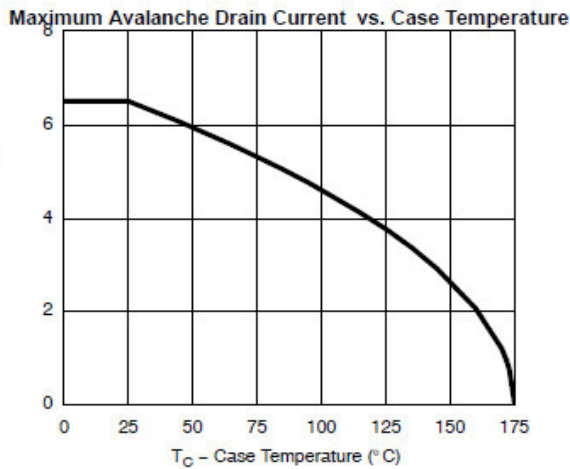
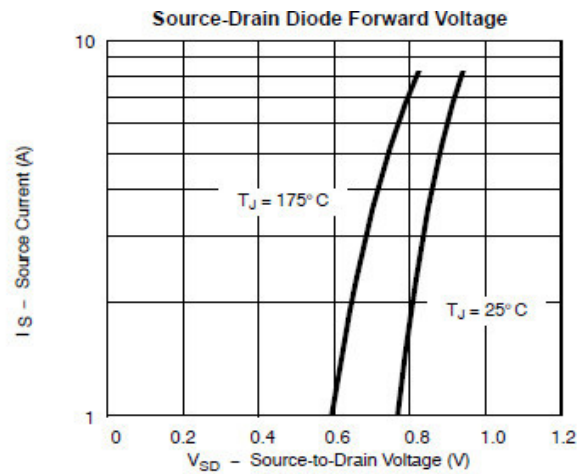
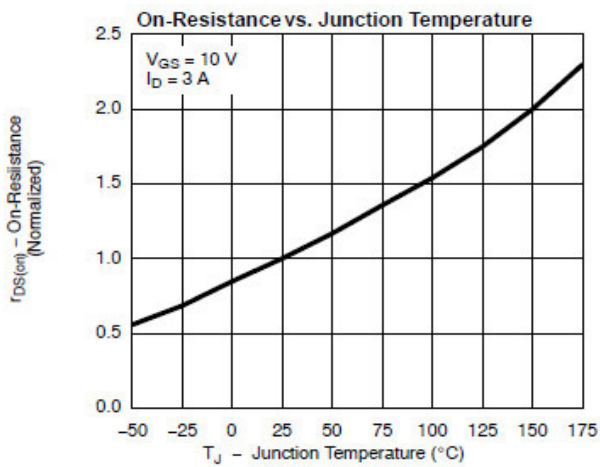
## ■ Typical electrical and thermal characteristics



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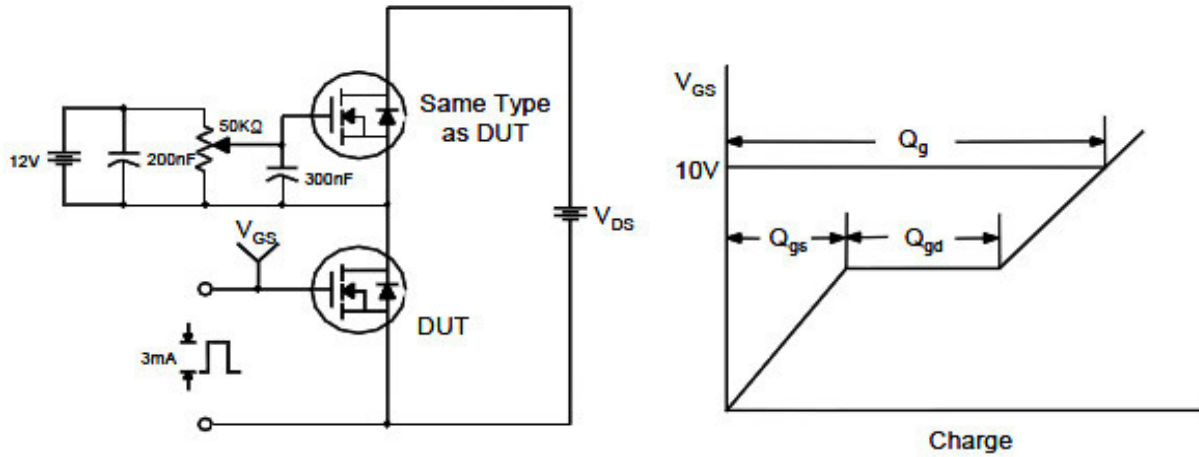
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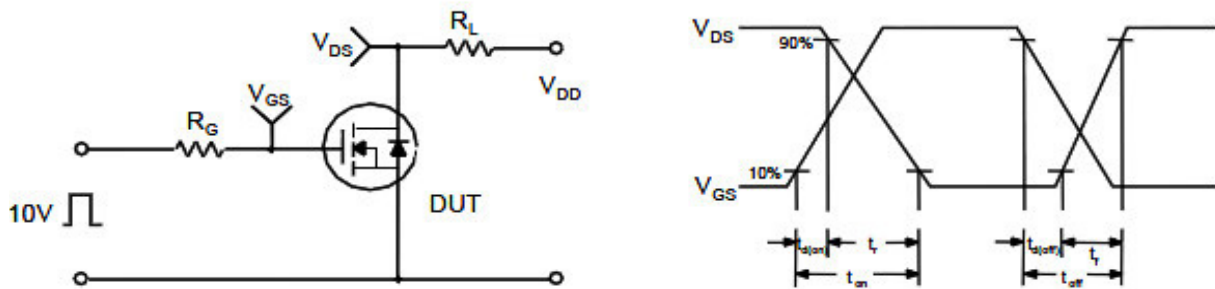
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## ■ Test circuit and waveform

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

