

Single P-channel MOSFET

ELM550101SA-S

<http://www.elm-tech.com>

■General description

ELM550101SA-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate threshold voltage.

■Features

- $V_{ds} = -100V$
- $I_d = -35A$
- $R_{ds(on)} = 46m\Omega$ ($V_{gs} = -10V$)
- $R_{ds(on)} = 52m\Omega$ ($V_{gs} = -4.5V$)

■Maximum absolute ratings

$T_a = 25^\circ C$. Unless otherwise noted.

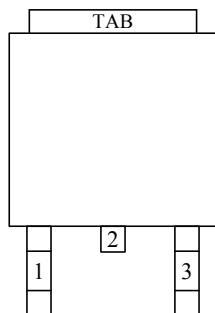
Parameter	Symbol	Limit	Unit
Drain-source voltage	V_{ds}	-100	V
Gate-source voltage	V_{gs}	± 20	V
Continuous drain current	I_d	-35	A
		-25	
Pulsed drain current	I_{dm}	-40	A
Single pulse avalanche current	I_{as}	-30	A
Avalanche energy	E_{as}	58	mJ
Power dissipation	P_d	40	W
		15	
Operating junction temperature	T_j	150	$^\circ C$
Storage temperature range	T_{stg}	-55 to 150	$^\circ C$

■Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit
Thermal resistance junction-to-ambient	$R_{\theta ja}$		62.5	$^\circ C/W$

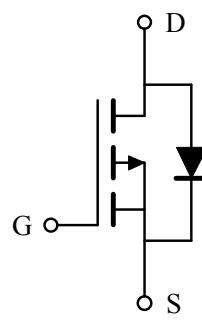
■Pin configuration

TO-252-3(TOP VIEW)



Pin No.	Pin name
1	GATE
2	DRAIN
3	SOURCE

■Circuit



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■Electrical characteristics

Ta=25°C. Unless otherwise noted.

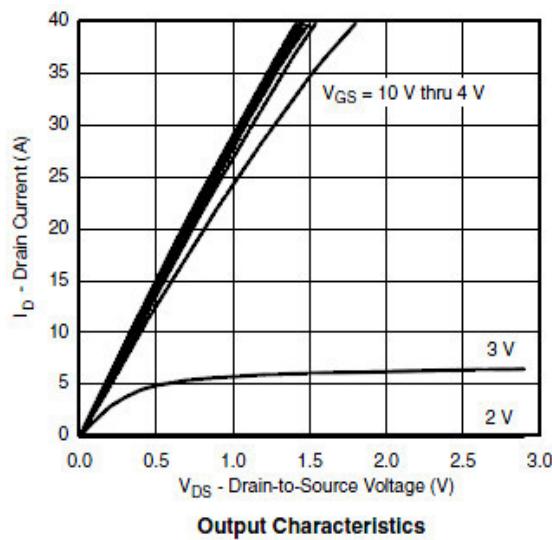
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	BVDss	Vgs=0V, Id=-250µA	-100			V
Zero gate voltage drain current	Idss	Vds=-80V, Vgs=0V		-1		µA
		Vds=-80V, Vgs=0V, Ta=85°C		-30		
Gate-body leakage current	Igss	Vds=0V, Vgs=±20V		±100		nA
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=-250µA	-1.0		-2.5	V
On state drain current	Id(on)	Vgs=-10V, Vds≥-10V	-35			A
Static drain-source on-resistance	Rds(on)	Vgs=-10V, Id=-18A		38	46	mΩ
		Vgs=-4.5V, Id=-10A		42	52	
Forward transconductance	Gfs	Vds=-15V, Id=-9.2A		38		S
Diode forward voltage	Vsd	Is=-2A, Vgs=0V		-0.8	-1.3	V
Max. body-diode continuous current	Is				-5.8	A
DYNAMIC PARAMETERS						
Input capacitance	Ciss	Vgs=0V, Vds=-50V, f=1MHz		4200		pF
Output capacitance	Coss			210		pF
Reverse transfer capacitance	Crss			165		pF
SWITCHING PARAMETERS						
Total gate charge	Qg	Vgs=-4.5V, Vds=-50V Id=-10A		50	80	nC
Gate-source charge	Qgs			15		nC
Gate-drain charge	Qgd			25		nC
Turn-on delay time	td(on)	Vgs=-10V, Vds=-50V RL=6.5Ω, Id=-10A Rgen=1Ω		15	30	ns
Turn-on rise time	tr			20	45	ns
Turn-off delay time	td(off)			100	180	ns
Turn-off fall time	tf			90	170	ns

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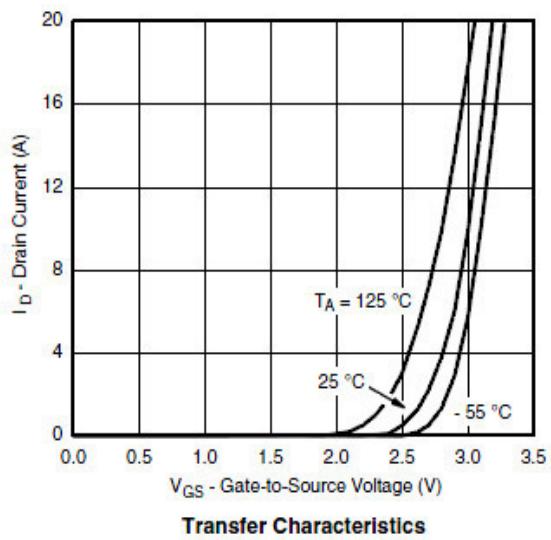
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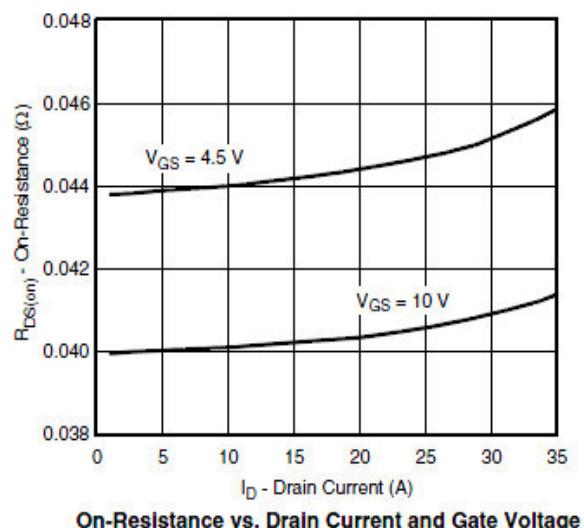
■ Typical electrical and thermal characteristics



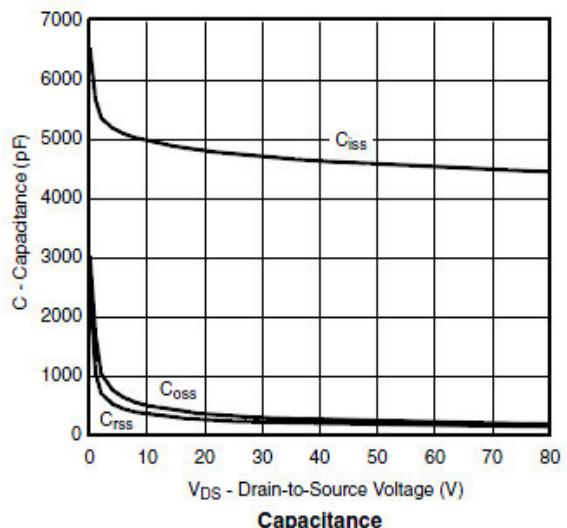
Output Characteristics



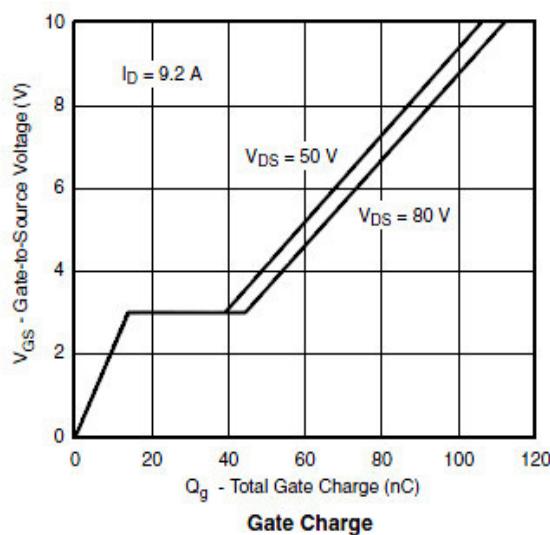
Transfer Characteristics



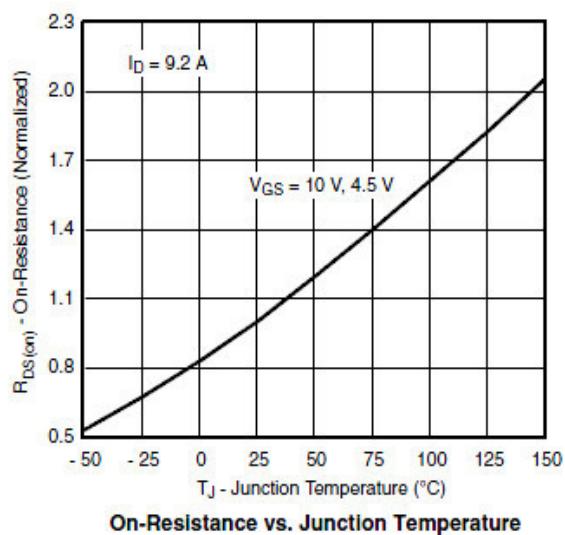
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



Gate Charge

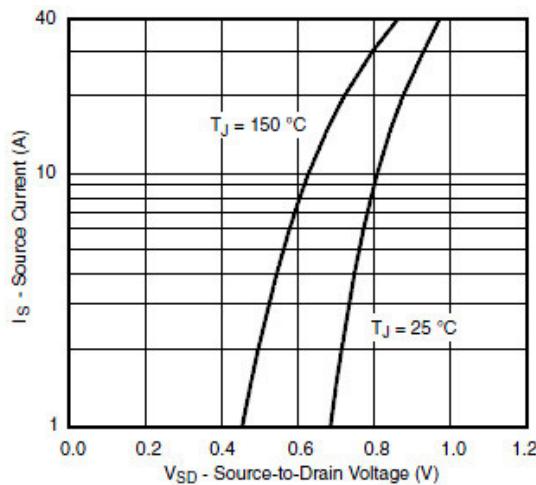


On-Resistance vs. Junction Temperature

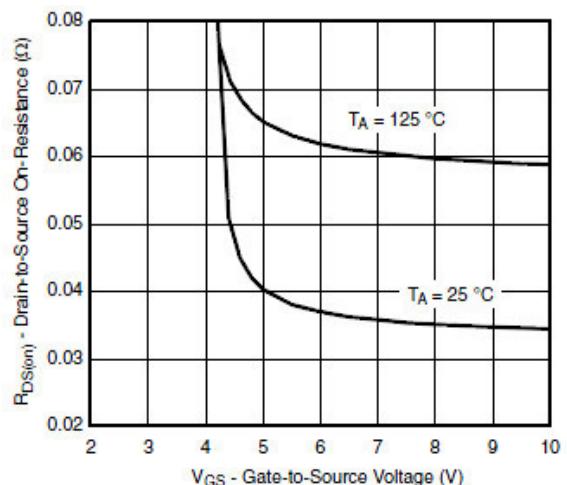
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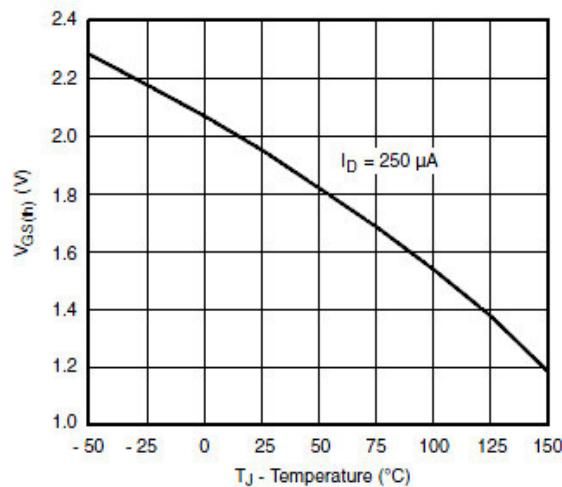
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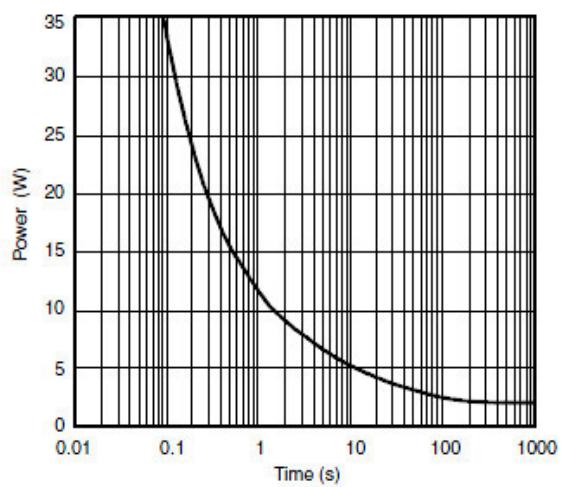
Source-Drain Diode Forward Voltage



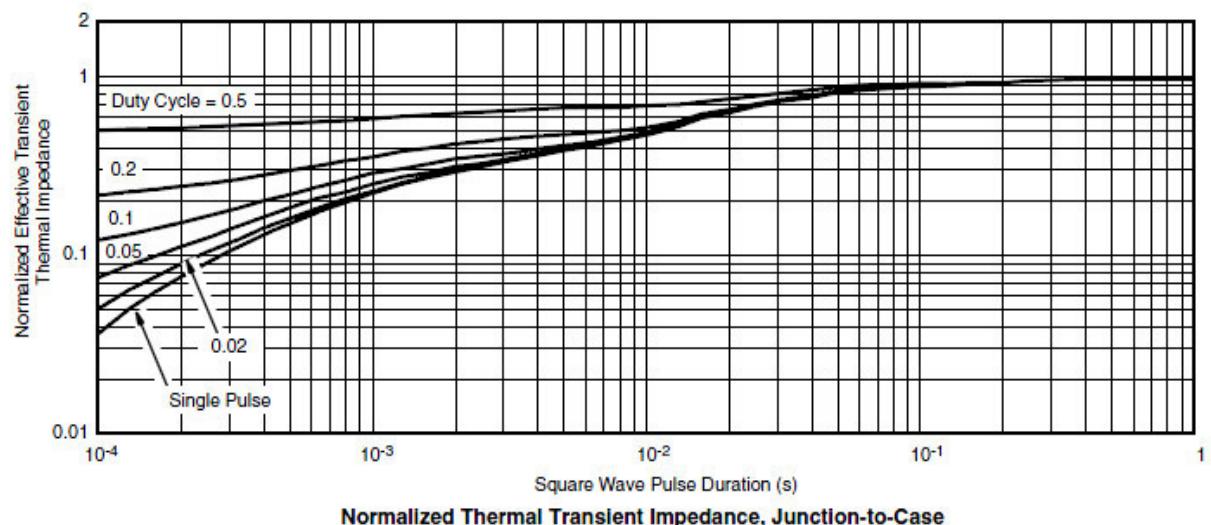
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



Single Pulse Power, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

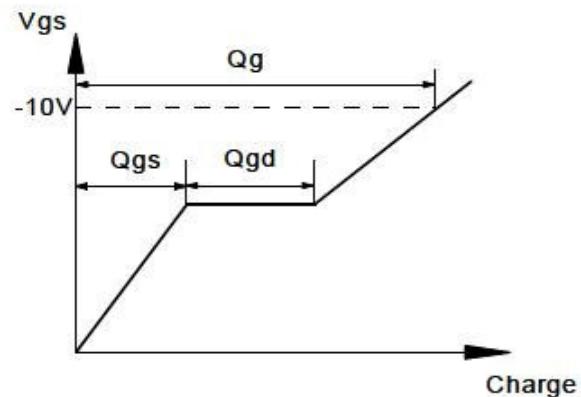
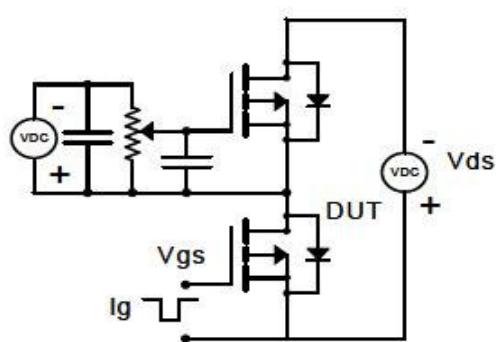
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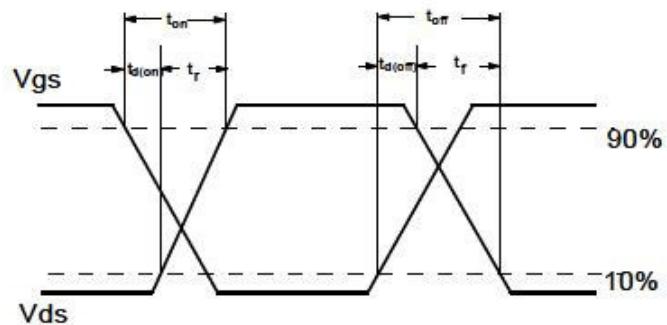
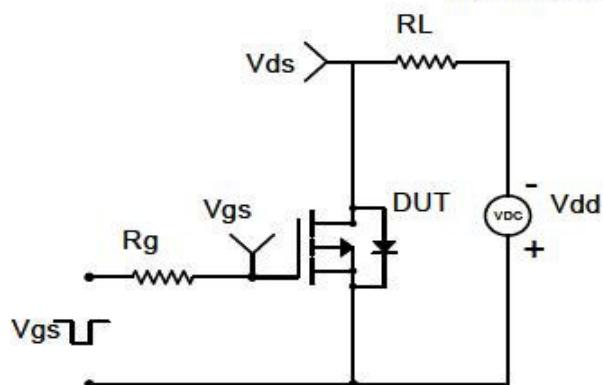
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■ Test circuit and waveform

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

