

Single P-channel MOSFET

ELM595781WSA-N

<http://www.elm-tech.com>

■ General description

ELM595781WSA-N uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate threshold voltage.

■ Features

- $V_{ds} = -60V$
- $I_d = -6.0A$
- $R_{ds(on)} = 40m\Omega$ ($V_{gs} = -10V$)
- $R_{ds(on)} = 48m\Omega$ ($V_{gs} = -4.5V$)

■ Maximum absolute ratings

$T_a = 25^\circ C$. Unless otherwise noted.

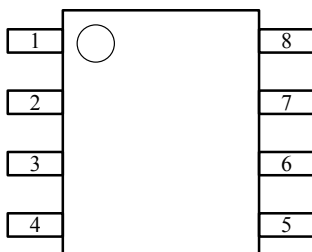
| Parameter | Symbol | Limit | Unit |
|--------------------------------|-----------|--------------------|------------|
| Drain-source voltage | V_{ds} | -60 | V |
| Gate-source voltage | V_{gs} | ± 20 | V |
| Continuous drain current | I_d | $T_a = 25^\circ C$ | -6 |
| | | $T_a = 70^\circ C$ | -5 |
| Pulsed drain current | I_{dm} | -20 | A |
| Power dissipation | P_d | $T_c = 25^\circ C$ | 2.8 |
| | | $T_c = 70^\circ C$ | 1.8 |
| Operating junction temperature | T_j | 150 | $^\circ C$ |
| Storage temperature range | T_{stg} | - 55 to 150 | $^\circ C$ |

■ Thermal characteristics

| Parameter | Symbol | Typ. | Max. | Unit |
|--|-----------------|------|------|--------------|
| Thermal resistance junction-to-ambient | $R_{\theta ja}$ | | 62.5 | $^\circ C/W$ |

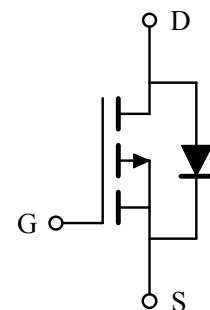
■ Pin configuration

SOP-8(TOP VIEW)



| Pin No. | Pin name |
|---------|----------|
| 1 | SOURCE |
| 2 | SOURCE |
| 3 | SOURCE |
| 4 | GATE |
| 5 | DRAIN |
| 6 | DRAIN |
| 7 | DRAIN |
| 8 | DRAIN |

■ Circuit



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■ Electrical characteristics

Ta=25°C. Unless otherwise noted.

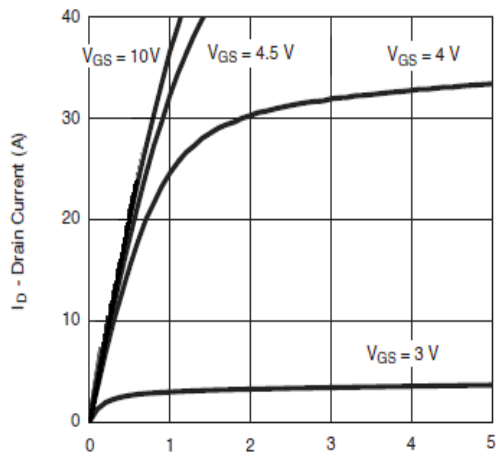
| Parameter | Symbol | Condition | Min. | Typ. | Max. | Unit |
|------------------------------------|---------|--|------|------|------|------|
| STATIC PARAMETERS | | | | | | |
| Drain-source breakdown voltage | BVdss | Vgs=0V, Id=-250μA | -60 | | | V |
| Zero gate voltage drain current | Idss | Vds=-48V, Vgs=0V | | | -1 | μA |
| | | Vds=-48V, Vgs=0V, Ta=85°C | | | -20 | |
| Gate-body leakage current | Igss | Vds=0V, Vgs=±20V | | | ±100 | nA |
| Gate threshold voltage | Vgs(th) | Vds=Vgs, Id=-250μA | -1.0 | | -2.0 | V |
| On state drain current | Id(on) | Vgs=-10V, Vds≥-5V | -6 | | | A |
| Static drain-source on-resistance | Rds(on) | Vgs=-10V, Id=-6A | | 33 | 40 | mΩ |
| | | Vgs=-4.5V, Id=-5A | | 39 | 48 | |
| Forward transconductance | Gfs | Vds=-15V, Id=-6A | | 25 | | S |
| Diode forward voltage | Vsd | Is=-3A, Vgs=0V | | -0.8 | -1.3 | V |
| Max. body-diode continuous current | Is | | | | -2.0 | A |
| DYNAMIC PARAMETERS | | | | | | |
| Input capacitance | Ciss | Vgs=0V, Vds=-30V, f=1MHz | | 1900 | | pF |
| Output capacitance | Coss | | | 210 | | pF |
| Reverse transfer capacitance | Crss | | | 170 | | pF |
| SWITCHING PARAMETERS | | | | | | |
| Total gate charge | Qg | Vgs=-4.5V, Vds=-30V Id=-6A | | 20 | 38 | nC |
| Gate-source charge | Qgs | | | 8 | | nC |
| Gate-drain charge | Qgd | | | 10 | | nC |
| Turn-on delay time | td(on) | Vgs=-10V, Vds=-30V RL=5Ω, Id=-6A Rgen=1Ω | | 10 | 20 | ns |
| Turn-on rise time | tr | | | 12 | 25 | ns |
| Turn-off delay time | td(off) | | | 30 | 60 | ns |
| Turn-off fall time | tf | | | 10 | 20 | ns |

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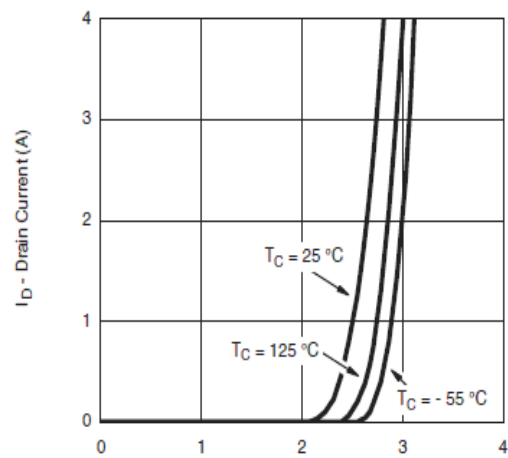
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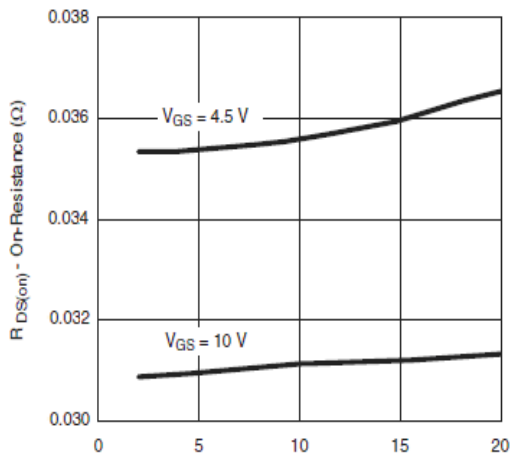
■ Typical electrical and thermal characteristics



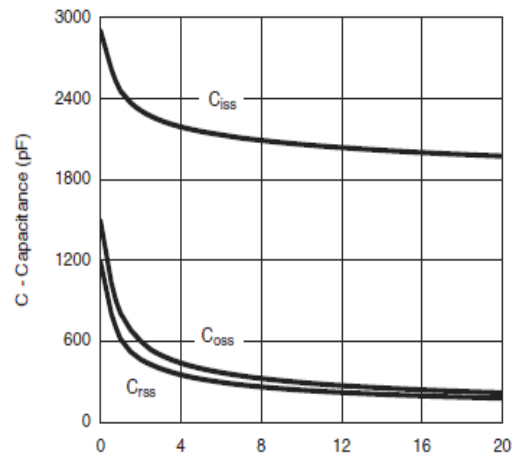
Output Characteristics



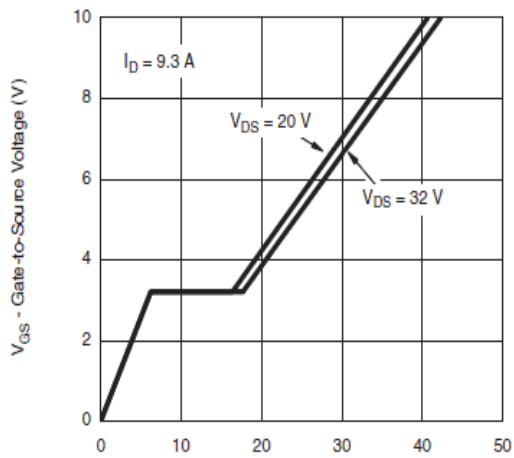
Transfer Characteristics



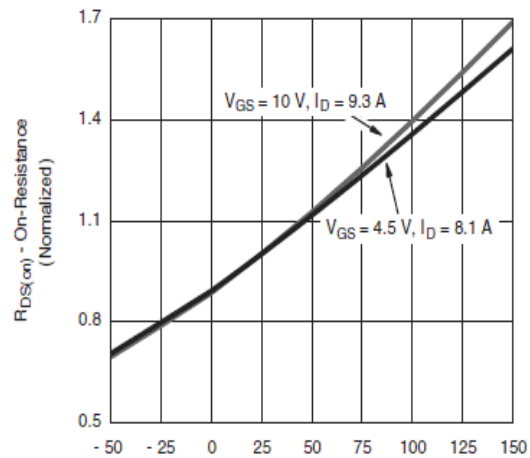
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



Gate Charge

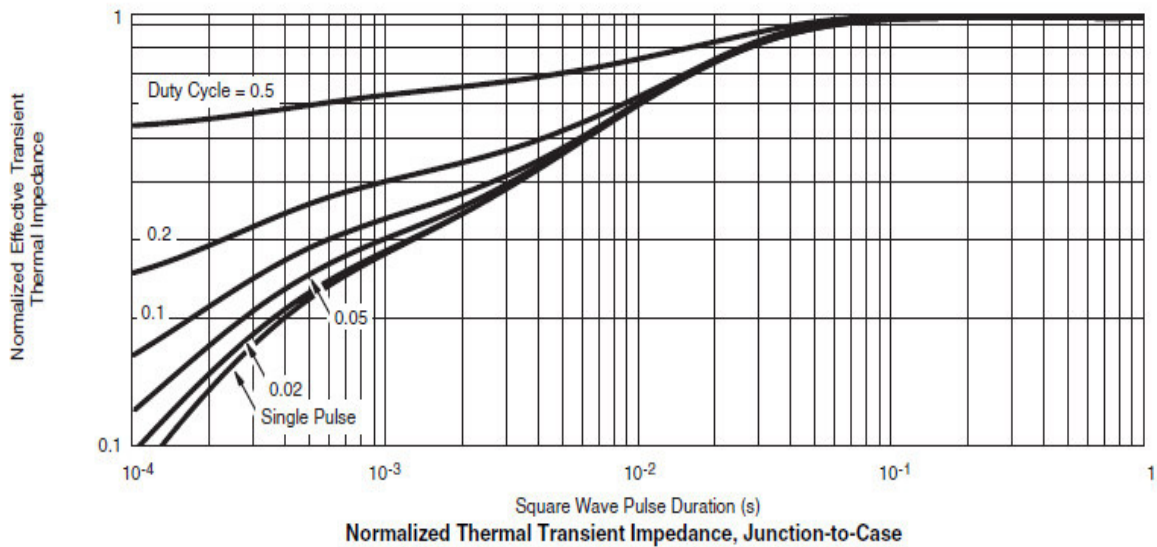
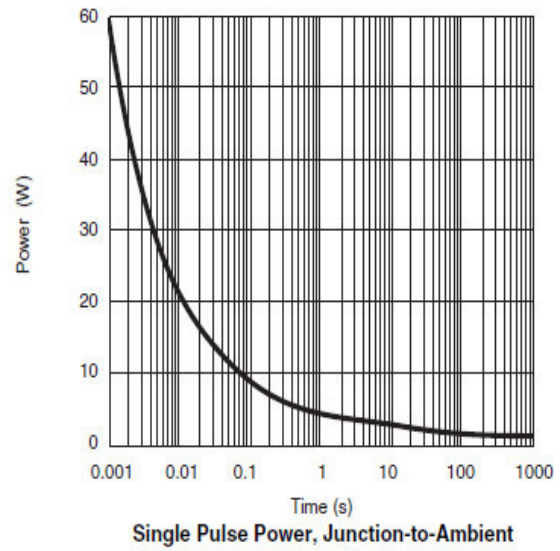
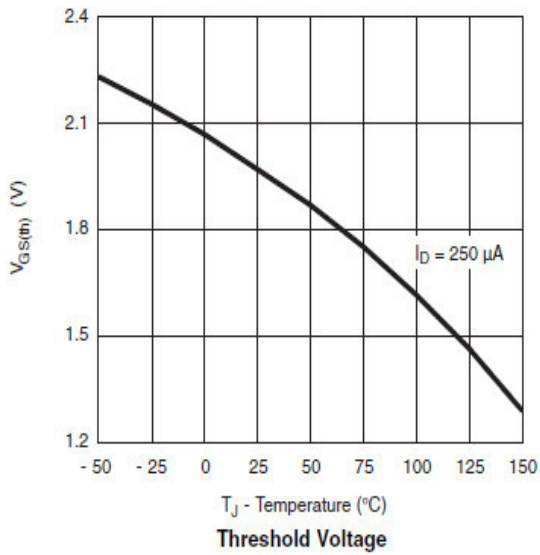
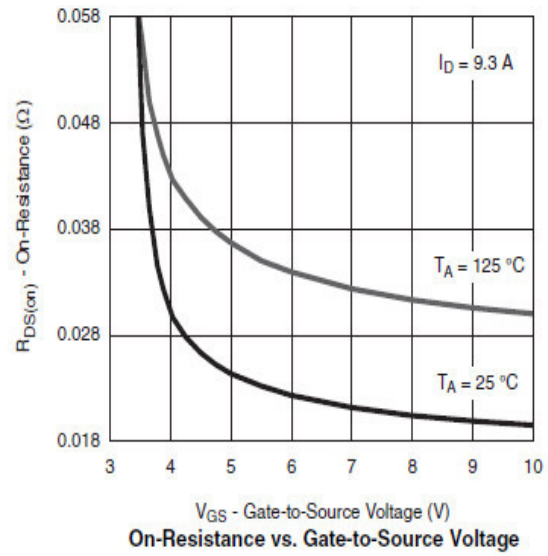
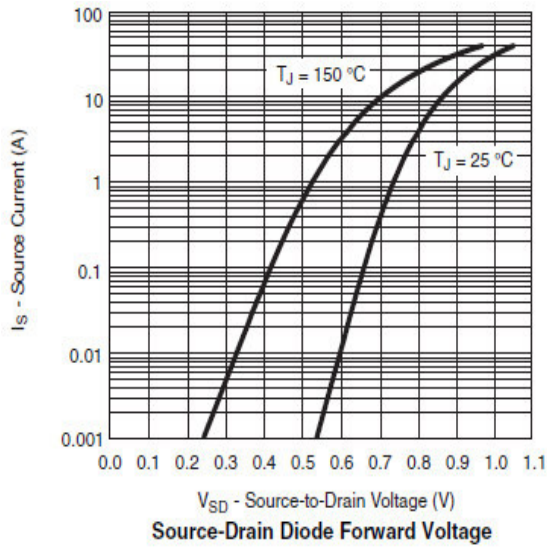


On-Resistance vs. Junction Temperature

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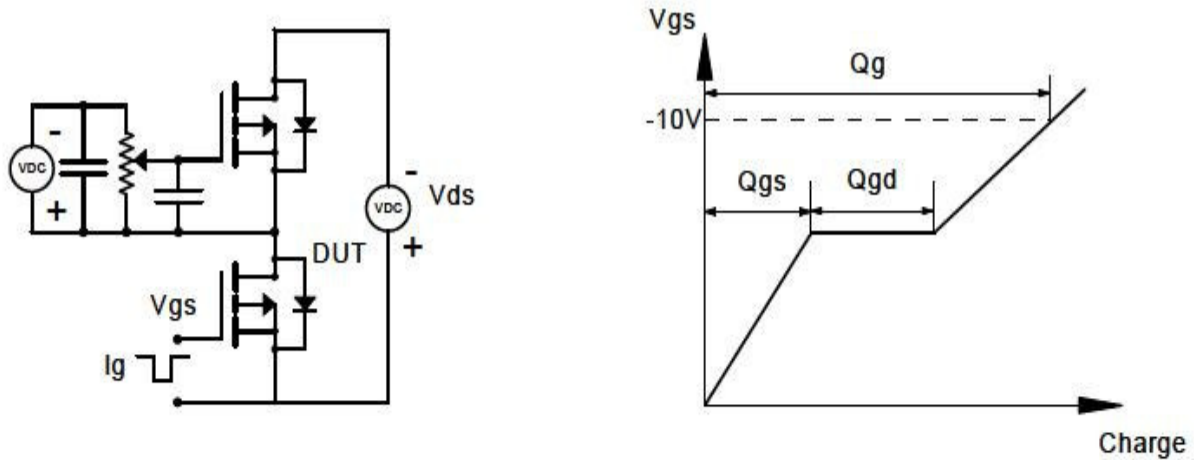
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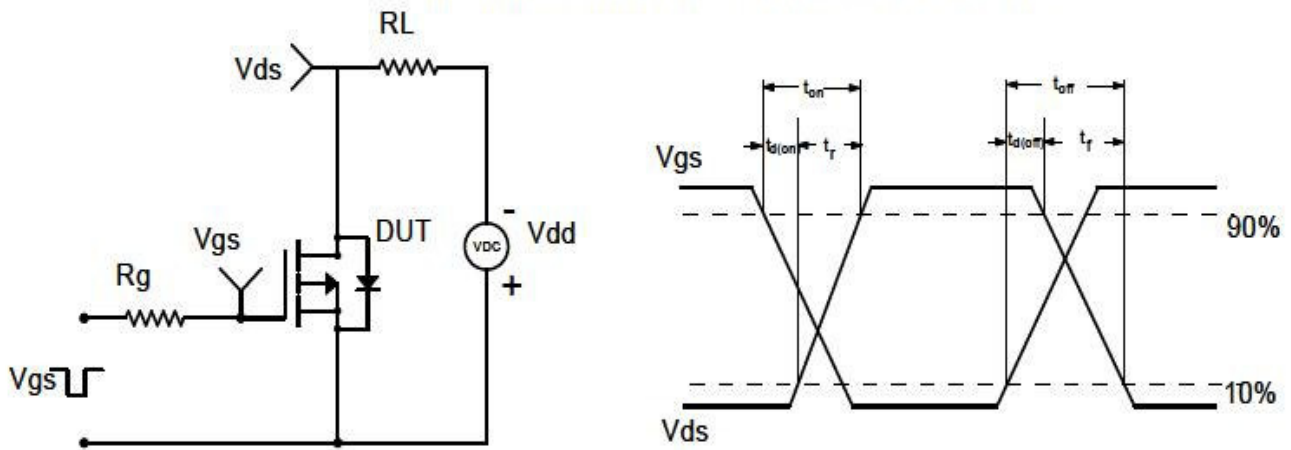
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■ Test circuit and waveform

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

