

Single P-channel MOSFET

ELM6PB010FAA-N

<http://www.elm-tech.com>

■ General description

ELM6PB010FAA-N uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate threshold voltage.

■ Features

- $V_{ds} = -40V$
- $I_d = -22A$ ($V_{gs} = -10V$)
- $R_{ds(on)} = 10m\Omega$ ($V_{gs} = -10V$)
- $R_{ds(on)} = 13m\Omega$ ($V_{gs} = -4.5V$)

■ Maximum absolute ratings

$T_a = 25^\circ C$. Unless otherwise noted.

Parameter	Symbol	Limit	Unit	Note	
Drain-source voltage	V_{ds}	-40	V		
Gate-source voltage	V_{gs}	± 20	V		
Continuous drain current	I_d	$T_c = 25^\circ C$	-22.0	A	1
		$T_c = 100^\circ C$	-14.0		
Continuous drain current	I_d	$T_a = 25^\circ C$	-9.7	A	2
		$T_a = 70^\circ C$	-7.8		
Pulsed drain current	I_{dm}	-88	A	3	
Max. body-diode continuous current ($T_c = 25^\circ C$)	I_s	-10	A	1	
Avalanche current ($L = 0.1mH$)	I_{as}	-30	A		
Avalanche energy ($L = 0.5mH$)	E_{as}	81	mJ		
Power dissipation	P_d	$T_c = 25^\circ C$	12.0	W	1
		$T_c = 100^\circ C$	4.6		
Power dissipation	P_d	$T_a = 25^\circ C$	2.3	W	2
		$T_a = 70^\circ C$	1.5		
Junction and storage temperature range	T_j, T_{stg}	-55 to +150	$^\circ C$		

■ Thermal characteristics

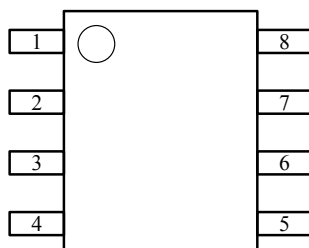
Parameter	Symbol	Typ.	Max.	Unit	Note
Thermal resistance, junction-to-ambient	$R_{\theta ja}$		55	$^\circ C/W$	2
Thermal resistance, junction-to-case	$R_{\theta jc}$		11		

NOTE : 1. The power dissipation P_d is based on $T_j(max) = 150^\circ C$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

2. The value of $R_{\theta ja}$ is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with $T_a = 25^\circ C$. The power dissipation P_{dsm} is based on $R_{\theta ja}$ and the maximum allowed junction temperature of $150^\circ C$. The value in any given application depends on the user's specific board design.
3. Pulse width limited by junction temperature $T_j(max) = 150^\circ C$. Ratings are based on low frequency and low duty cycles to keep initial $T_j = 25^\circ C$.

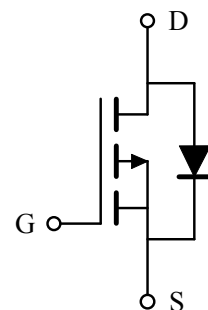
■ Pin configuration

SOP-8(TOP VIEW)



Pin No.	Pin name
1	SOURCE
2	SOURCE
3	SOURCE
4	GATE
5	DRAIN
6	DRAIN
7	DRAIN
8	DRAIN

■ Circuit



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■Electrical characteristics

Ta=25°C. Unless otherwise noted.

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
STATIC PARAMETERS							
Drain-source breakdown voltage	BV _{dss}	I _d =-250μA, V _{gs} =0V	-40	-	-	V	
Zero gate voltage drain current	I _{dss}	V _{ds} =-32V, V _{gs} =0V	-	-	-10	μA	
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±20V	-	-	±100	nA	
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , I _d =-250μA	-1.0	-	-2.5	V	
Static drain-source on-resistance	R _{ds(on)}	V _{gs} =-10V, I _d =-10A	-	10.0	13.0	mΩ	
		V _{gs} =-4.5V, I _d =-8A	-	13.0	18.5		
Forward transconductance	G _{fs}	V _{ds} =-10V, I _d =-10A	-	24	-	S	
Diode forward voltage	V _{sd}	I _s =-10A, V _{gs} =0V	-	-0.82	-1.20	V	1
DYNAMIC PARAMETERS							
Input capacitance	C _{iss}	V _{gs} =0V, V _{ds} =-20V, f=1MHz	-	3400	-	pF	
Output capacitance	C _{oss}		-	280	-	pF	
Reverse transfer capacitance	C _{rss}		-	200	-	pF	
Gate resistance	R _g	f=1MHz	-	5.3	-	Ω	
SWITCHING PARAMETERS							
Total gate charge	Q _g	V _{gs} =-10V, V _{ds} =-20V I _d =-10A	-	68.0	-	nC	1, 2
Gate-source charge	Q _{gs}		-	10.5	-	nC	1, 2
Gate-drain charge	Q _{gd}		-	14.5	-	nC	1, 2
Turn-on delay time	t _{d(on)}	V _{gs} =-10V, V _{ds} =-20V I _d =-10A, R _{gen} =1Ω	-	17	-	ns	1, 2
Turn-on rise time	t _r		-	21	-	ns	1, 2
Turn-off delay time	t _{d(off)}		-	97	-	ns	1, 2
Turn-off fall time	t _f		-	17	-	ns	1, 2
Body diode reverse recovery time	t _{rr}	I _f =-10A, dI _f /dt=100A/μs	-	16	-	ns	
Body diode reverse recovery charge	Q _{rr}		-	11	-	nC	

* 1. Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%.

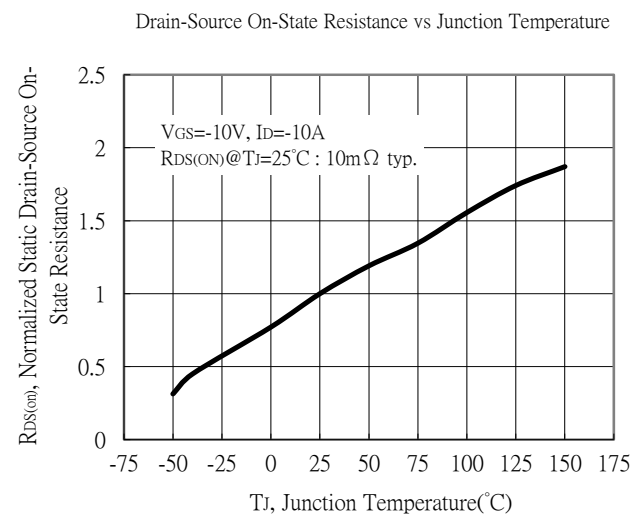
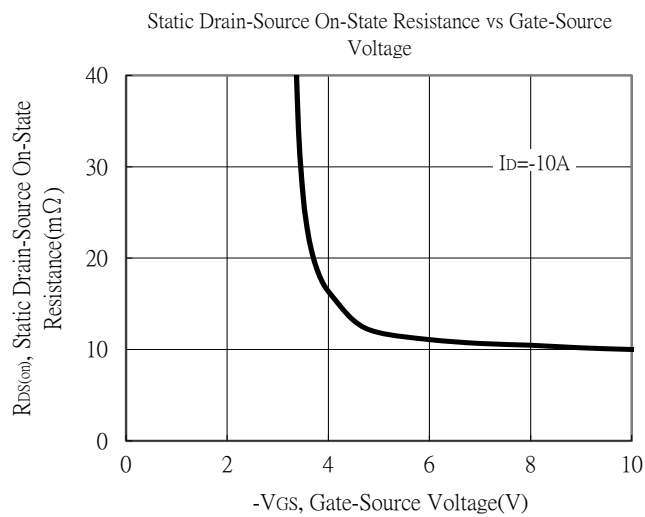
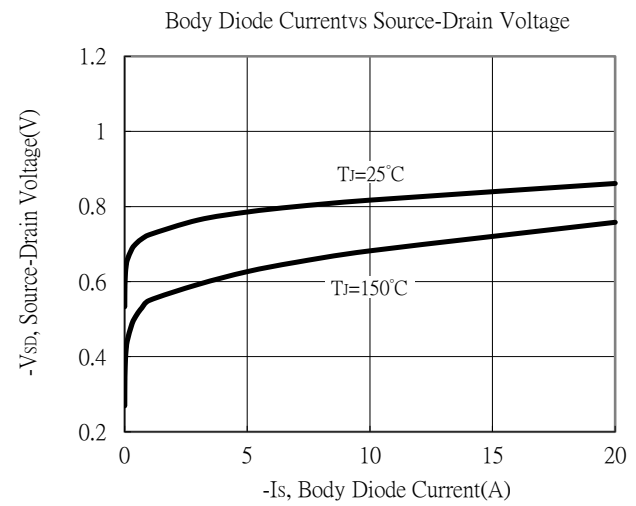
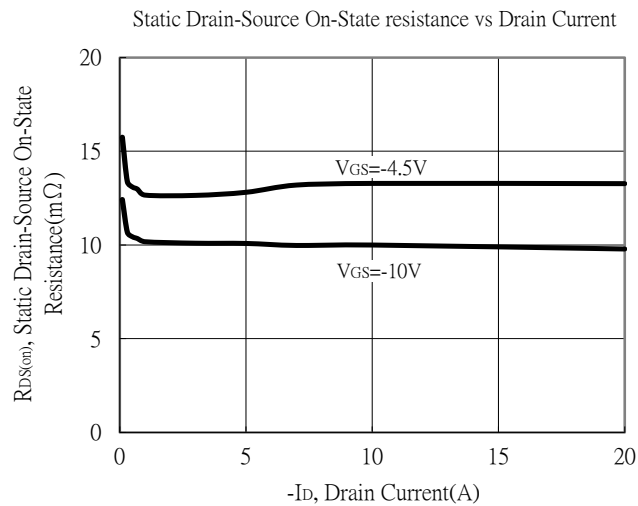
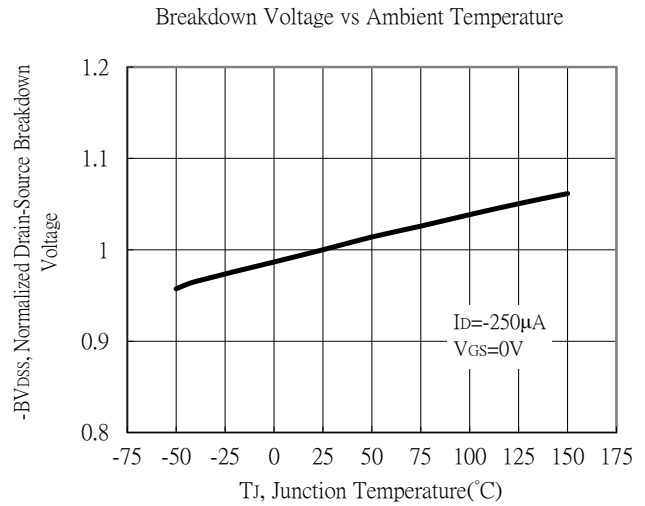
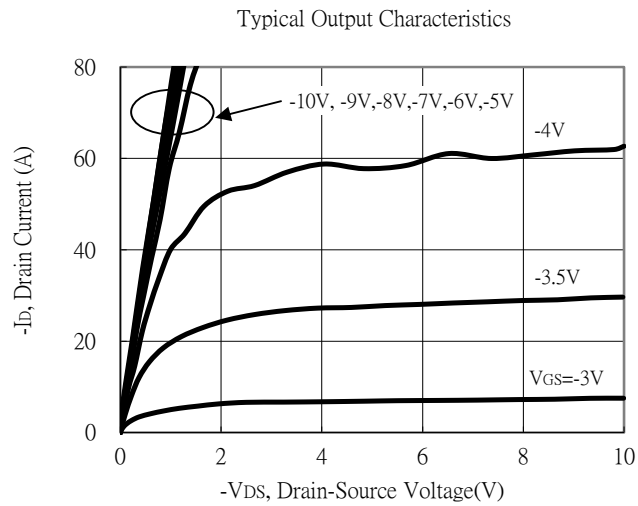
2. Independent of operating temperature.

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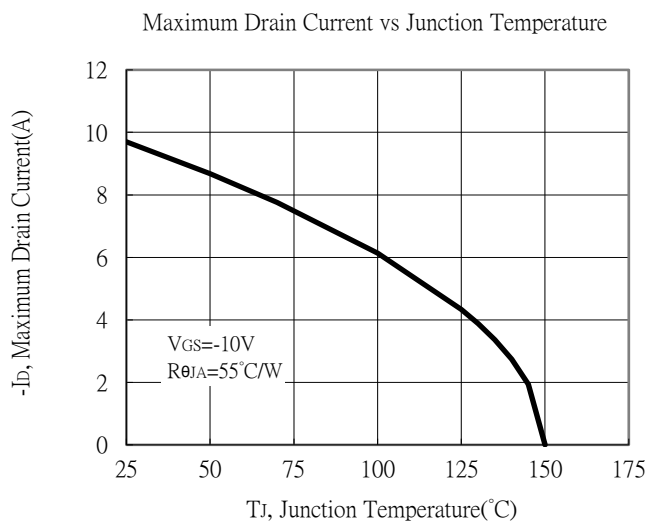
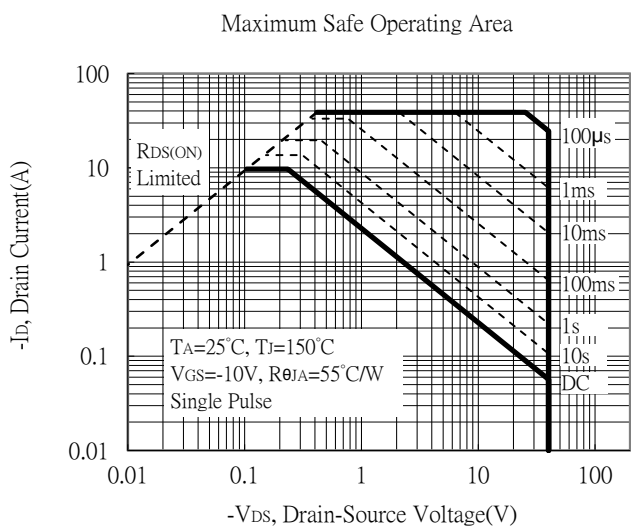
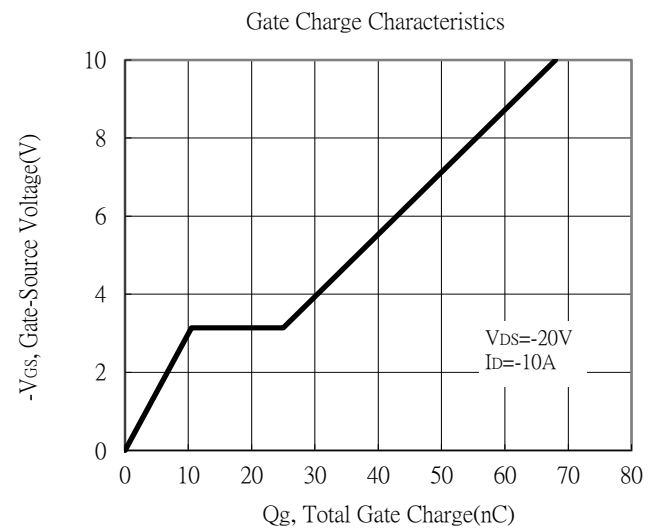
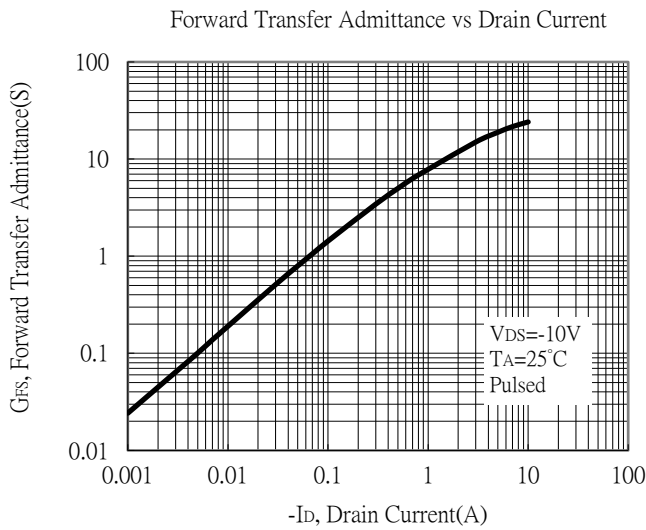
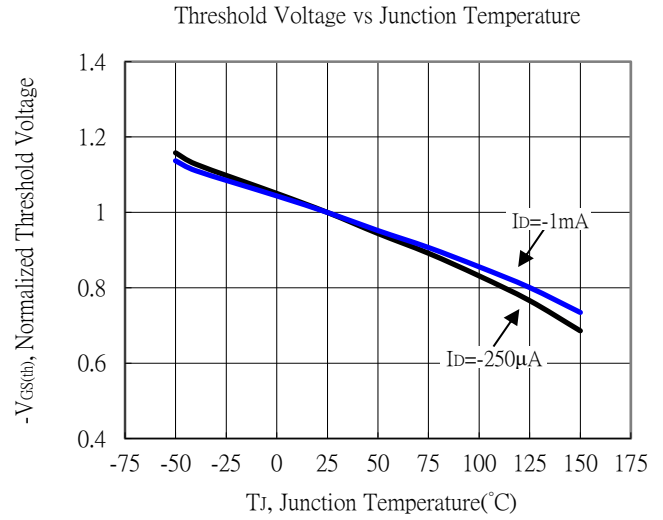
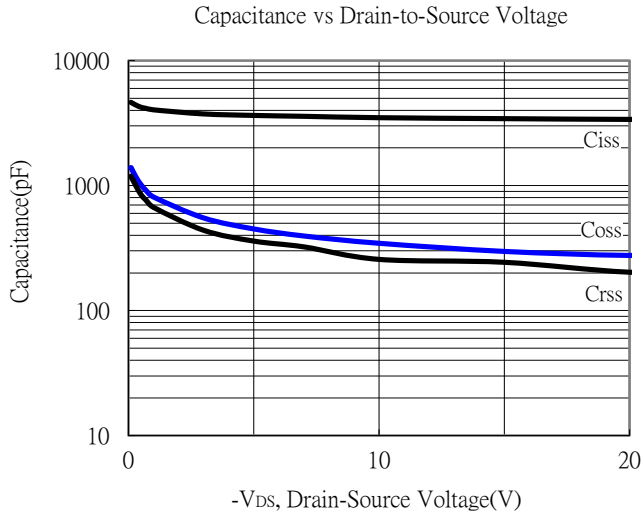
Typical electrical and thermal characteristics



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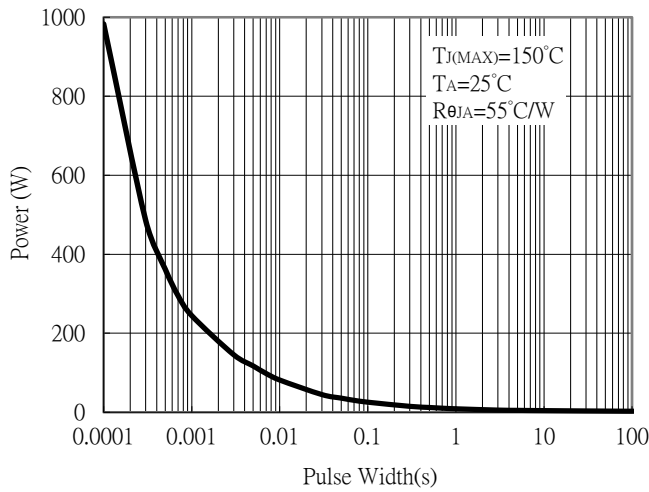


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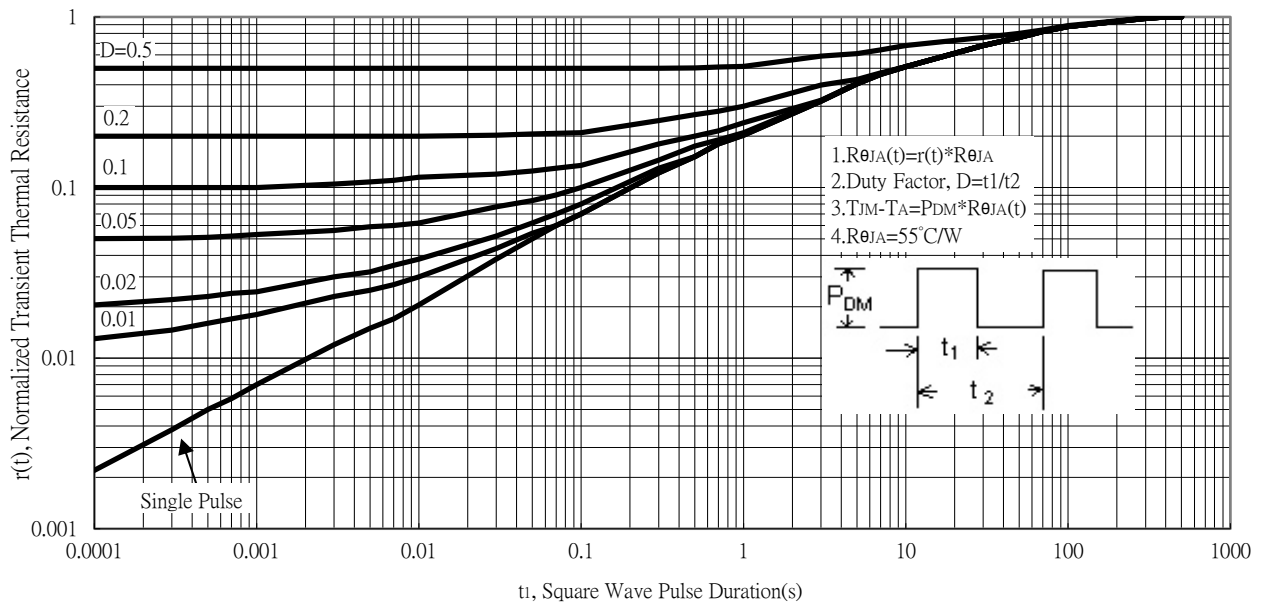
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Single Pulse Power Rating, Junction to Ambient



Transient Thermal Response Curves



Recommended soldering footprint

