

Single N-channel MOSFET

ELM530150SA-S

<http://www.elm-tech.com>

■General description

ELM530150SA-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate threshold voltage.

■Features

- $V_{ds}=30V$
- $I_d=75A$
- $R_{ds(on)} = 5.1m\Omega$ ($V_{gs}=10V$)
- $R_{ds(on)} = 6.8m\Omega$ ($V_{gs}=4.5V$)

■Maximum absolute ratings

Ta=25°C. Unless otherwise noted.

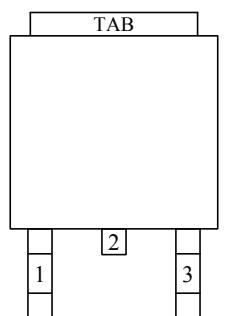
Parameter	Symbol	Limit	Unit
Drain-source voltage	V_{ds}	30	V
Gate-source voltage	V_{gs}	± 20	V
Continuous drain current($T_j=150^{\circ}C$)	I_d	75	A
		55	
Pulsed drain current	I_{dm}	200	A
Power dissipation	P_d	40	W
		15	
Operating junction temperature	T_j	150	°C
Storage temperature range	T_{stg}	- 55 to 150	°C

■Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit
Thermal resistance junction-to-ambient	$R_{\theta ja}$		62.5	°C/W

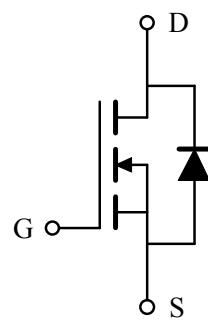
■Pin configuration

TO-252-3(TOP VIEW)



Pin No.	Pin name
1	GATE
2	DRAIN
3	SOURCE

■Circuit



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■Electrical characteristics

T_a=25°C. Unless otherwise noted.

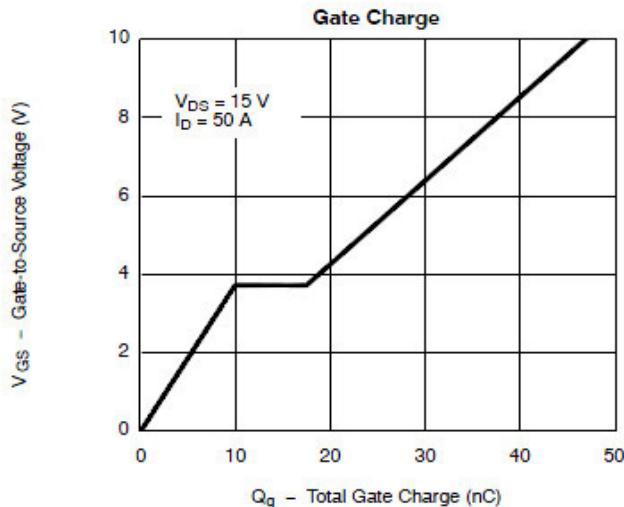
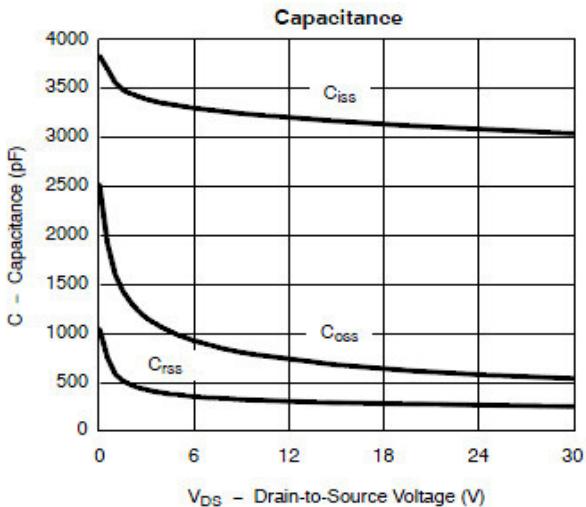
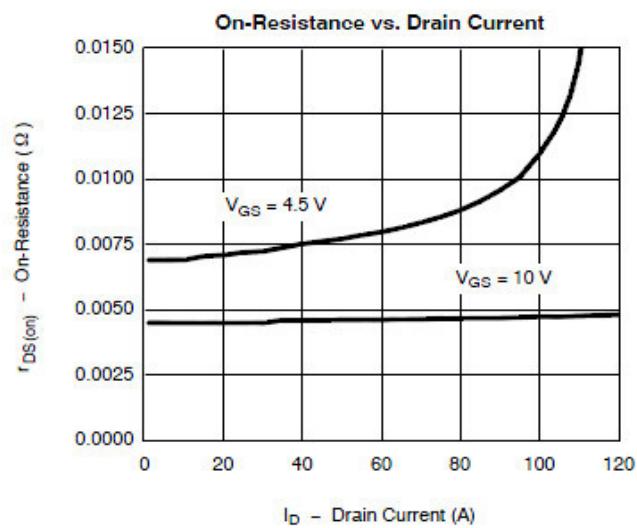
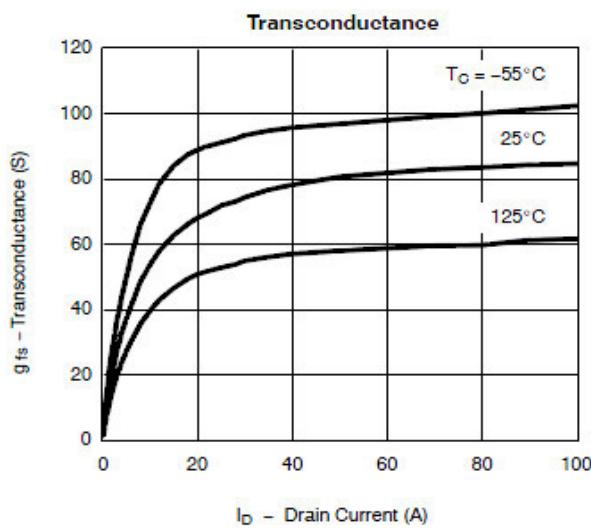
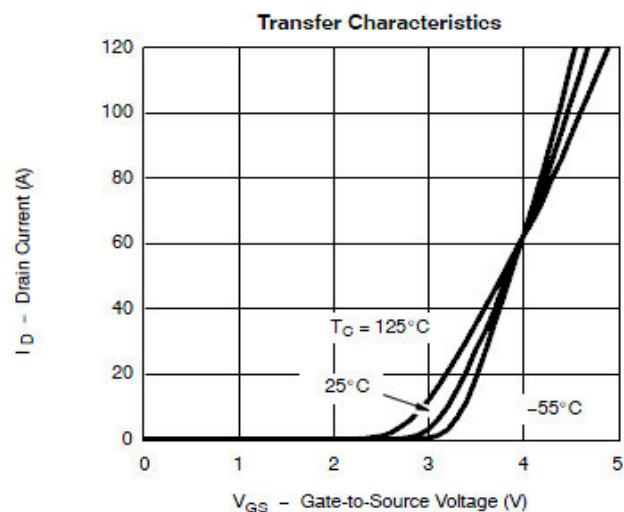
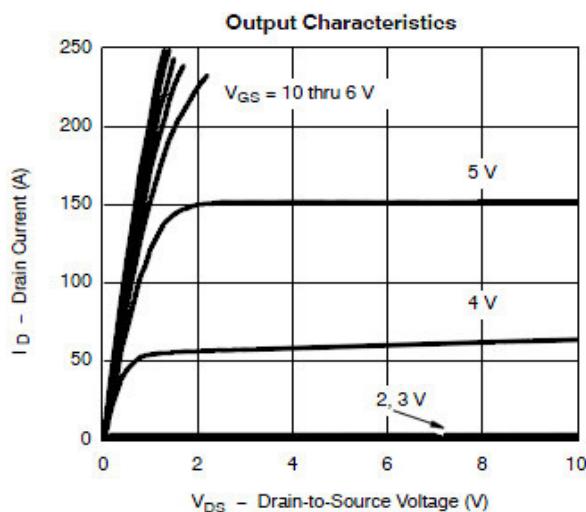
Parameter	Symbol	Condition		Min.	Typ.	Max.	Unit
STATIC PARAMETERS							
Drain-source breakdown voltage	BV _{dss}	Id=250μA, V _{gs} =0V		30			V
Zero gate voltage drain current	Id _{ss}	V _{ds} =24V, V _{gs} =0V			1		μA
			T _a =85°C			10	
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±20V				±100	nA
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , Id=250μA		1.0		2.0	V
On state drain current	I _{d(on)}	V _{gs} =10V, V _{ds} ≥5V		15			A
Static drain-source on-resistance	R _{ds(on)}	V _{gs} =10V, Id=45A			3.6	5.1	mΩ
		V _{gs} =4.5V, Id=30A			5.1	6.8	
Forward transconductance	G _{fs}	V _{ds} =15V, Id=20A			24		S
Diode forward voltage	V _{sd}	I _s =30A, V _{gs} =0V			0.8	1.3	V
Max. body-diode continuous current	I _s					9	A
DYNAMIC PARAMETERS							
Input capacitance	C _{iss}	V _{gs} =0V, V _{ds} =25V, f=1MHz			2800		pF
Output capacitance	C _{oss}				550		pF
Reverse transfer capacitance	C _{rss}				300		pF
SWITCHING PARAMETERS							
Total gate charge	Q _g	V _{gs} =10V, V _{ds} =15V Id=40A			50	70	nC
Gate-source charge	Q _{gs}				10		nC
Gate-drain charge	Q _{gd}				8		nC
Turn-on delay time	t _{d(on)}	V _{gs} =10V, V _{ds} =15V RL=0.3Ω, Id=40A R _{gen} =2.5Ω			12	20	ns
Turn-on rise time	t _r				12	20	ns
Turn-off delay time	t _{d(off)}				30	45	ns
Turn-off fall time	t _f				10	20	ns

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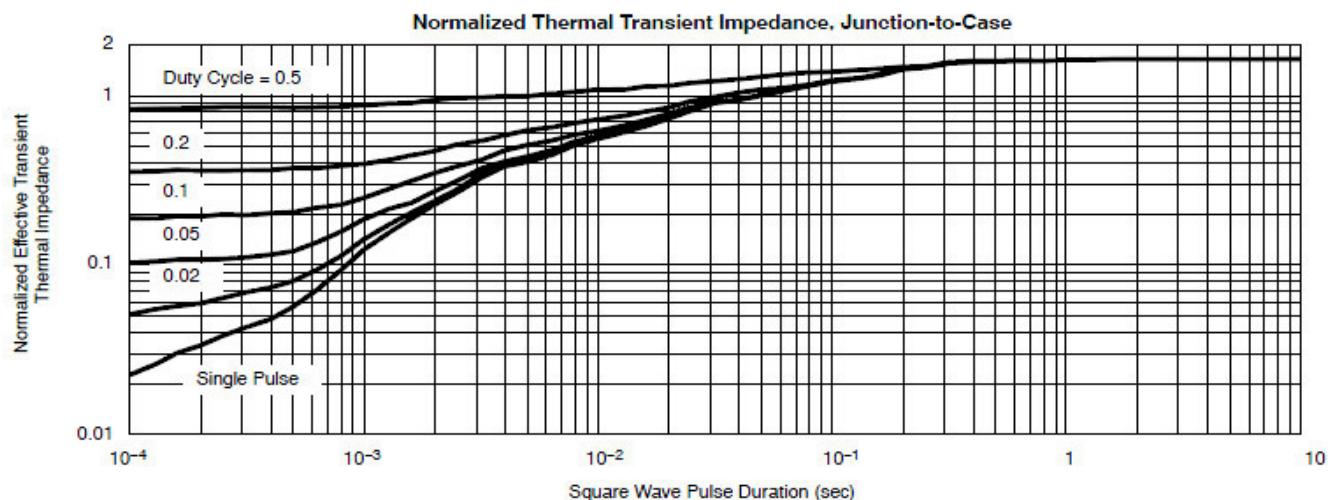
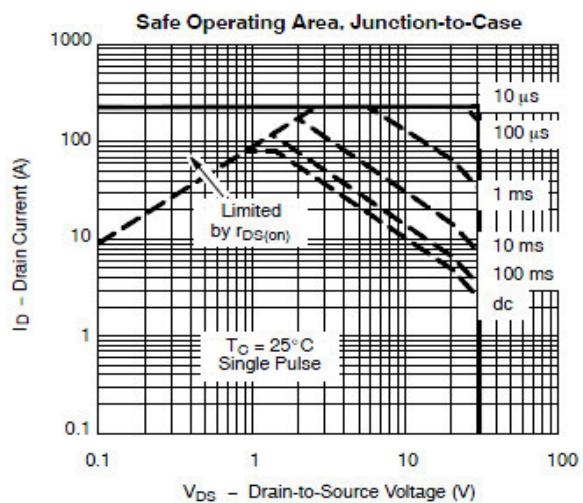
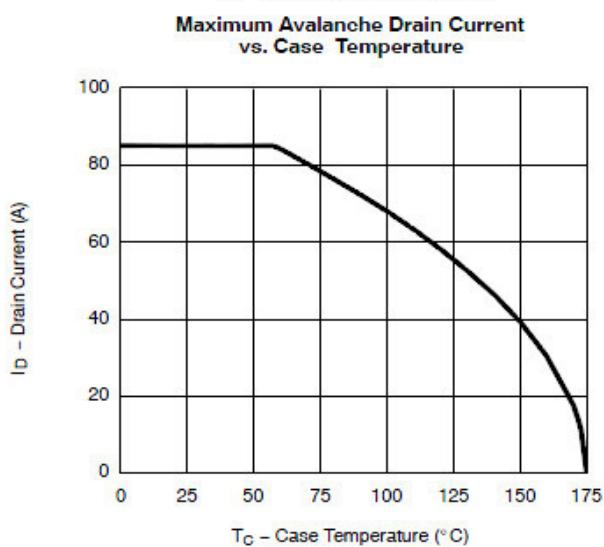
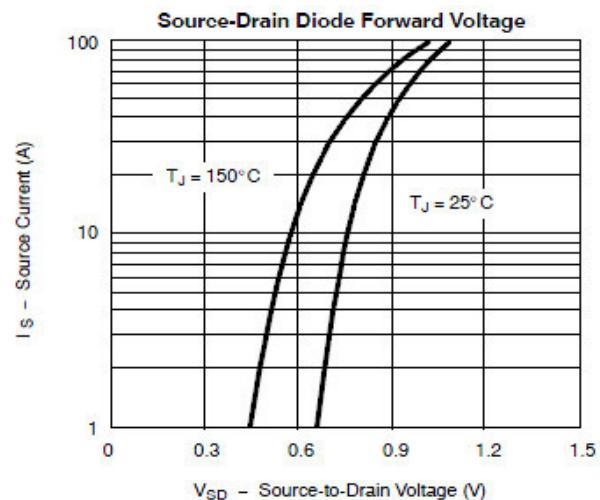
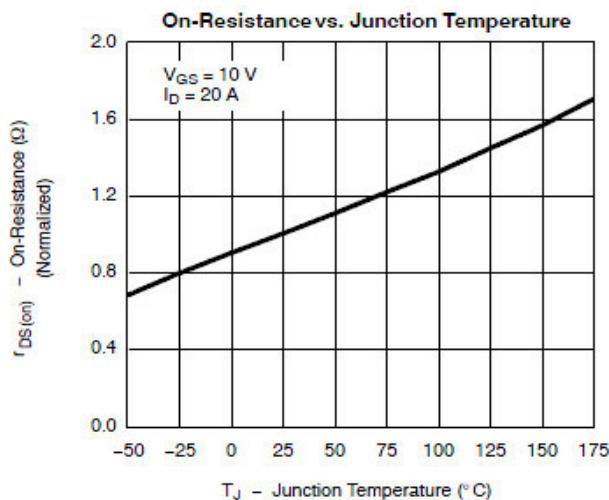
■ Typical electrical and thermal characteristics



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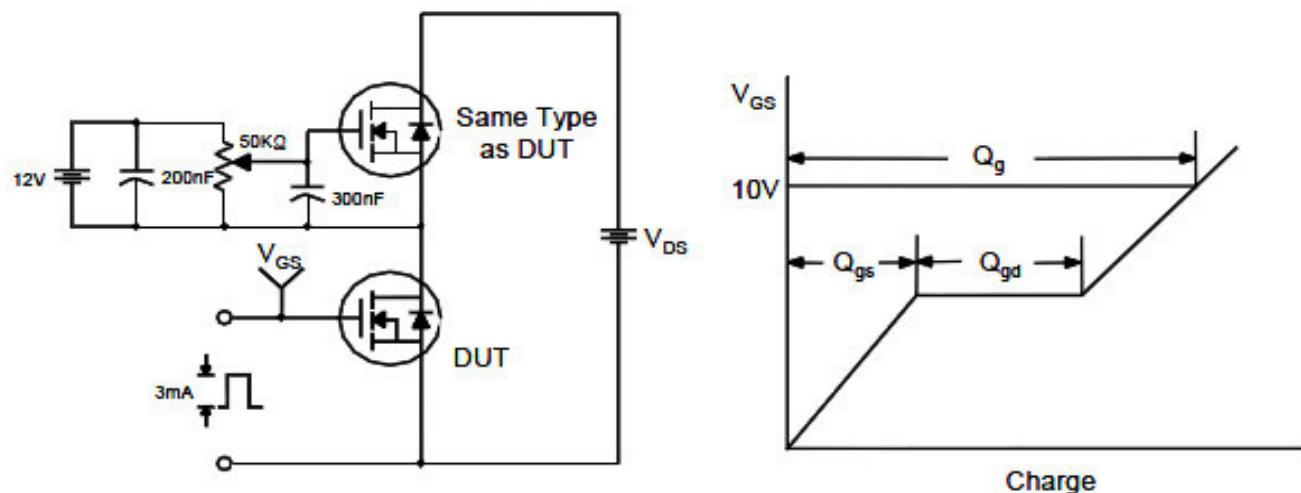
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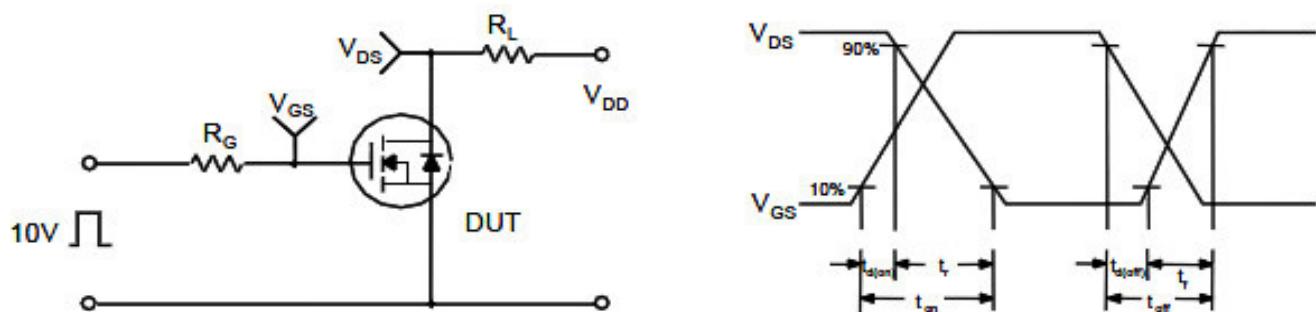
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■ Test circuit and waveform

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

