

Single N-channel MOSFET

ELM59498A-S

<http://www.elm-tech.com>

■General description

ELM59498A-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and low gate threshold voltage.

■Features

- $V_{ds}=100V$
- $I_d=10A$
- $R_{ds(on)} = 135m\Omega$ ($V_{gs}=10V$)
- $R_{ds(on)} = 145m\Omega$ ($V_{gs}=4.5V$)

■Maximum absolute ratings

Ta=25°C. Unless otherwise noted.

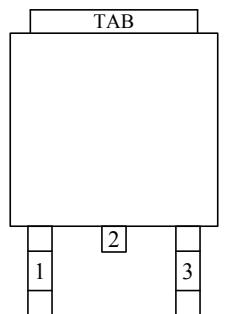
Parameter	Symbol	Limit	Unit
Drain-source voltage	V_{ds}	100	V
Gate-source voltage	V_{gs}	± 20	V
Continuous drain current($T_j=150^{\circ}C$)	I_d	10	A
		8	
Pulsed drain current	I_{dm}	20	A
Single pulse avalanche current	I_{as}	8	A
Power dissipation	P_d	40	W
		15	
Operating junction temperature	T_j	150	°C
Storage temperature range	T_{stg}	- 55 to 150	°C

■Thermal characteristics

Parameter	Symbol	Typ.	Max.	Unit
Thermal resistance junction-to-ambient	$R_{\theta ja}$		62.5	°C/W

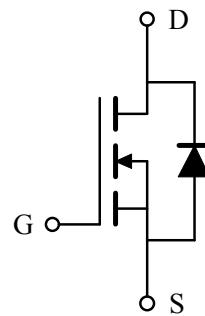
■Pin configuration

TO-252-3(TOP VIEW)



Pin No.	Pin name
1	GATE
2	DRAIN
3	SOURCE

■Circuit



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■Electrical characteristics

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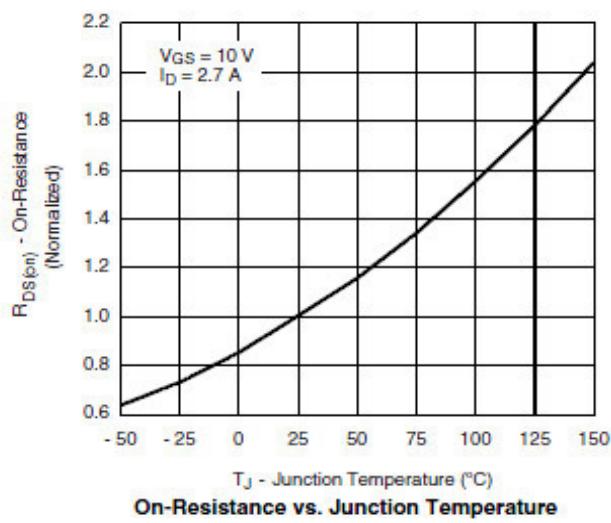
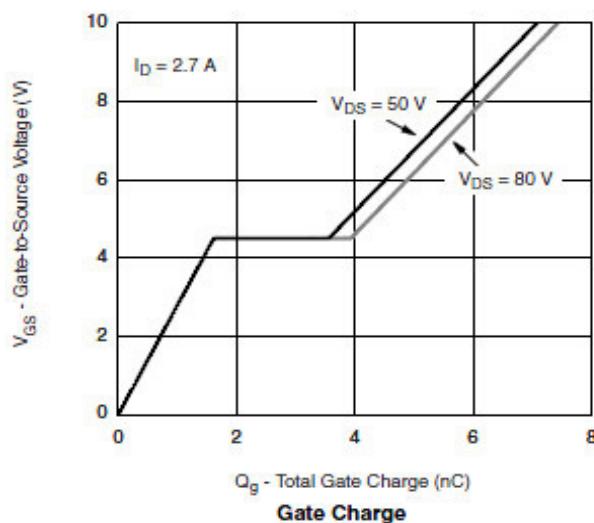
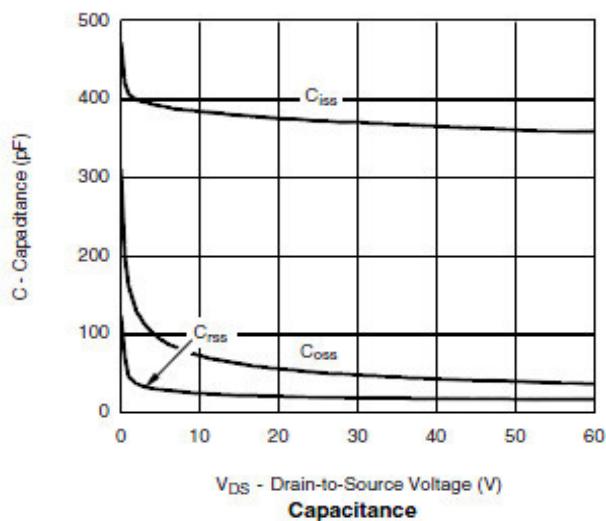
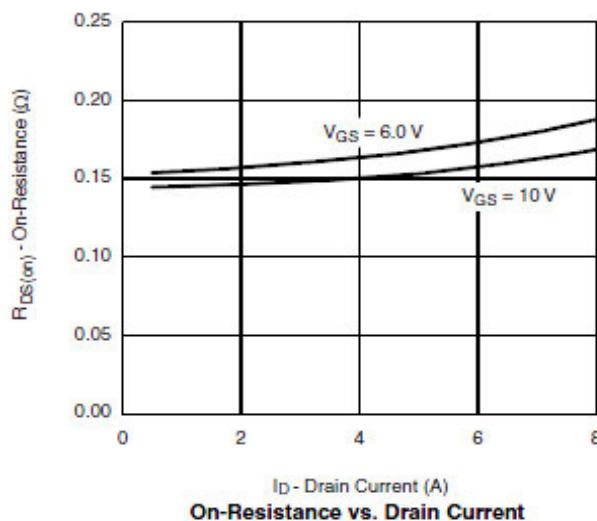
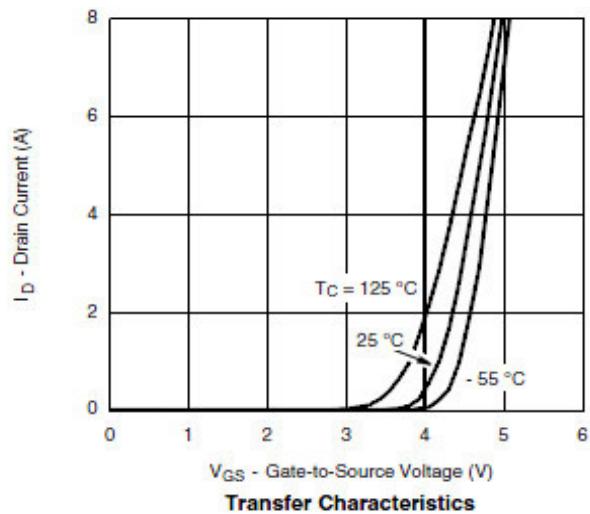
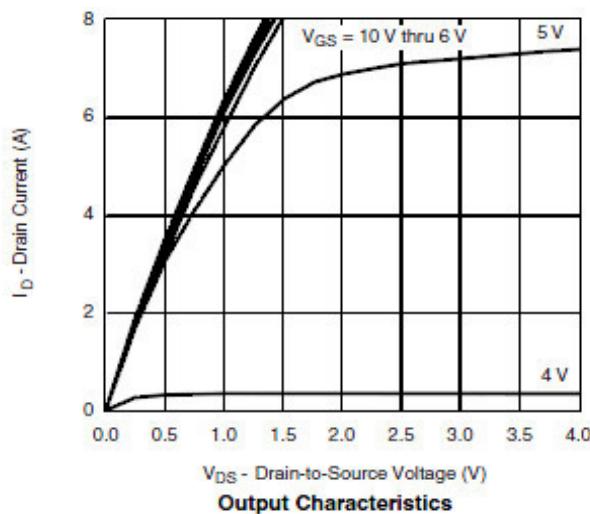
Parameter	Symbol	Condition		Min.	Typ.	Max.	Unit	
STATIC PARAMETERS								
Drain-source breakdown voltage	BV _{dss}	Id=250μA, V _{gs} =0V		100			V	
Zero gate voltage drain current	Id _{ss}	V _{ds} =80V, V _{gs} =0V	Ta=85°C			1	μA	
						5		
Gate-body leakage current	I _{gss}	V _{ds} =0V, V _{gs} =±20V				±100	nA	
Gate threshold voltage	V _{gs(th)}	V _{ds} =V _{gs} , Id=250μA		1.0	1.8	2.5	V	
On state drain current	I _{d(on)}	V _{gs} =10V, V _{ds} ≥5V		8			A	
Static drain-source on-resistance	R _{ds(on)}	V _{gs} =10V, Id=5A			120	135	mΩ	
		V _{gs} =4.5V, Id=3A			125	145		
Forward transconductance	G _{fs}	V _{ds} =10V, Id=3A			12		S	
Diode forward voltage	V _{sd}	I _s =2A, V _{gs} =0V			0.8	1.2	V	
Max. body-diode continuous current	I _s					2	A	
DYNAMIC PARAMETERS								
Input capacitance	C _{iss}	V _{gs} =0V, V _{ds} =50V, f=1MHz			415		pF	
Output capacitance	C _{oss}				40		pF	
Reverse transfer capacitance	C _{rss}				20		pF	
SWITCHING PARAMETERS								
Total gate charge	Q _g	V _{gs} =10V, V _{ds} =50V Id=4.5A			10.0	15.0	nC	
Gate-source charge	Q _{gs}				1.7		nC	
Gate-drain charge	Q _{gd}				2.0		nC	
Turn-on delay time	t _{d(on)}	V _{gs} =10V, V _{ds} =50V RL=23.8Ω, Id=2.1A R _{gen} =1.0Ω			10	15	ns	
Turn-on rise time	t _r				10	15	ns	
Turn-off delay time	t _{d(off)}				12	20	ns	
Turn-off fall time	t _f				10	15	ns	

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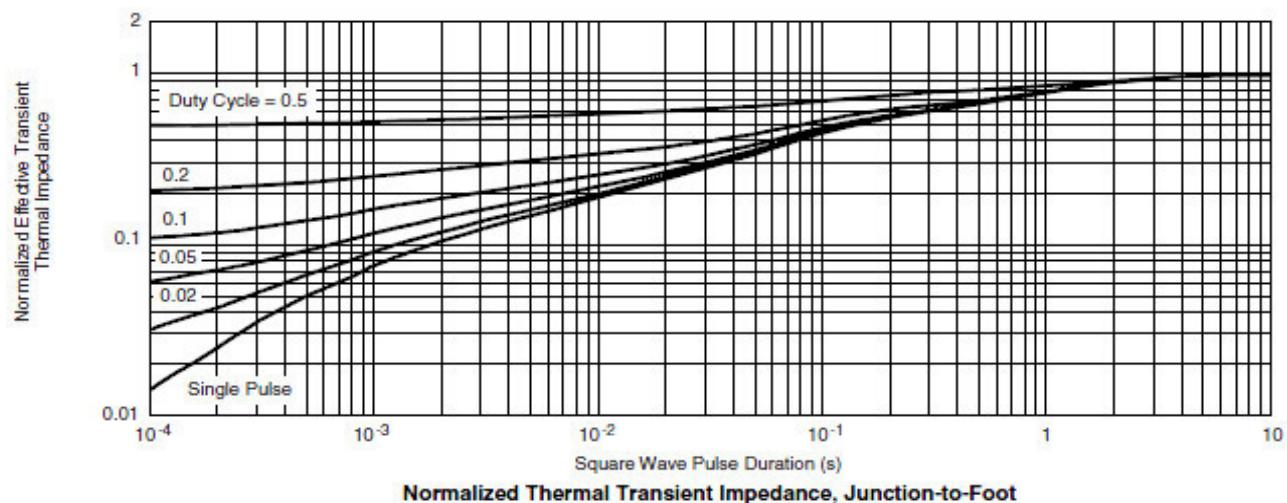
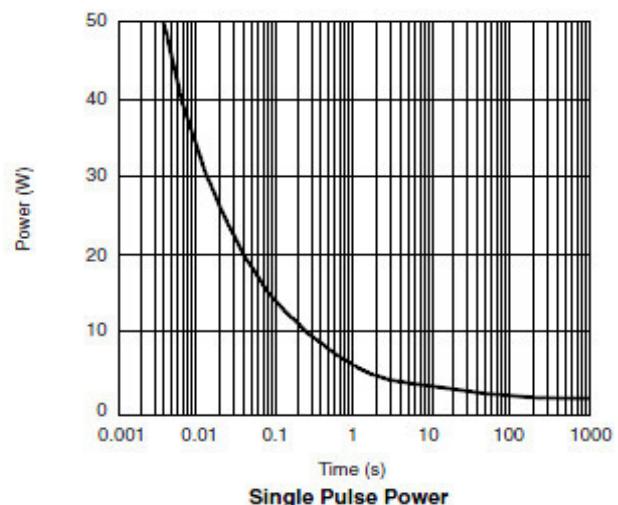
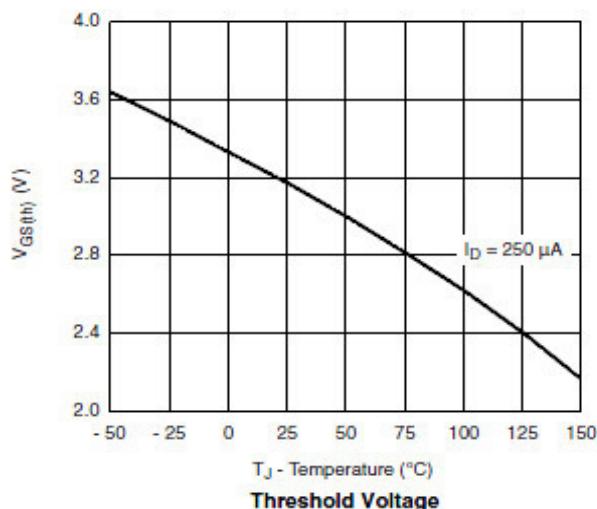
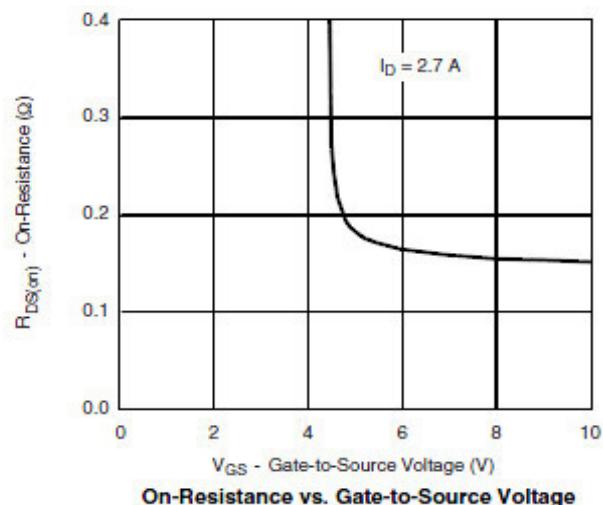
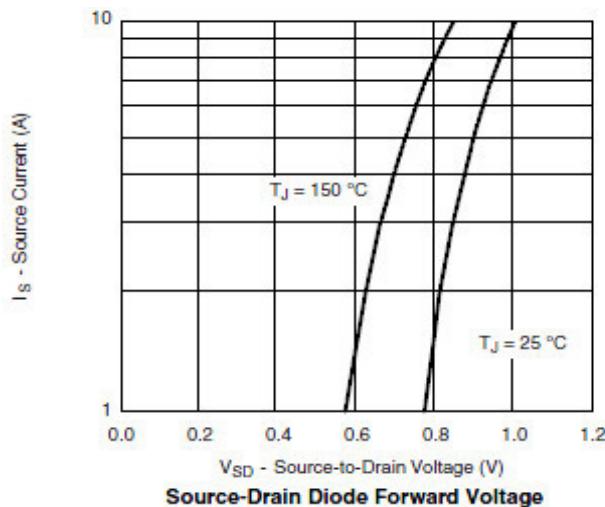
■ Typical electrical and thermal characteristics



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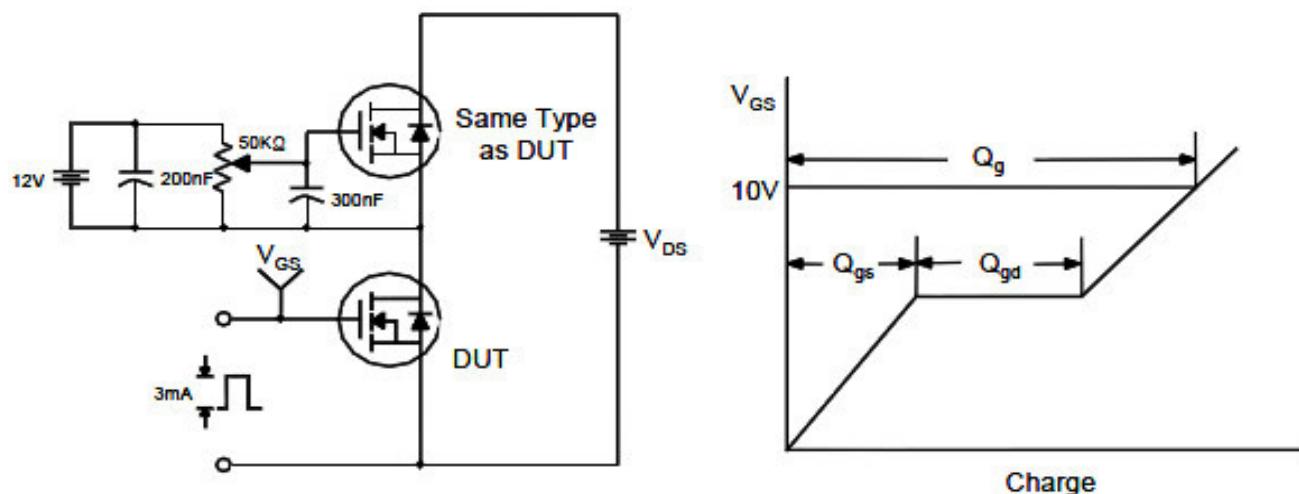
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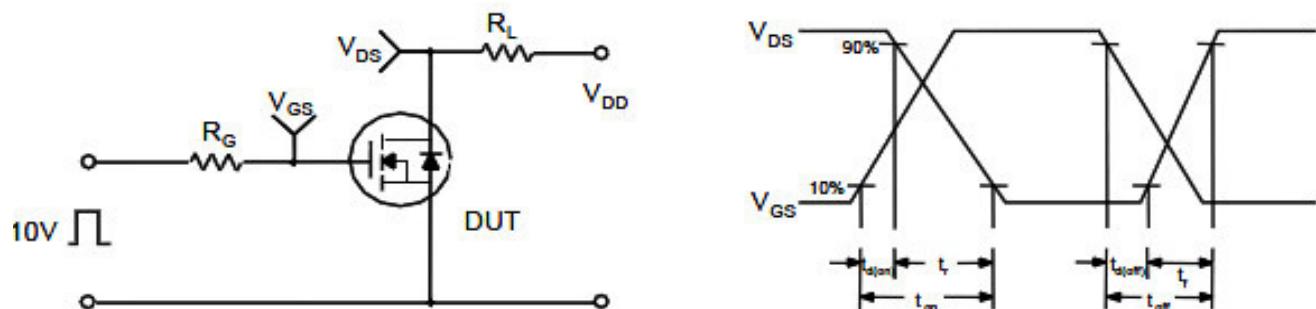
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■ Test circuit and waveform

Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms

